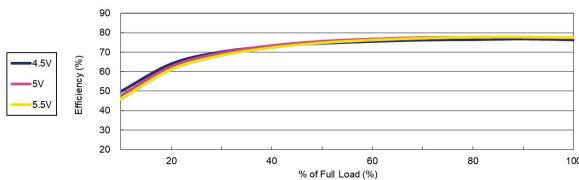


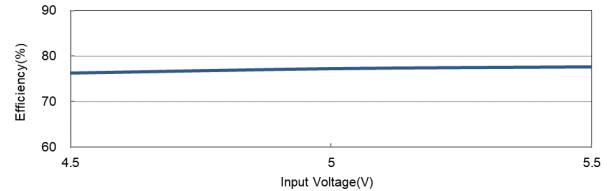
Characteristic Curves

TSM 0505S

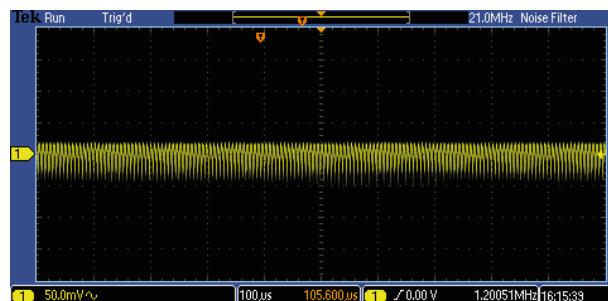
Efficiency versus Output Load



Efficiency versus Input Voltage



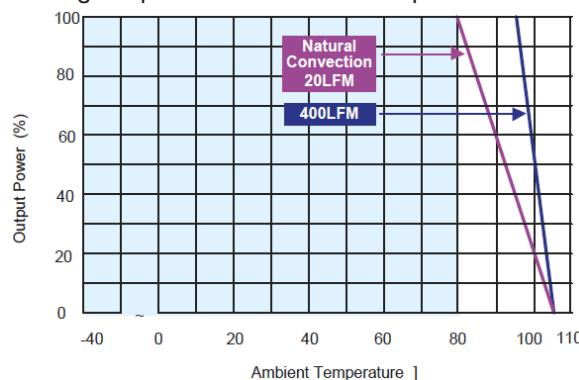
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



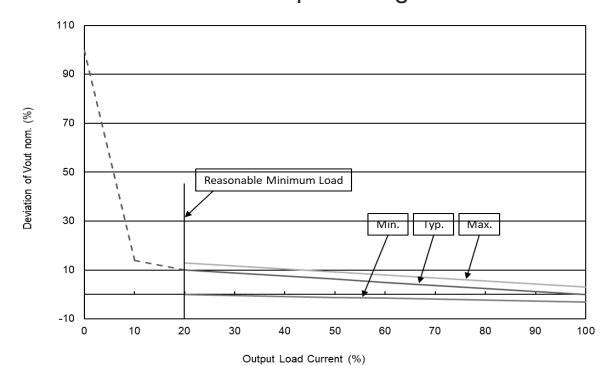
Typical Input Start-Up and Output Rise Characteristic



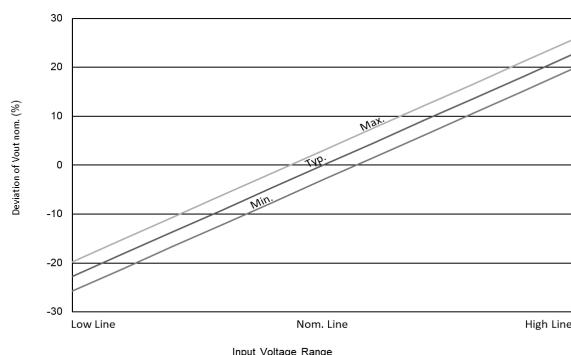
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

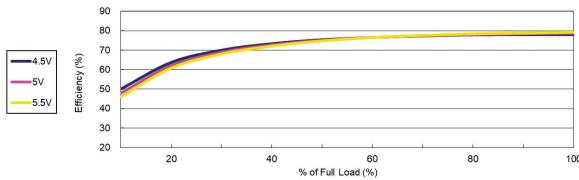


Input Variation versus Output Voltage

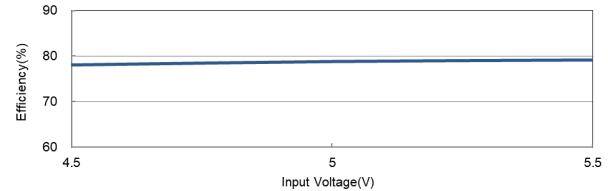


TSM 0509S

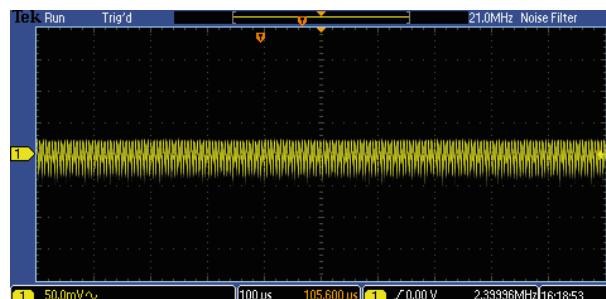
Efficiency versus Output Load



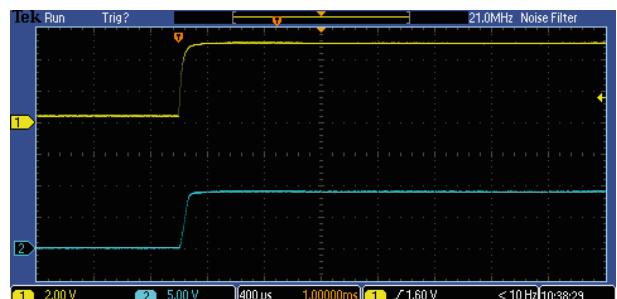
Efficiency versus Input Voltage



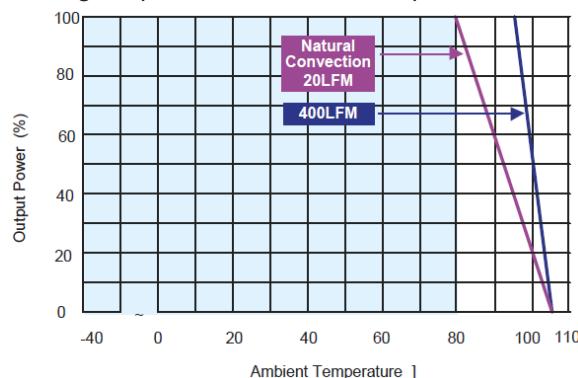
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



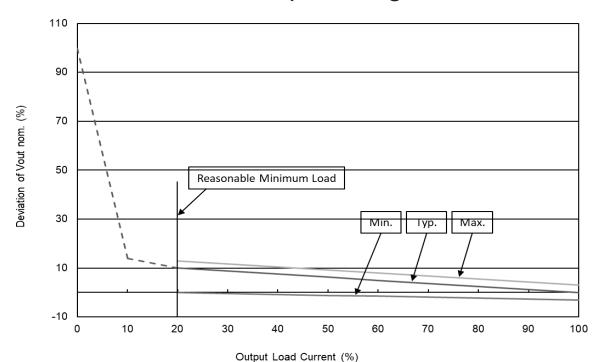
Typical Input Start-Up and Output Rise Characteristic



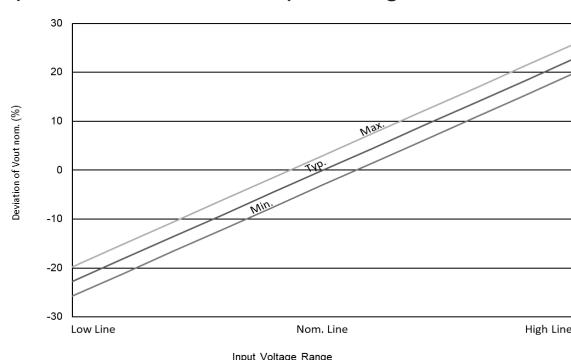
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

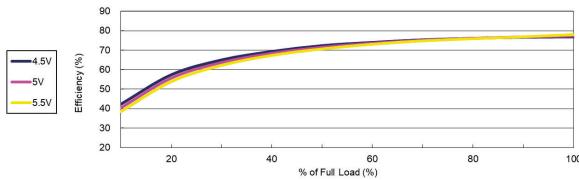


Input Variation versus Output Voltage

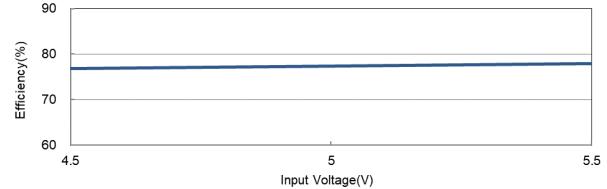


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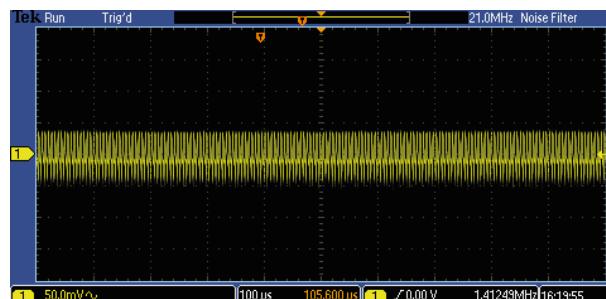
Efficiency versus Output Load



Efficiency versus Input Voltage



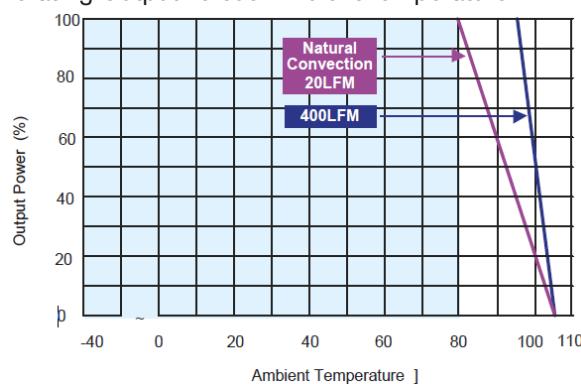
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



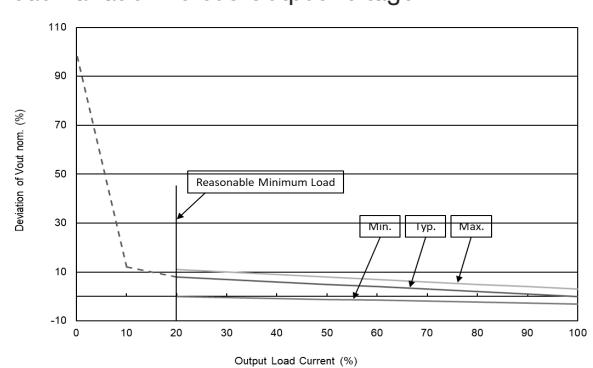
Typical Input Start-Up and Output Rise Characteristic



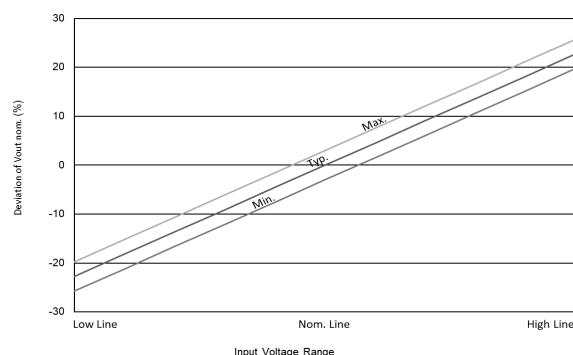
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

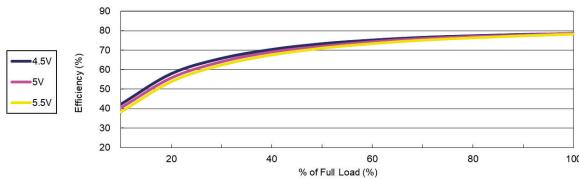


Input Variation versus Output Voltage

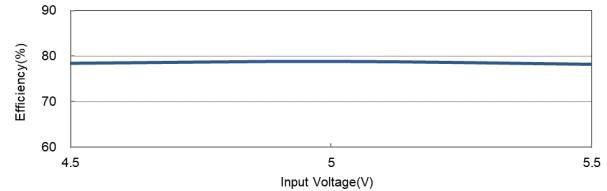


TSM 0515S

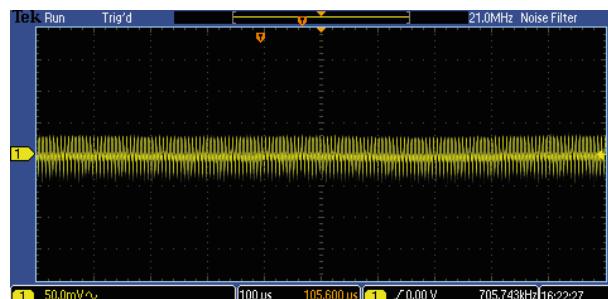
Efficiency versus Output Load



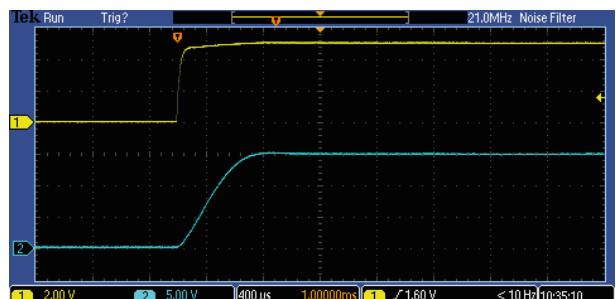
Efficiency versus Input Voltage



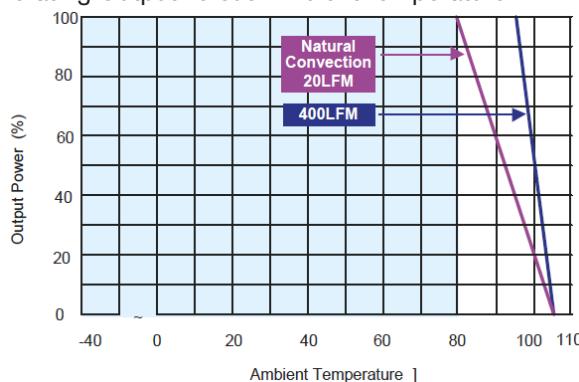
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



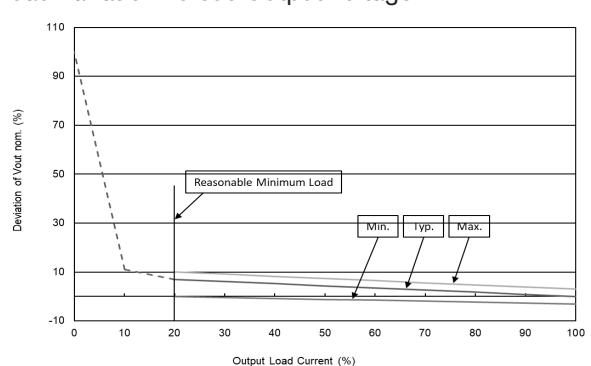
Typical Input Start-Up and Output Rise Characteristic



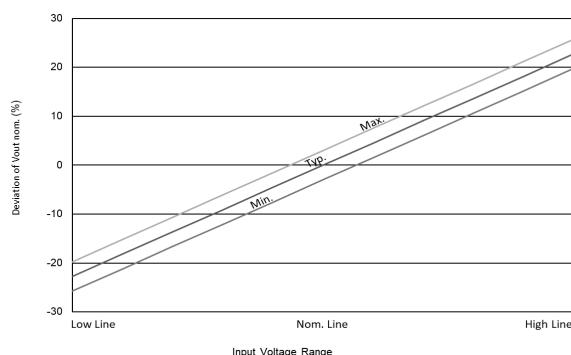
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

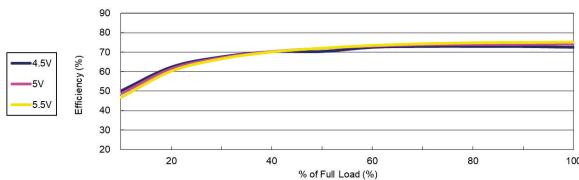


Input Variation versus Output Voltage

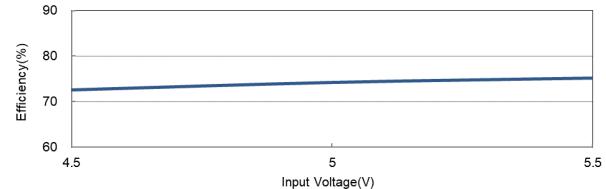


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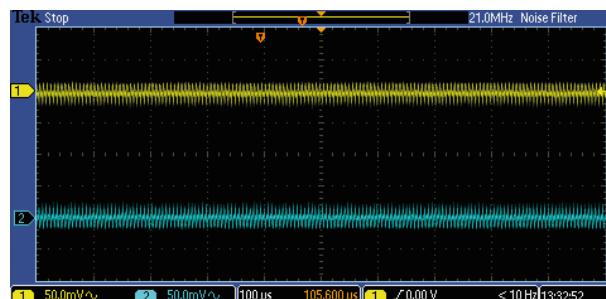
Efficiency versus Output Load



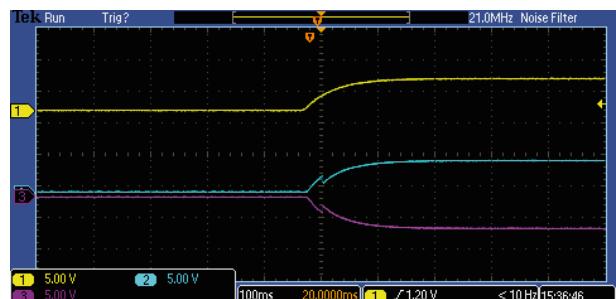
Efficiency versus Input Voltage



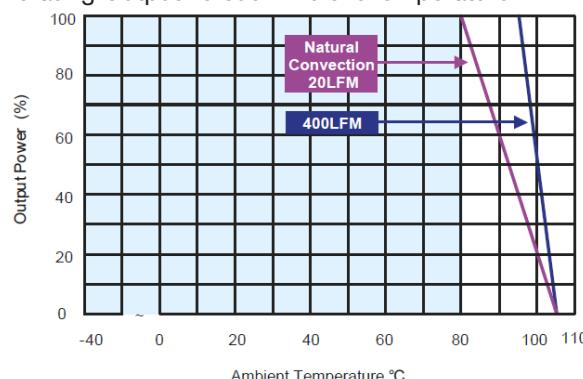
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



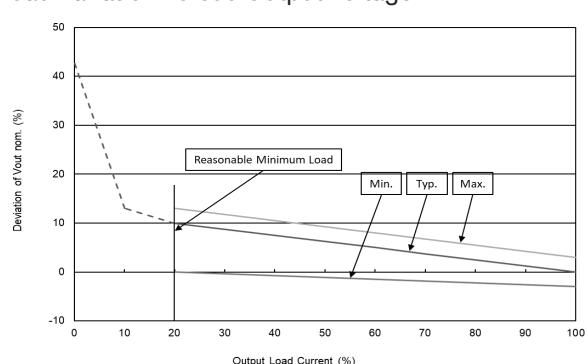
Typical Input Start-Up and Output Rise Characteristic



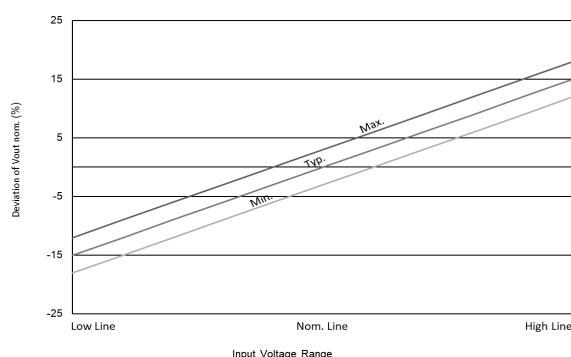
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

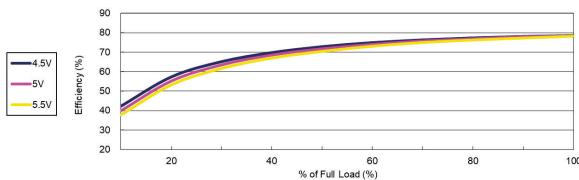


Input Variation versus Output Voltage

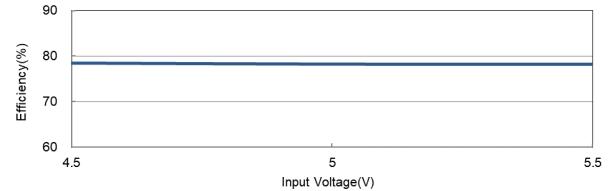


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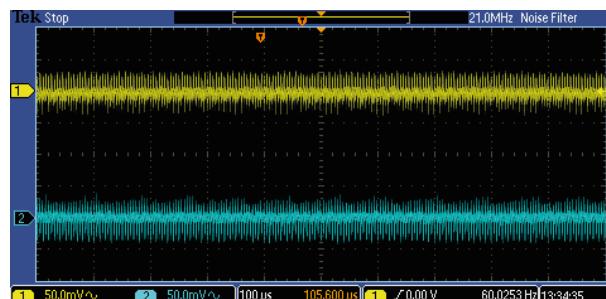
Efficiency versus Output Load



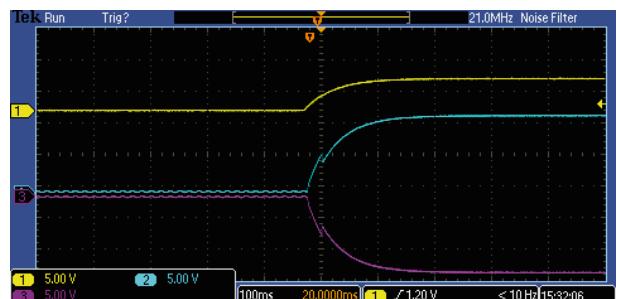
Efficiency versus Input Voltage



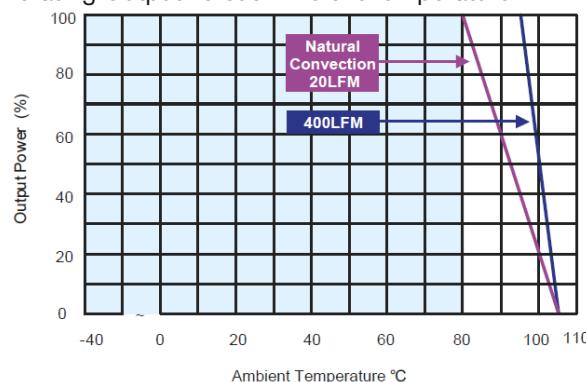
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



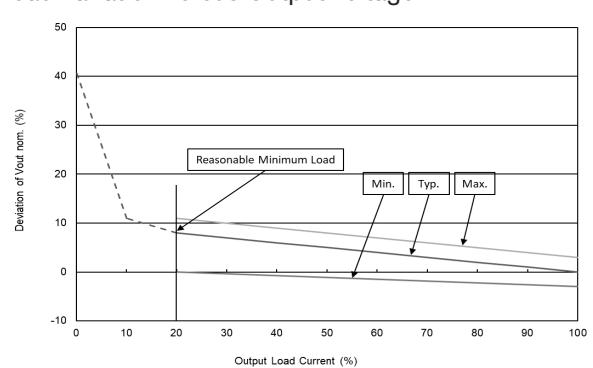
Typical Input Start-Up and Output Rise Characteristic



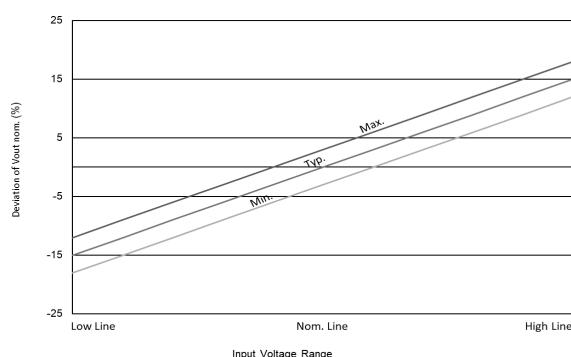
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

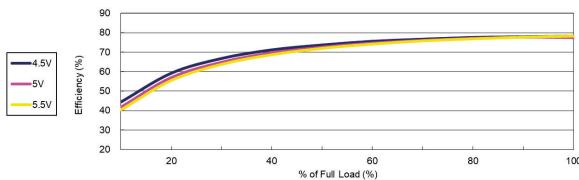


Input Variation versus Output Voltage

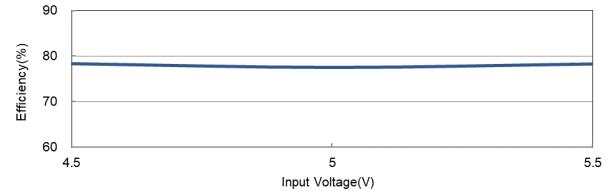


TSM 0515D

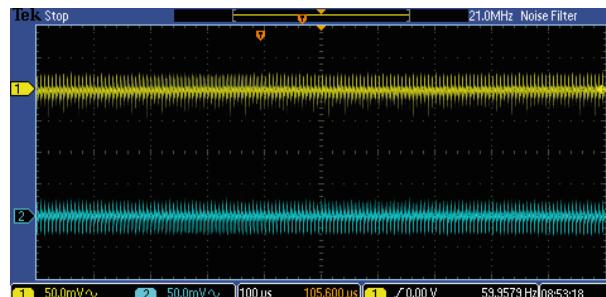
Efficiency versus Output Load



Efficiency versus Input Voltage



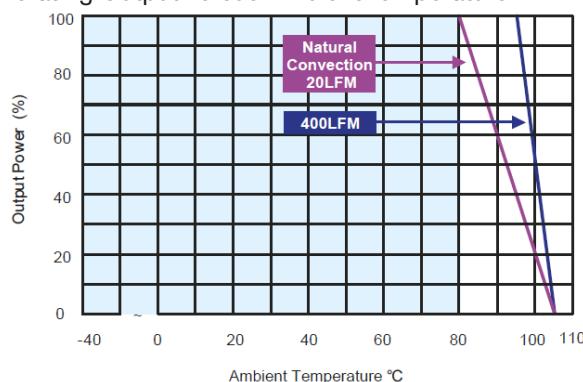
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



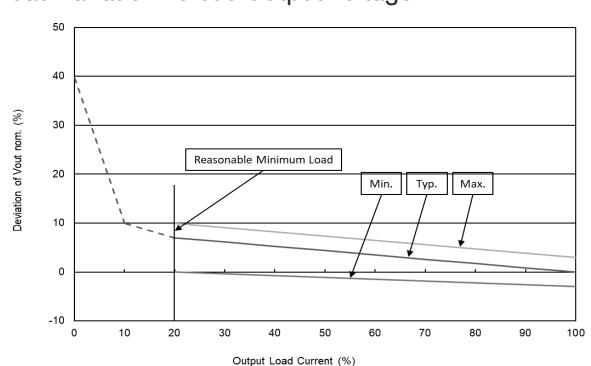
Typical Input Start-Up and Output Rise Characteristic



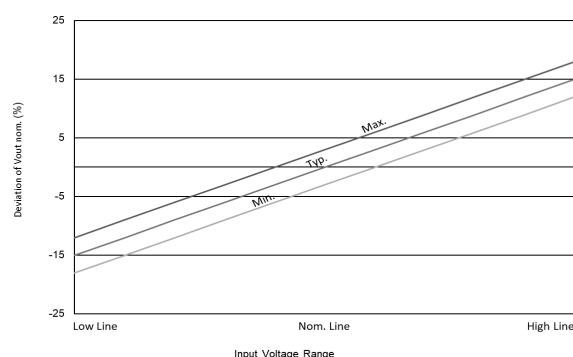
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

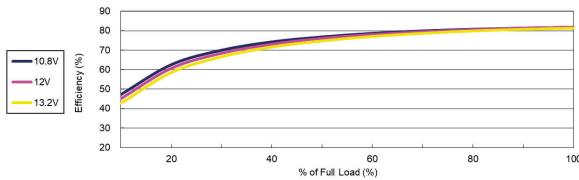


Input Variation versus Output Voltage

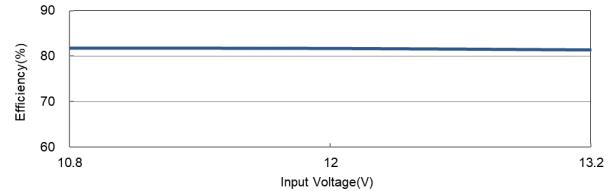


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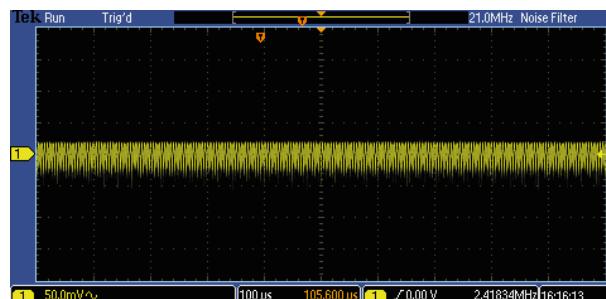
Efficiency versus Output Load



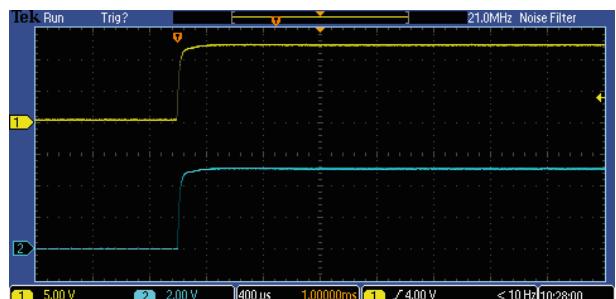
Efficiency versus Input Voltage



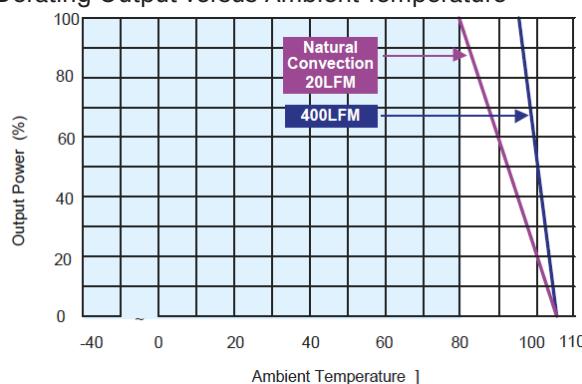
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



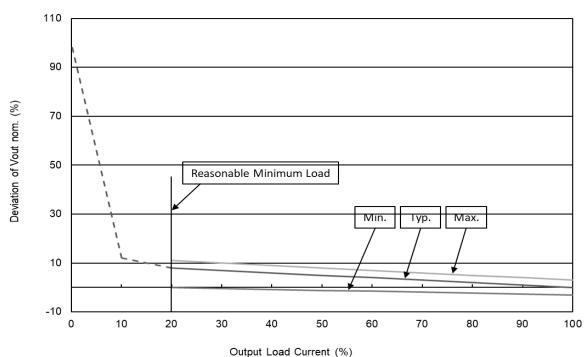
Typical Input Start-Up and Output Rise Characteristic



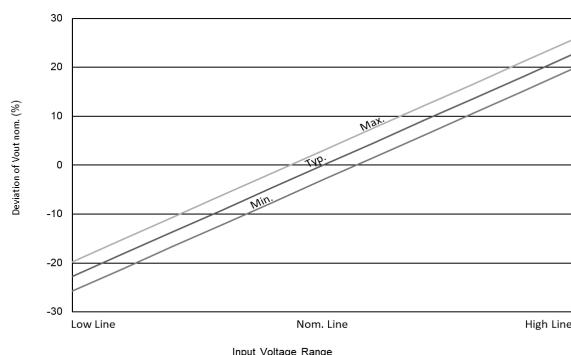
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

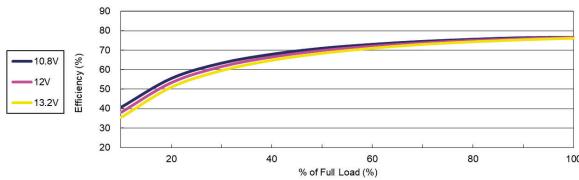


Input Variation versus Output Voltage

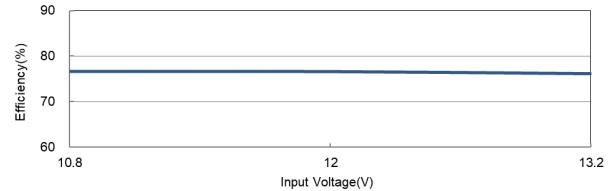


TSM 1209S

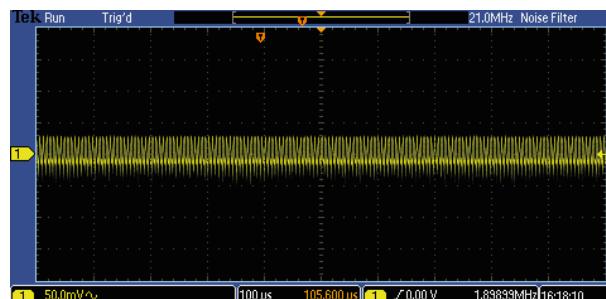
Efficiency versus Output Load



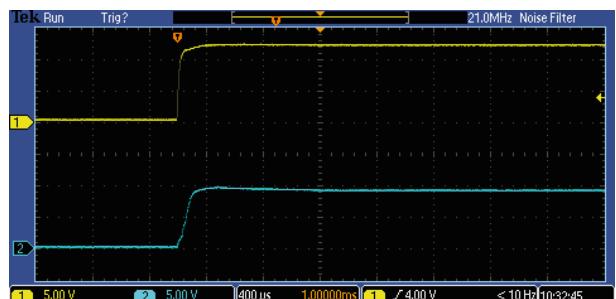
Efficiency versus Input Voltage



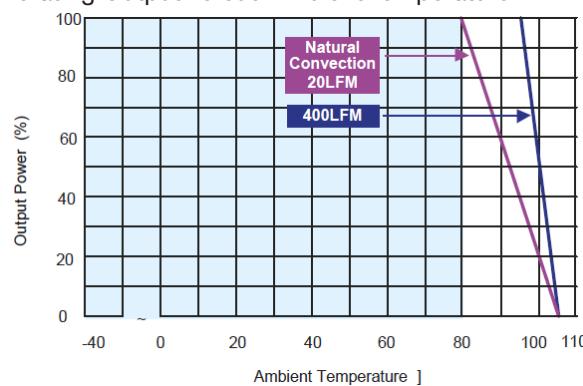
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



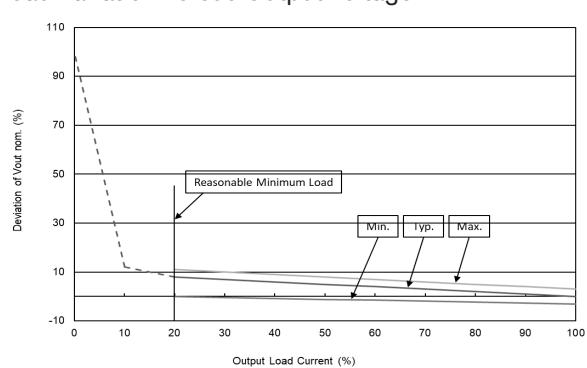
Typical Input Start-Up and Output Rise Characteristic



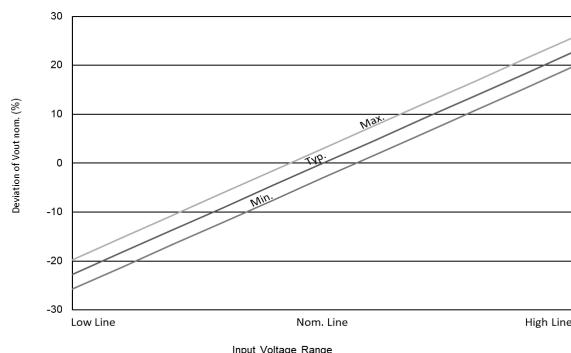
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

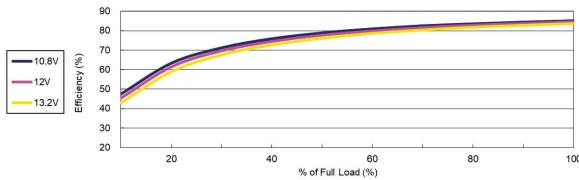


Input Variation versus Output Voltage

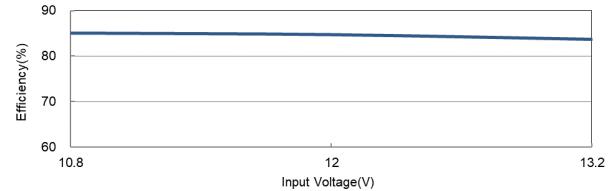


TSM 1212S

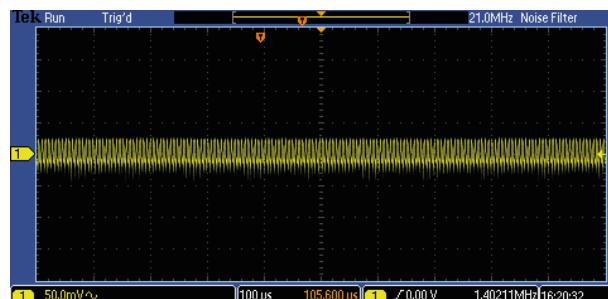
Efficiency versus Output Load



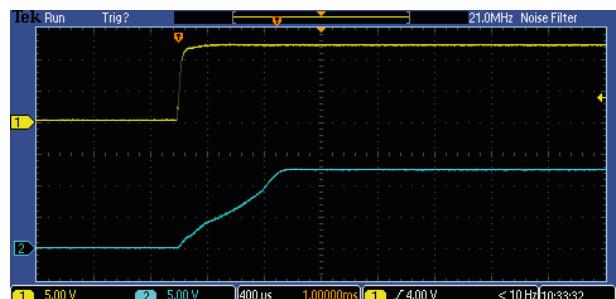
Efficiency versus Input Voltage



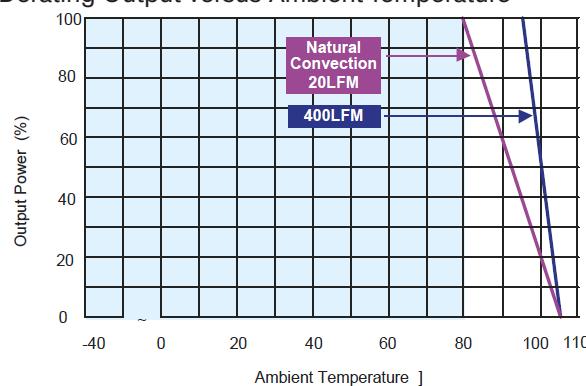
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



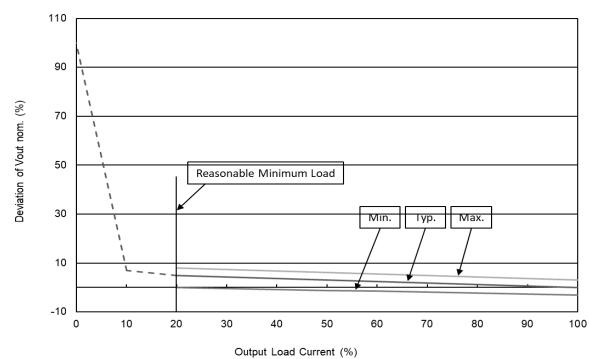
Typical Input Start-Up and Output Rise Characteristic



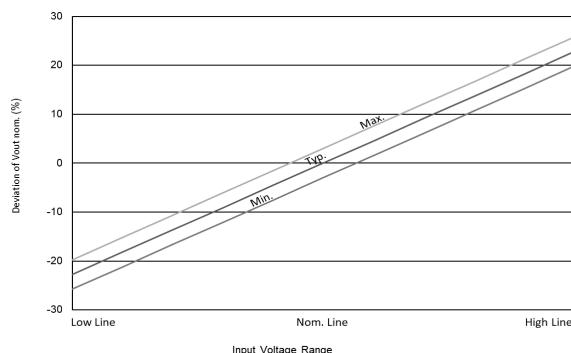
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

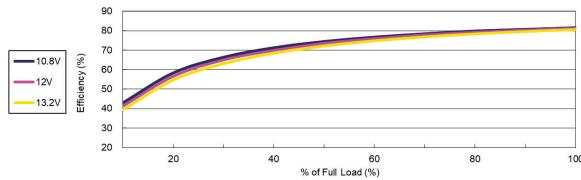


Input Variation versus Output Voltage

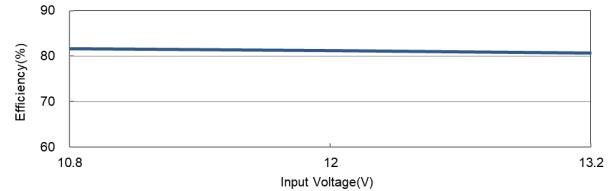


TSM 1215S

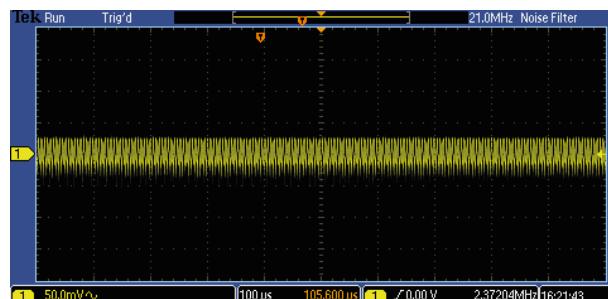
Efficiency versus Output Load



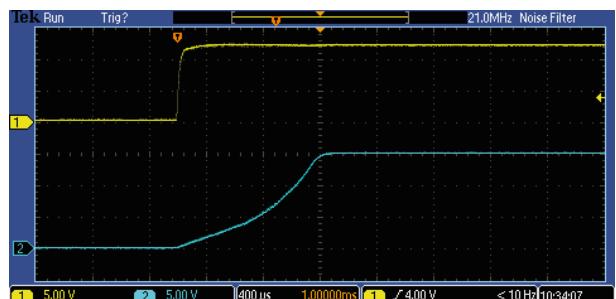
Efficiency versus Input Voltage



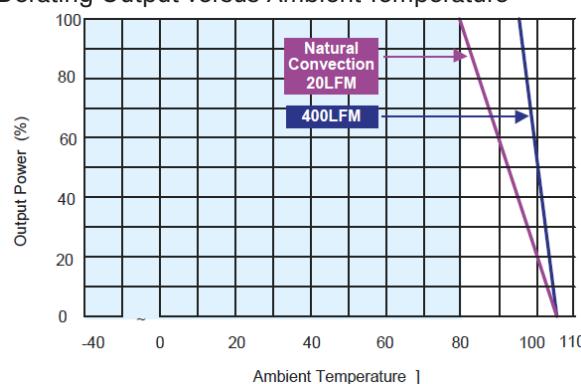
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



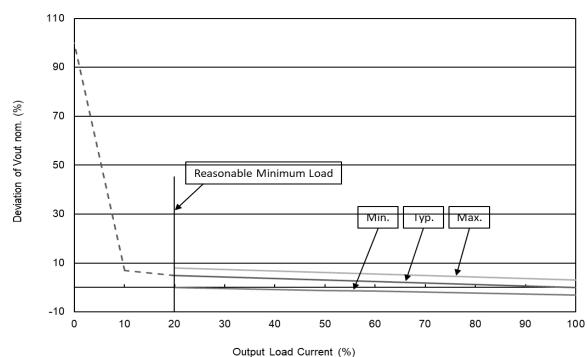
Typical Input Start-Up and Output Rise Characteristic



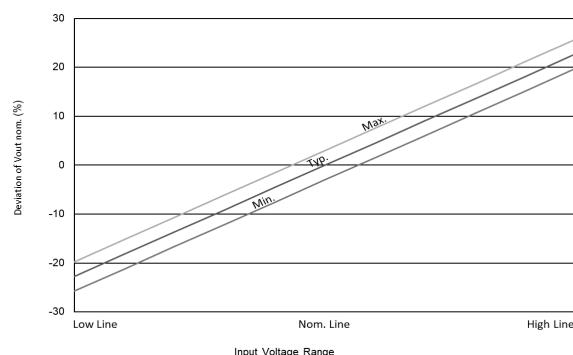
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

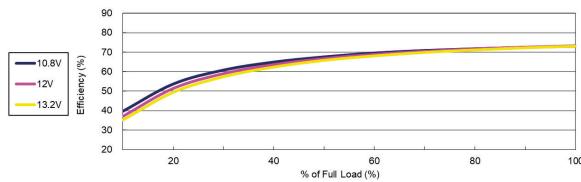


Input Variation versus Output Voltage

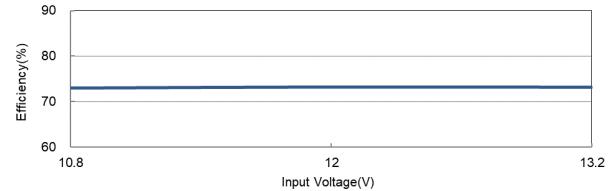


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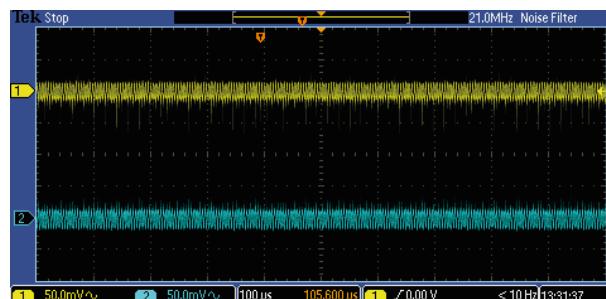
Efficiency versus Output Load



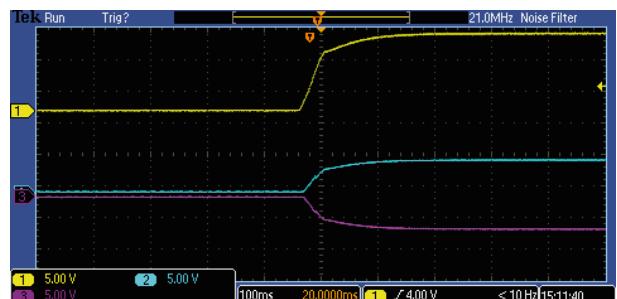
Efficiency versus Input Voltage



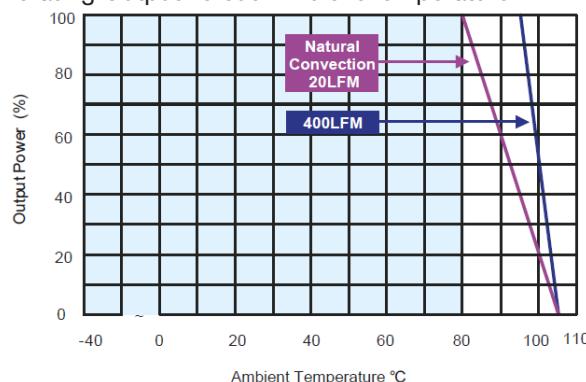
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



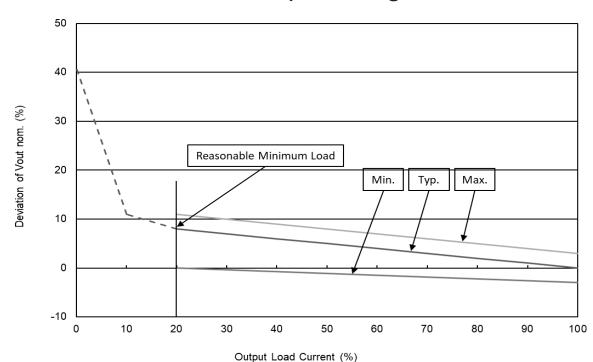
Typical Input Start-Up and Output Rise Characteristic



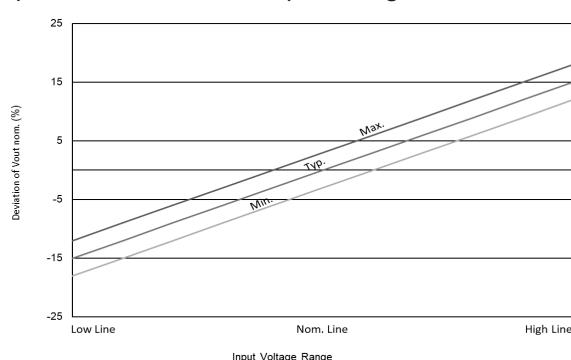
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

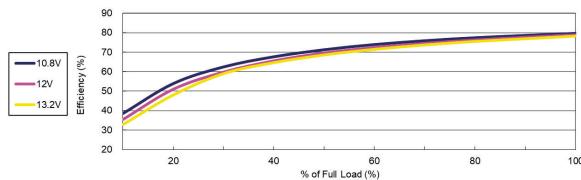


Input Variation versus Output Voltage

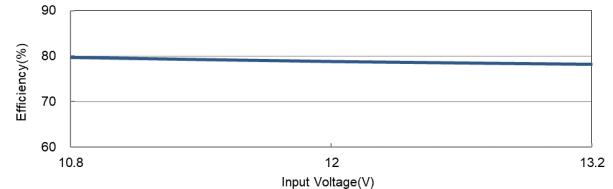


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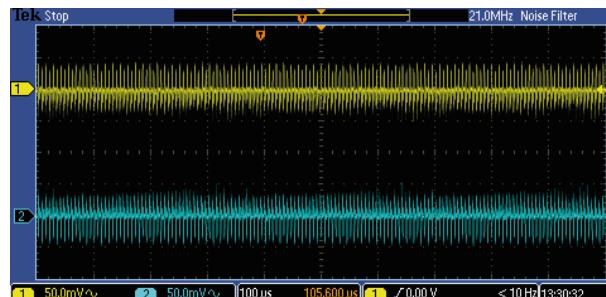
Efficiency versus Output Load



Efficiency versus Input Voltage



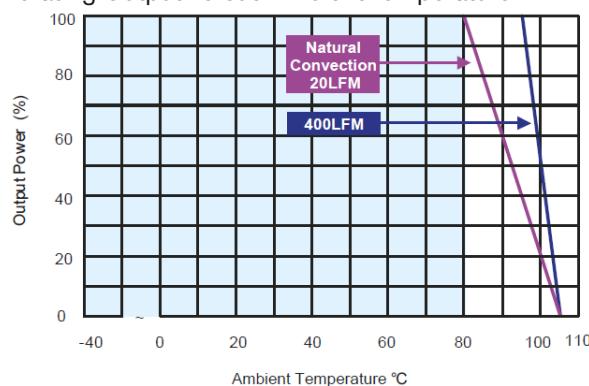
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



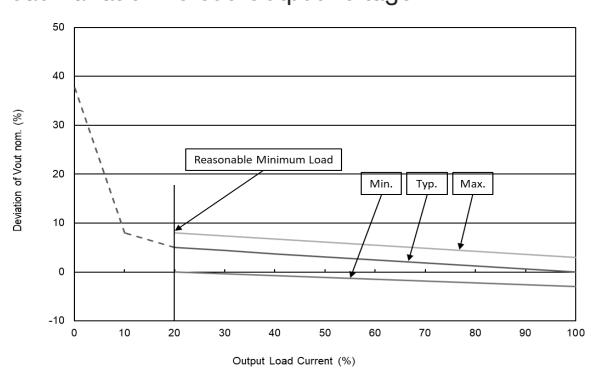
Typical Input Start-Up and Output Rise Characteristic



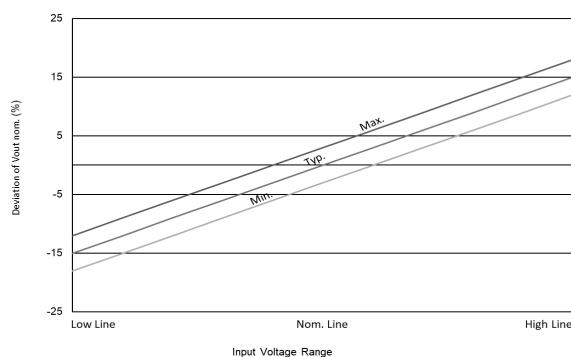
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

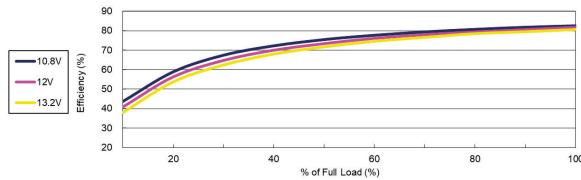


Input Variation versus Output Voltage

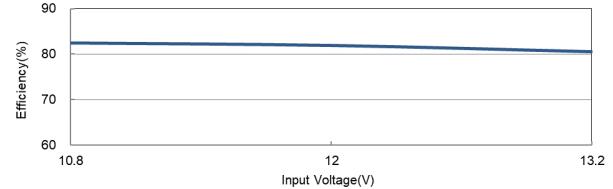


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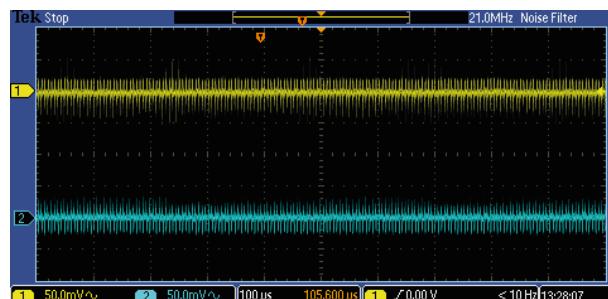
Efficiency versus Output Load



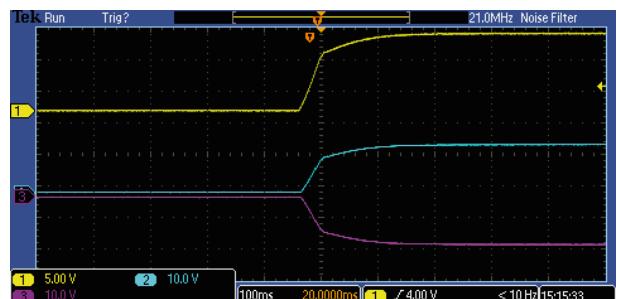
Efficiency versus Input Voltage



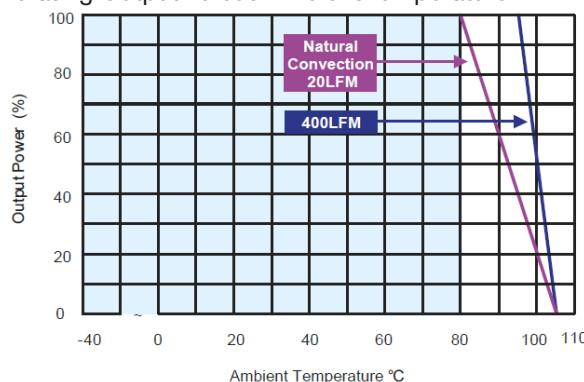
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



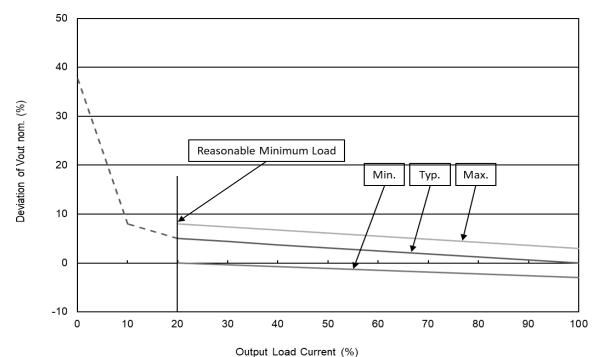
Typical Input Start-Up and Output Rise Characteristic



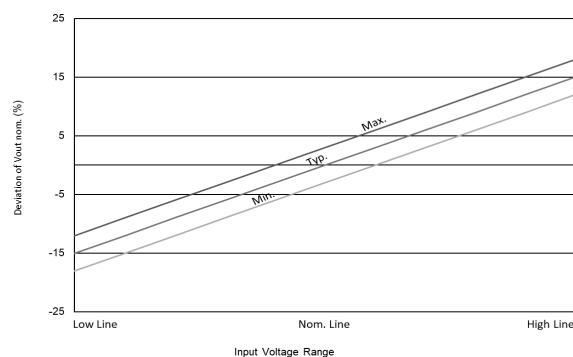
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

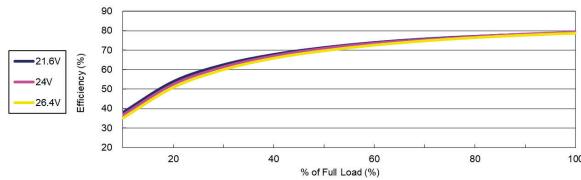


Input Variation versus Output Voltage

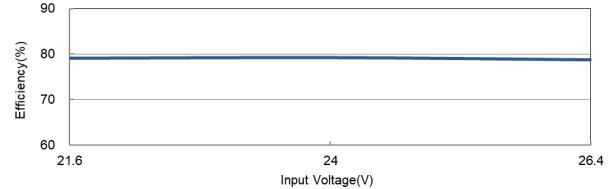


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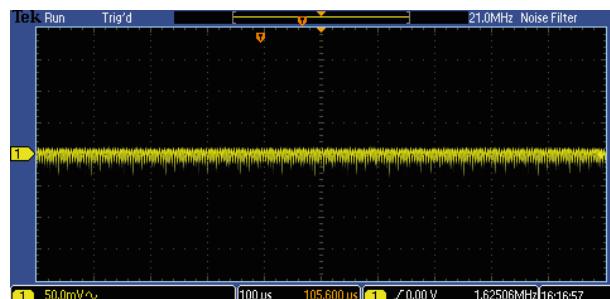
Efficiency versus Output Load



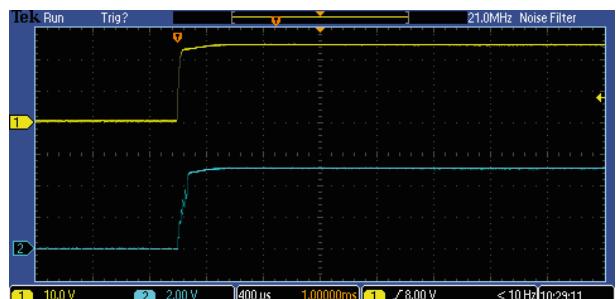
Efficiency versus Input Voltage



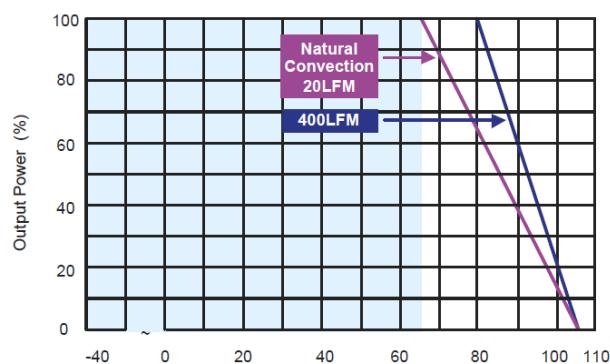
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



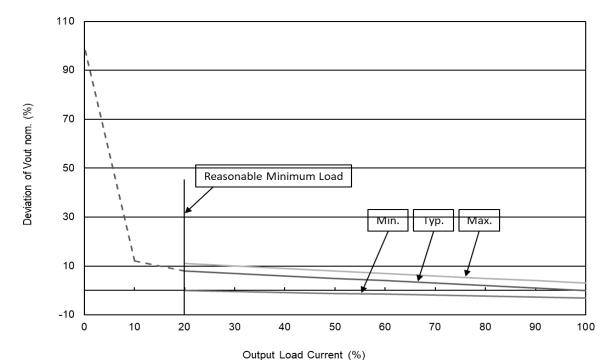
Typical Input Start-Up and Output Rise Characteristic



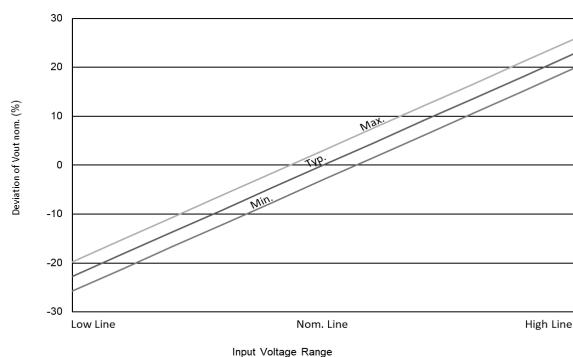
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

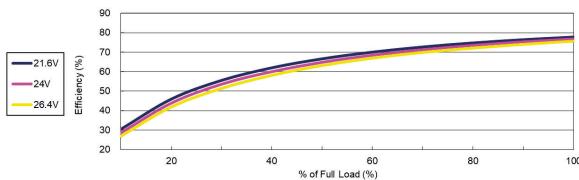


Input Variation versus Output Voltage

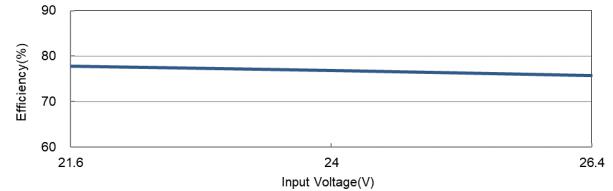


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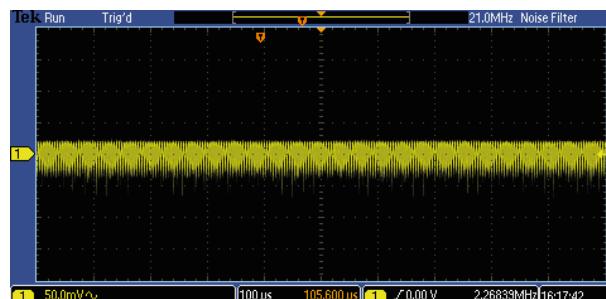
Efficiency versus Output Load



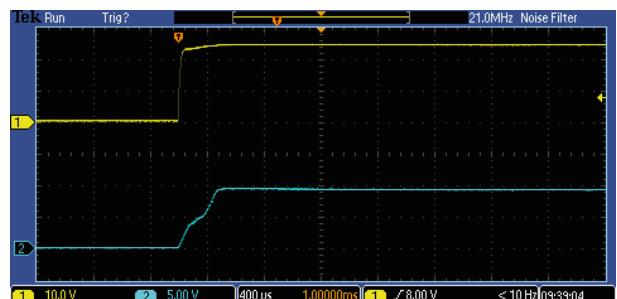
Efficiency versus Input Voltage



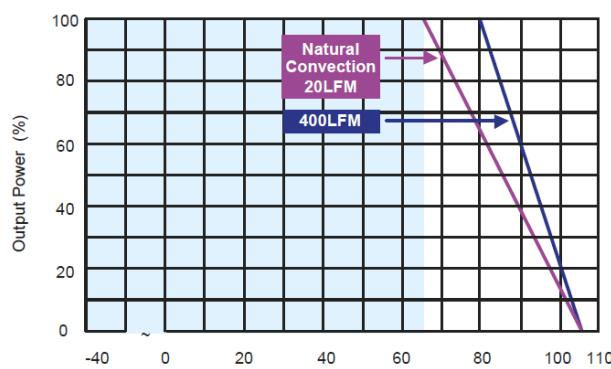
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



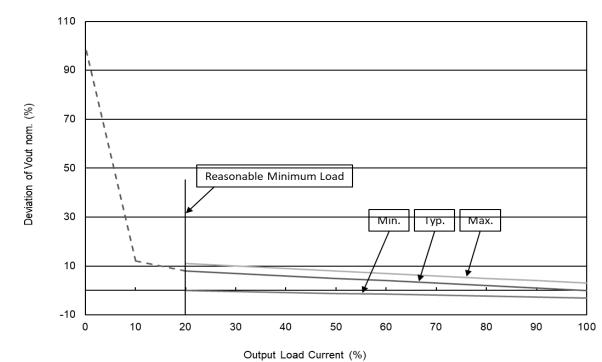
Typical Input Start-Up and Output Rise Characteristic



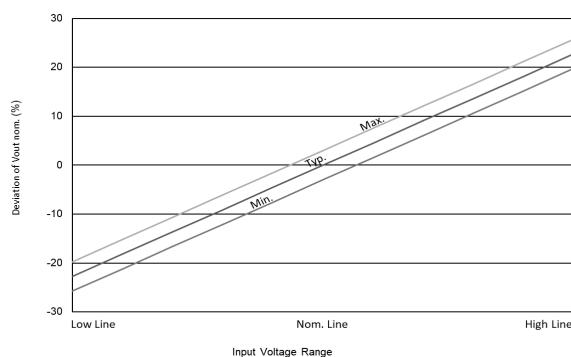
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

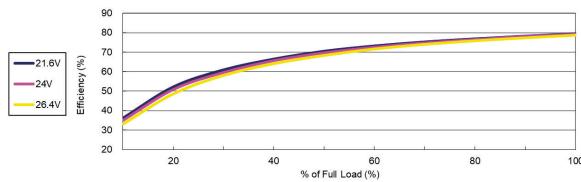


Input Variation versus Output Voltage

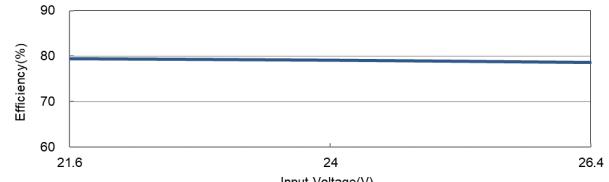


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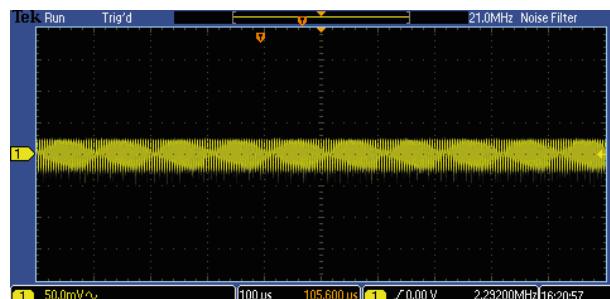
Efficiency versus Output Load



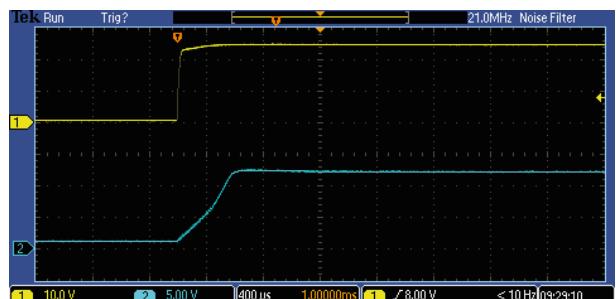
Efficiency versus Input Voltage



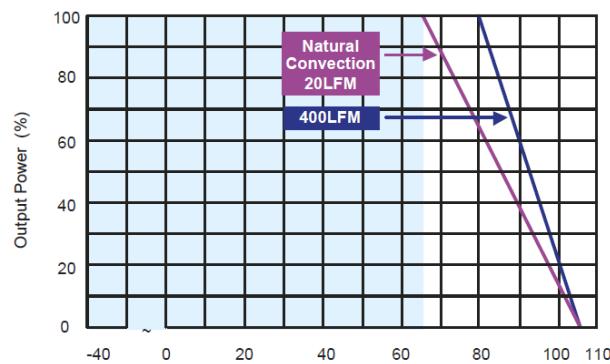
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



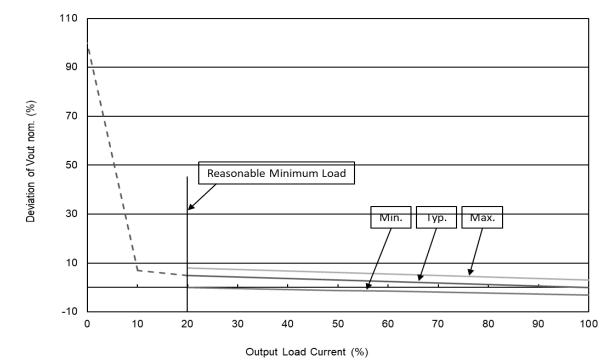
Typical Input Start-Up and Output Rise Characteristic



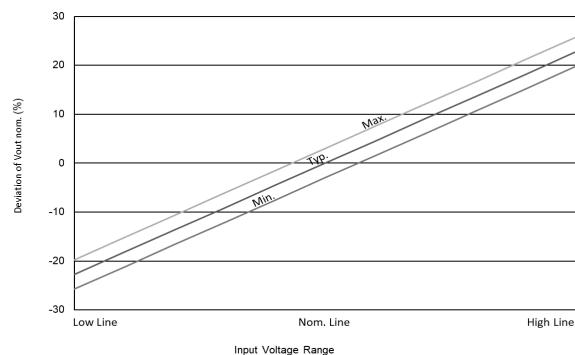
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

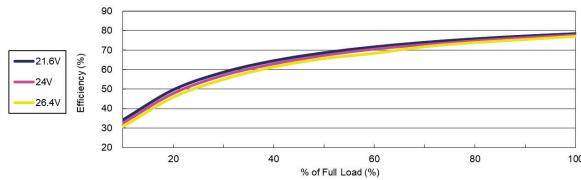


Input Variation versus Output Voltage

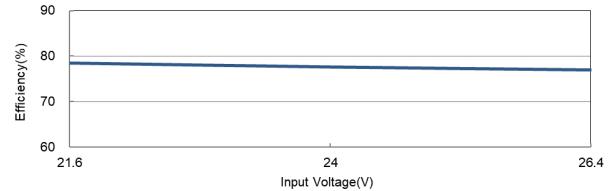


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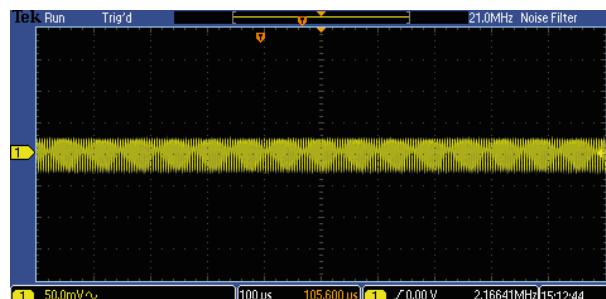
Efficiency versus Output Load



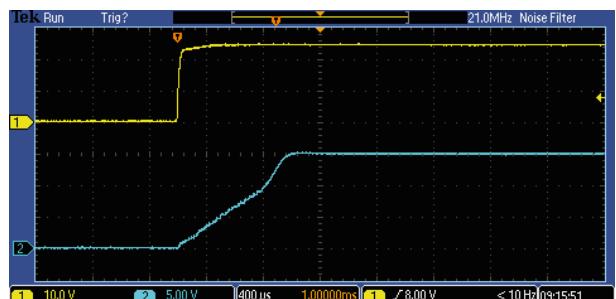
Efficiency versus Input Voltage



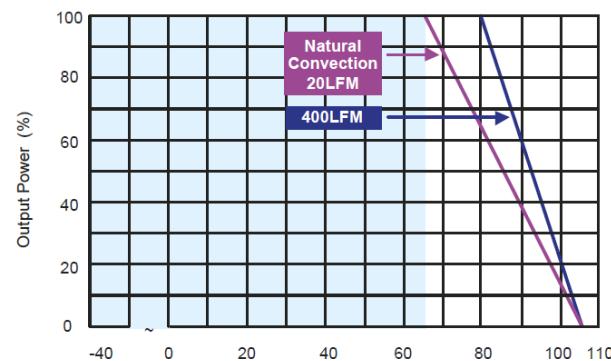
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



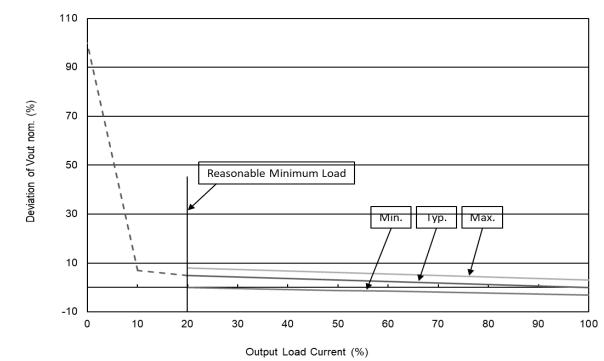
Typical Input Start-Up and Output Rise Characteristic



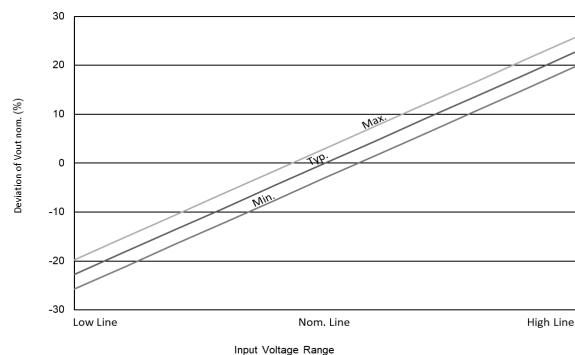
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

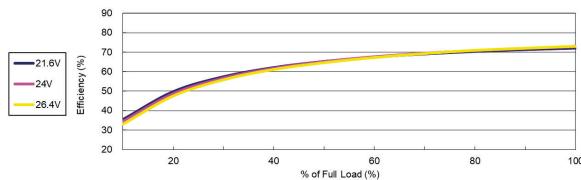


Input Variation versus Output Voltage

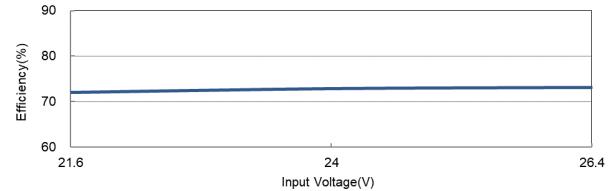


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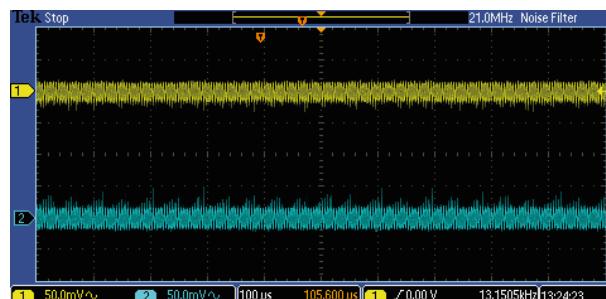
Efficiency versus Output Load



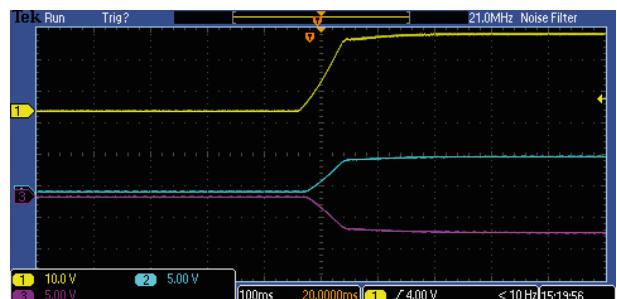
Efficiency versus Input Voltage



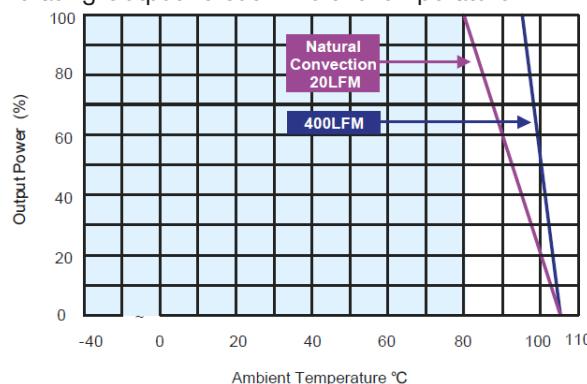
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



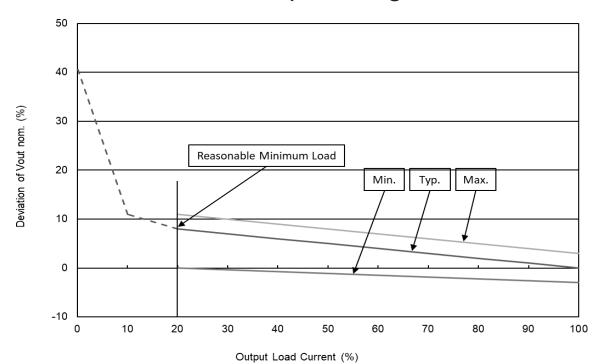
Typical Input Start-Up and Output Rise Characteristic



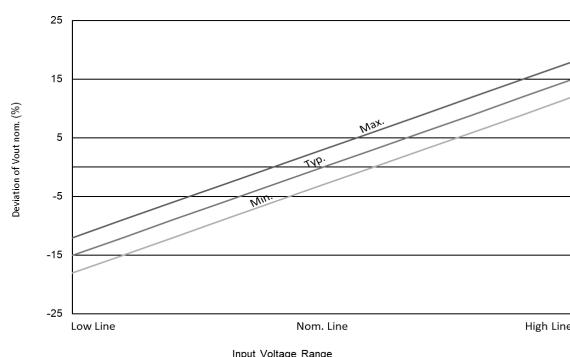
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

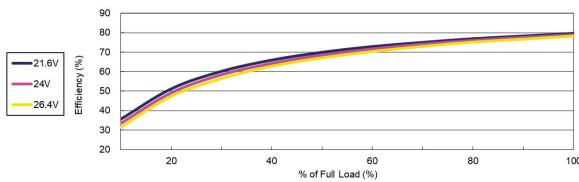


Input Variation versus Output Voltage

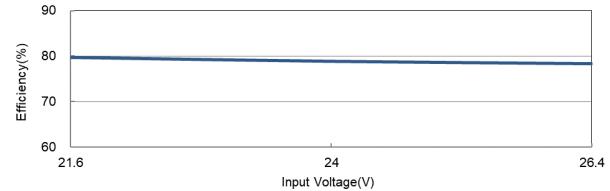


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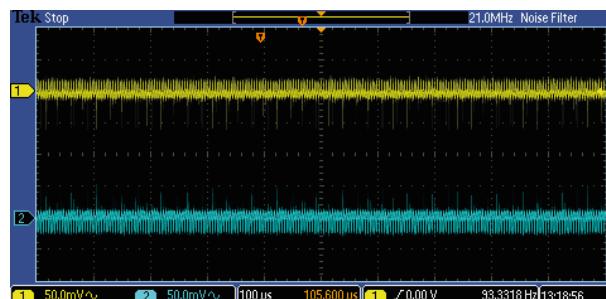
Efficiency versus Output Load



Efficiency versus Input Voltage



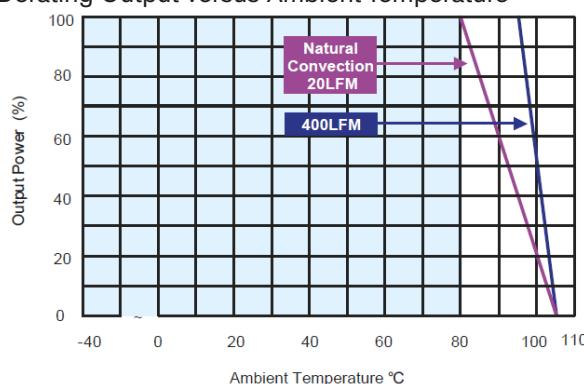
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



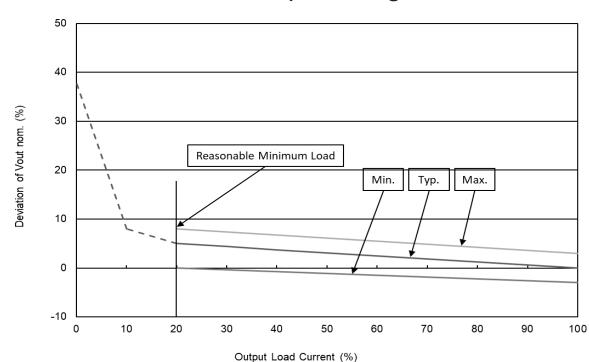
Typical Input Start-Up and Output Rise Characteristic



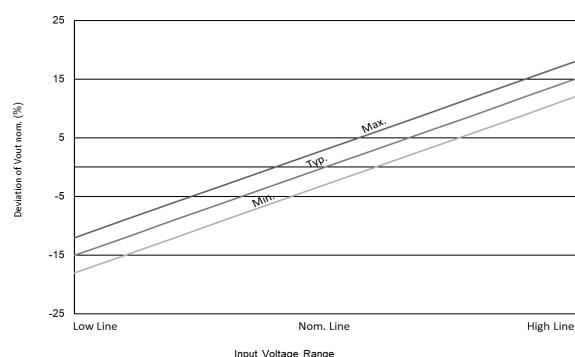
Derating Output versus Ambient Temperature



Load Variation versus Output Voltage

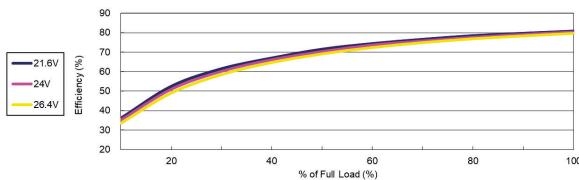


Input Variation versus Output Voltage

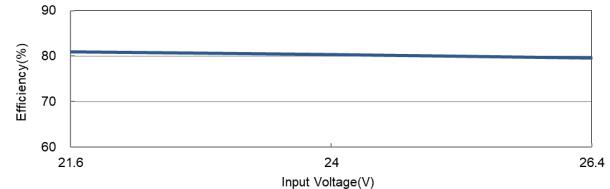


TSM 2415D

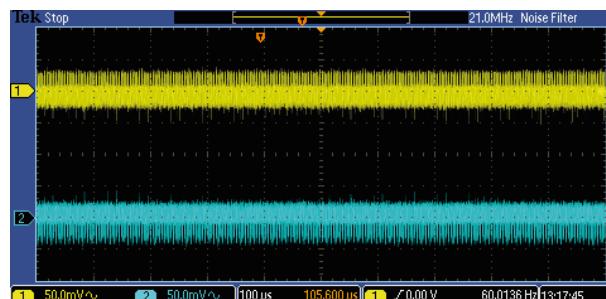
Efficiency versus Output Load



Efficiency versus Input Voltage



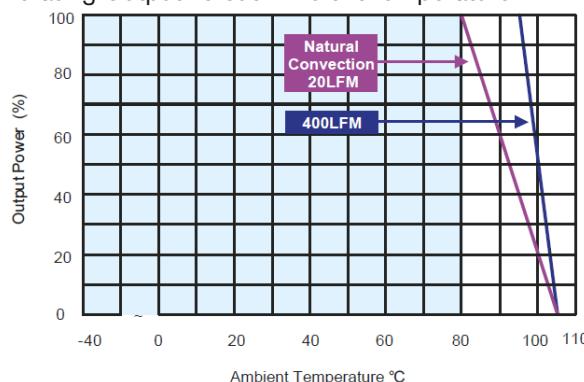
Typical Output Ripple and Noise
(with external capacitor; see datasheet)



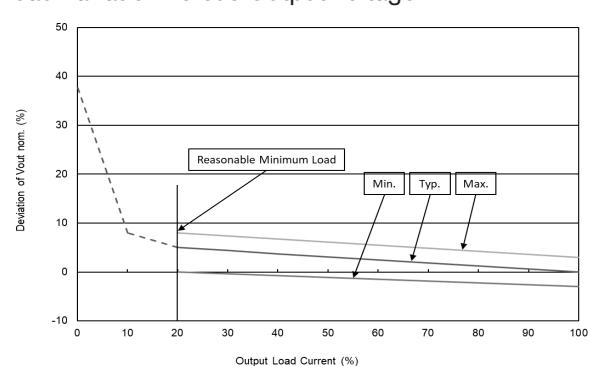
Typical Input Start-Up and Output Rise Characteristic



Derating Output versus Ambient Temperature



Load Variation versus Output Voltage



Input Variation versus Output Voltage

