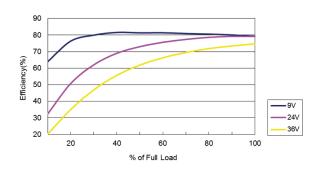
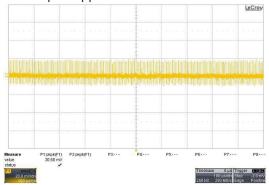
### **Characteristic Curves**

#### **TEN 3-2410WIN**

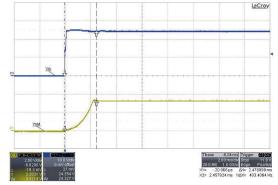
Efficiency vs Output Load



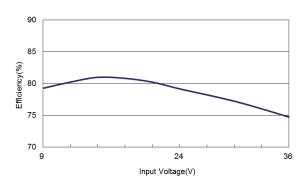
Typical Output Ripple and Noise



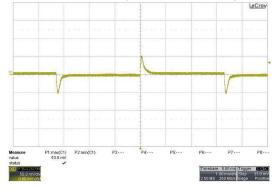
Typical Input Start-Up and Output Rise Characteristic

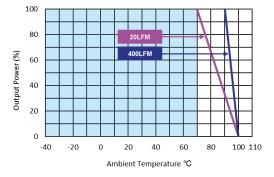


Efficiency vs Input Voltage



Transient Response to Dynamic Load Change (25%)

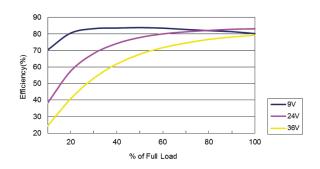




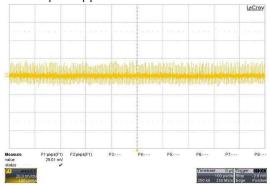
### **TEN 3WIN Series**

#### **TEN 3-2411WIN**

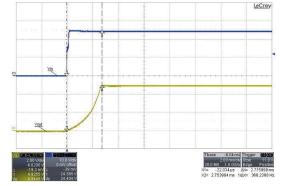
Efficiency vs Output Load



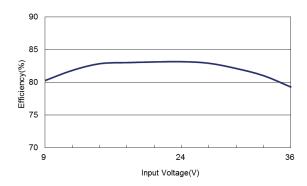
Typical Output Ripple and Noise



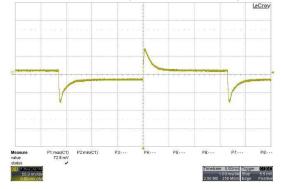
Typical Input Start-Up and Output Rise Characteristic

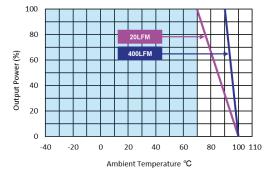


Efficiency vs Input Voltage



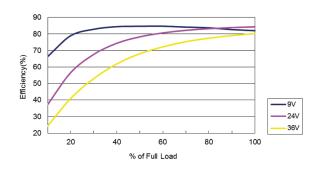
Transient Response to Dynamic Load Change (25%)



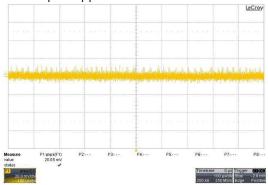


#### **TEN 3-2412WIN**

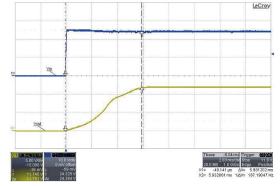
Efficiency vs Output Load



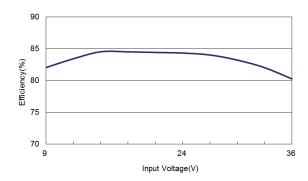
Typical Output Ripple and Noise



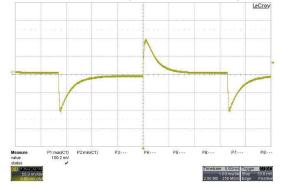
Typical Input Start-Up and Output Rise Characteristic

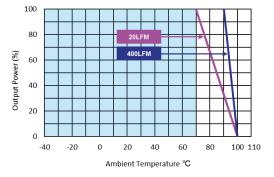


Efficiency vs Input Voltage



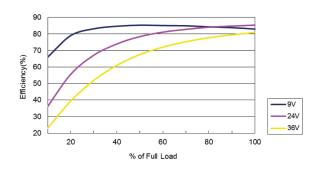
Transient Response to Dynamic Load Change (25%)



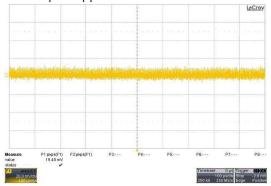


#### **TEN 3-2413WIN**

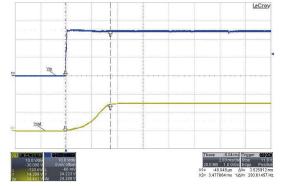
Efficiency vs Output Load



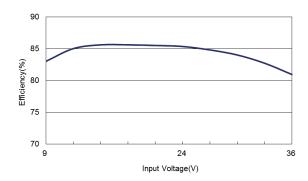
Typical Output Ripple and Noise



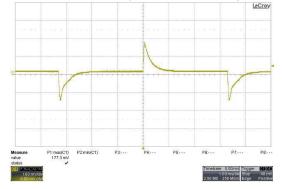
Typical Input Start-Up and Output Rise Characteristic

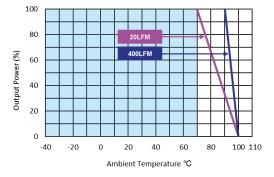


Efficiency vs Input Voltage



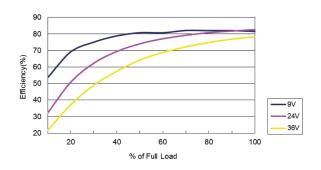
Transient Response to Dynamic Load Change (25%)



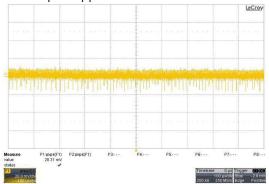


#### **TEN 3-2415WIN**

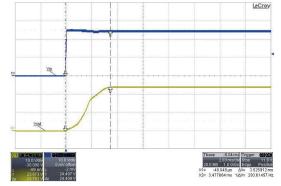
Efficiency vs Output Load



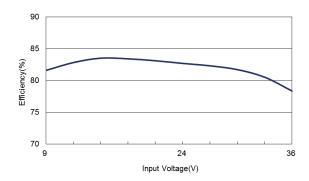
Typical Output Ripple and Noise



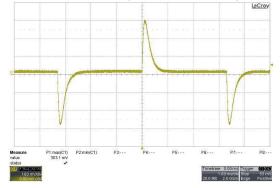
Typical Input Start-Up and Output Rise Characteristic

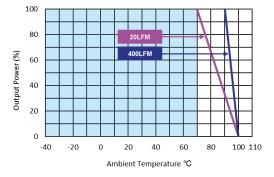


Efficiency vs Input Voltage



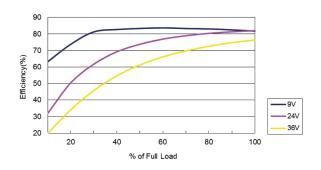
Transient Response to Dynamic Load Change (25%)



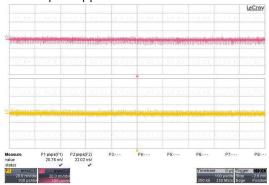


#### **TEN 3-2421WIN**

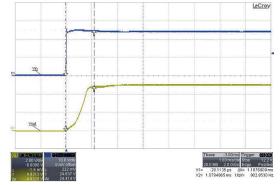
Efficiency vs Output Load



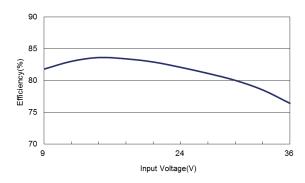
Typical Output Ripple and Noise



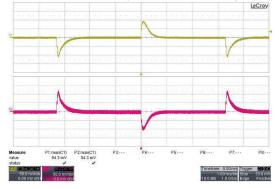
Typical Input Start-Up and Output Rise Characteristic

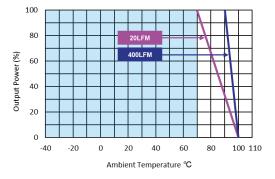


Efficiency vs Input Voltage



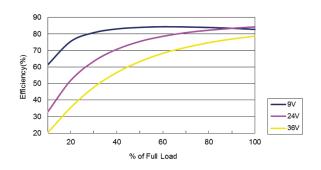
Transient Response to Dynamic Load Change (25%)



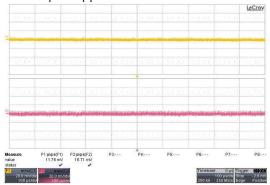


#### **TEN 3-2422WIN**

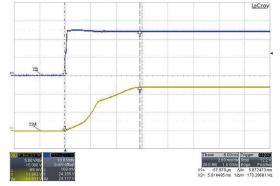
Efficiency vs Output Load



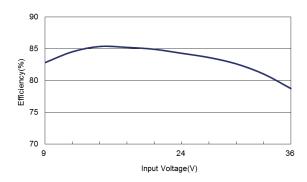
Typical Output Ripple and Noise



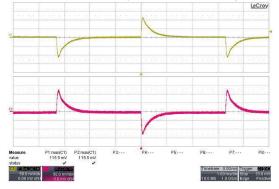
Typical Input Start-Up and Output Rise Characteristic

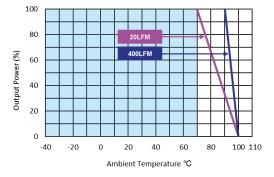


Efficiency vs Input Voltage



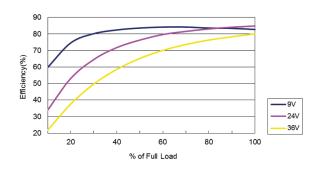
Transient Response to Dynamic Load Change (25%)



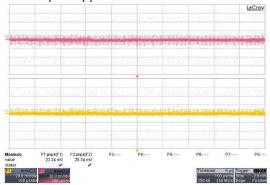


#### **TEN 3-2423WIN**

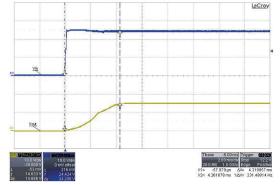
Efficiency vs Output Load



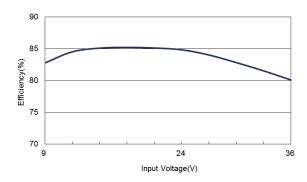
Typical Output Ripple and Noise



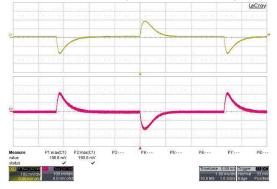
Typical Input Start-Up and Output Rise Characteristic

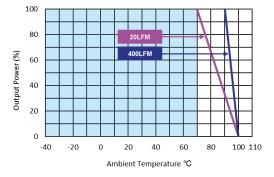


Efficiency vs Input Voltage



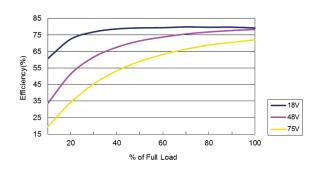
Transient Response to Dynamic Load Change (25%)



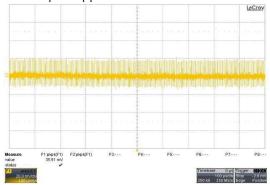


#### **TEN 3-4810WIN**

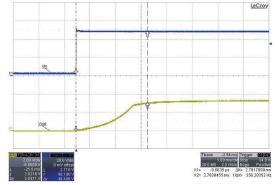
Efficiency vs Output Load



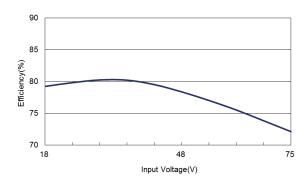
Typical Output Ripple and Noise



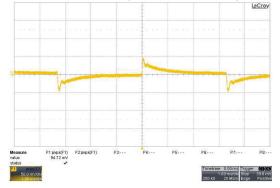
Typical Input Start-Up and Output Rise Characteristic

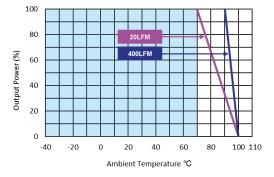


Efficiency vs Input Voltage



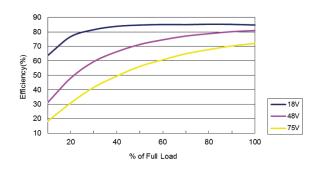
Transient Response to Dynamic Load Change (25%)



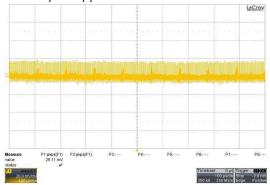


#### **TEN 3-4811WIN**

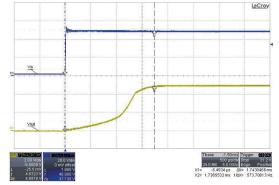
Efficiency vs Output Load



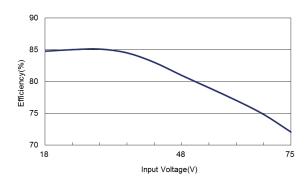
Typical Output Ripple and Noise



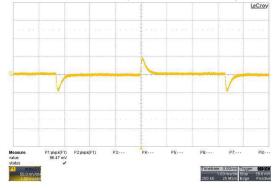
Typical Input Start-Up and Output Rise Characteristic

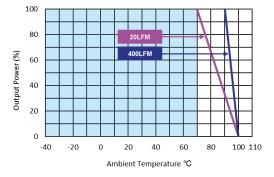


Efficiency vs Input Voltage



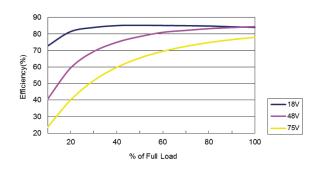
Transient Response to Dynamic Load Change (25%)



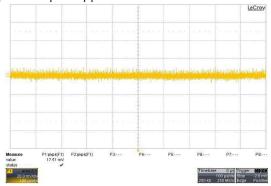


#### **TEN 3-4812WIN**

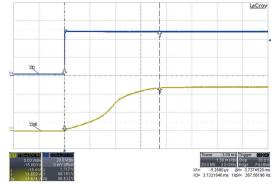
Efficiency vs Output Load



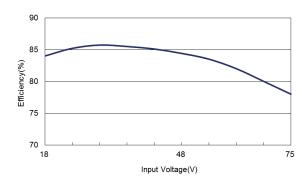
Typical Output Ripple and Noise



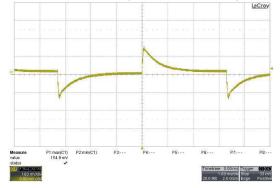
Typical Input Start-Up and Output Rise Characteristic

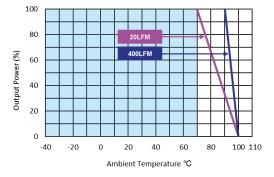


Efficiency vs Input Voltage



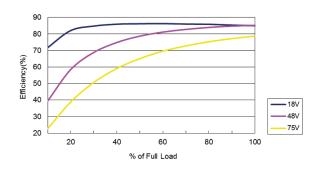
Transient Response to Dynamic Load Change (25%)



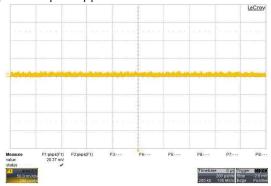


#### **TEN 3-4813WIN**

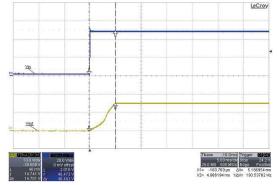
Efficiency vs Output Load



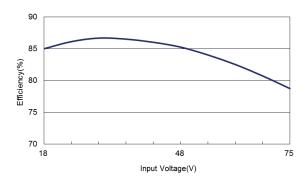
Typical Output Ripple and Noise



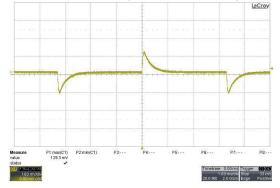
Typical Input Start-Up and Output Rise Characteristic

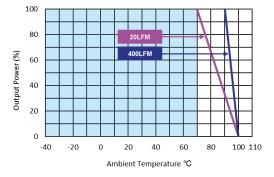


Efficiency vs Input Voltage



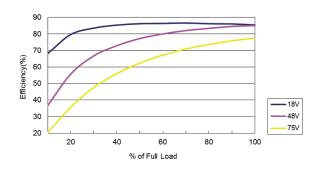
Transient Response to Dynamic Load Change (25%)



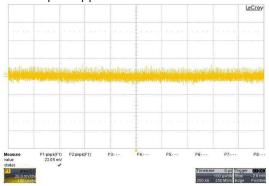


#### **TEN 3-4815WIN**

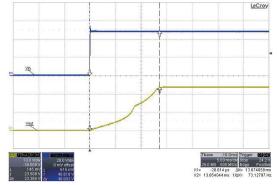
Efficiency vs Output Load



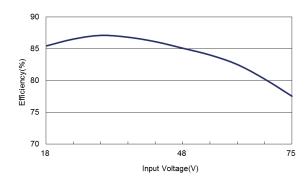
Typical Output Ripple and Noise



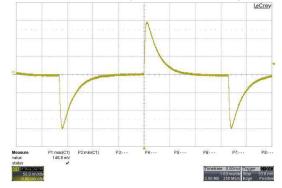
Typical Input Start-Up and Output Rise Characteristic

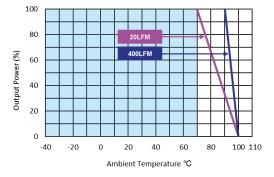


Efficiency vs Input Voltage



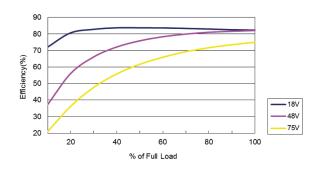
Transient Response to Dynamic Load Change (25%)



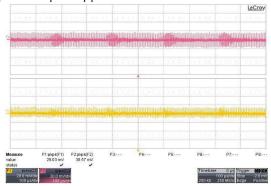


#### **TEN 3-4821WIN**

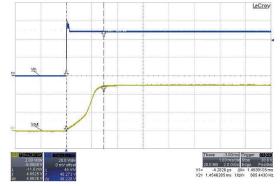
Efficiency vs Output Load



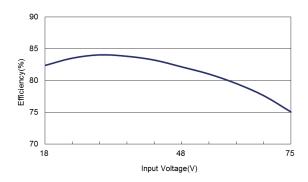
Typical Output Ripple and Noise



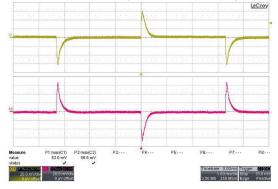
Typical Input Start-Up and Output Rise Characteristic

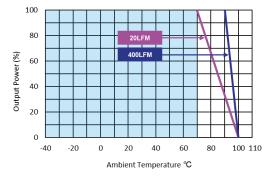


Efficiency vs Input Voltage



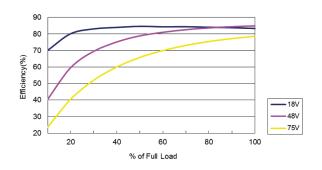
Transient Response to Dynamic Load Change (25%)



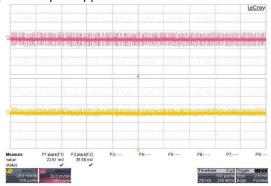


#### **TEN 3-4822WIN**

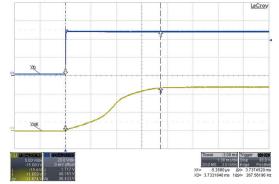
Efficiency vs Output Load



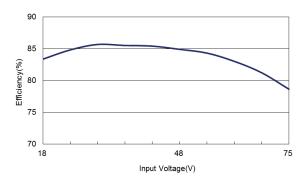
Typical Output Ripple and Noise



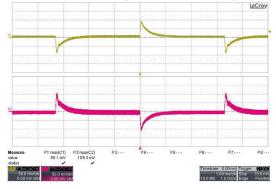
Typical Input Start-Up and Output Rise Characteristic

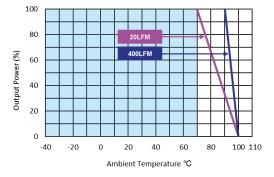


Efficiency vs Input Voltage



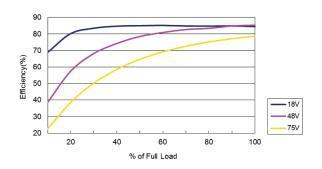
Transient Response to Dynamic Load Change (25%)



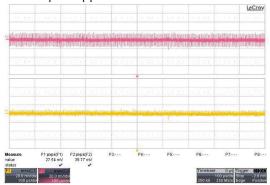


#### **TEN 3-4823WIN**

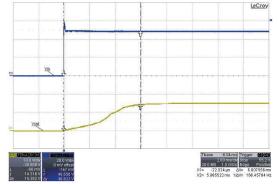
Efficiency vs Output Load



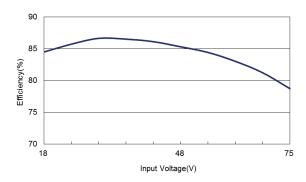
Typical Output Ripple and Noise



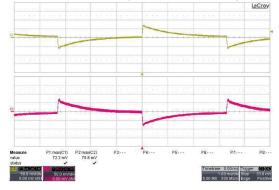
Typical Input Start-Up and Output Rise Characteristic



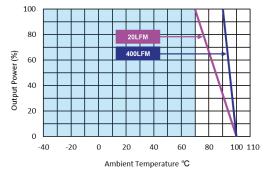
Efficiency vs Input Voltage



Transient Response to Dynamic Load Change (25%)



Derating Output Load versus Ambient Temperature



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