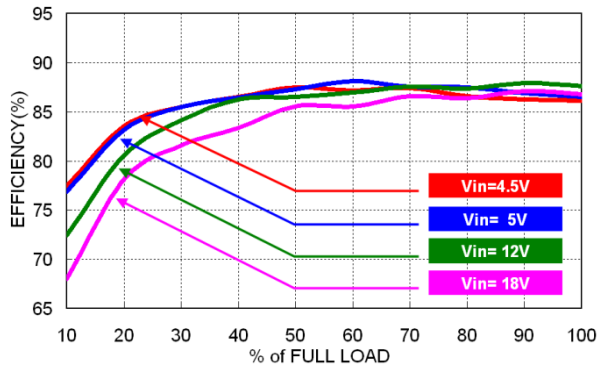


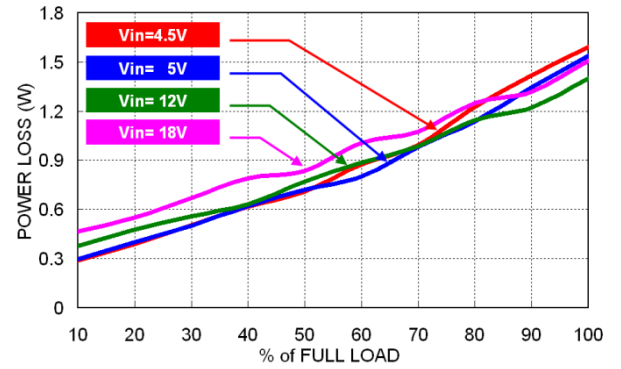
### Characteristic Curves

#### TMR 12-1210WI

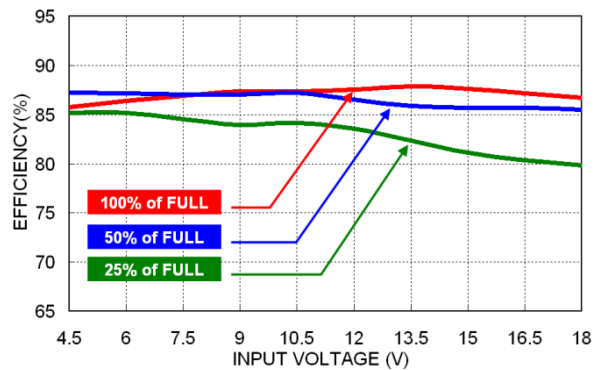
Efficiency versus Output Load



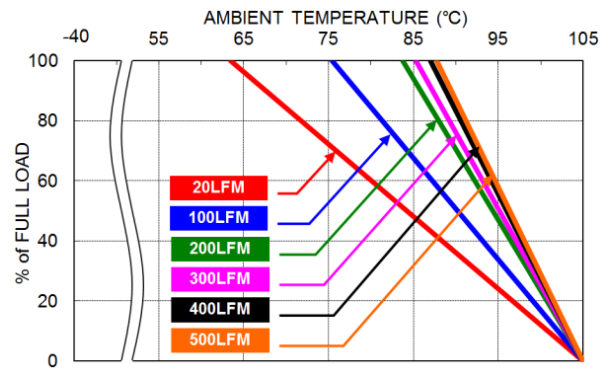
Power Dissipation versus Output Load



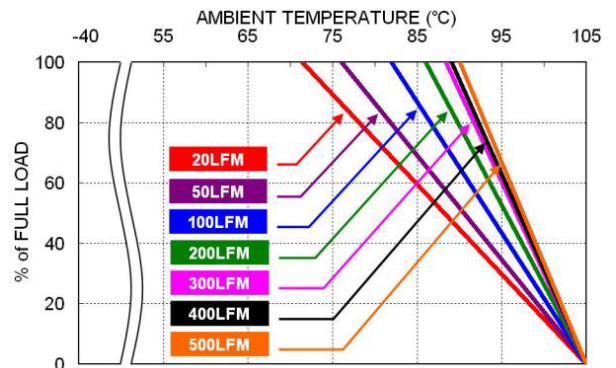
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

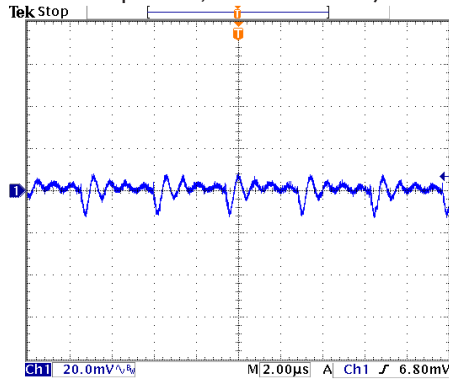


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

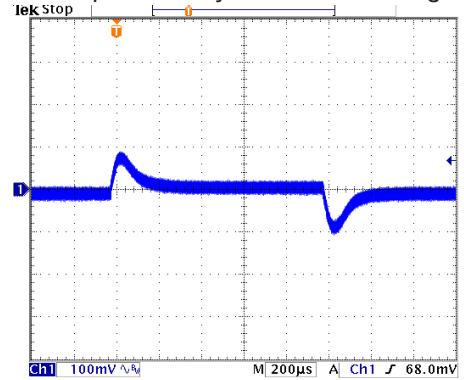


### TMR 12-1210WI

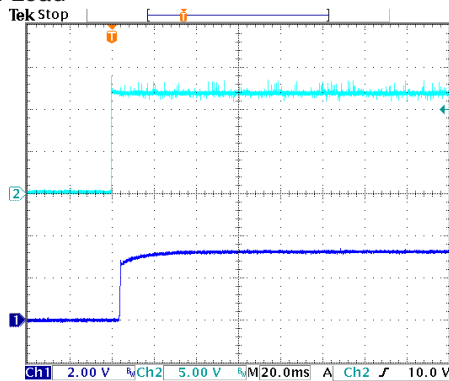
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



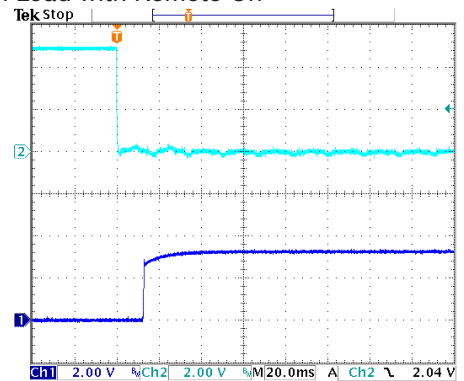
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

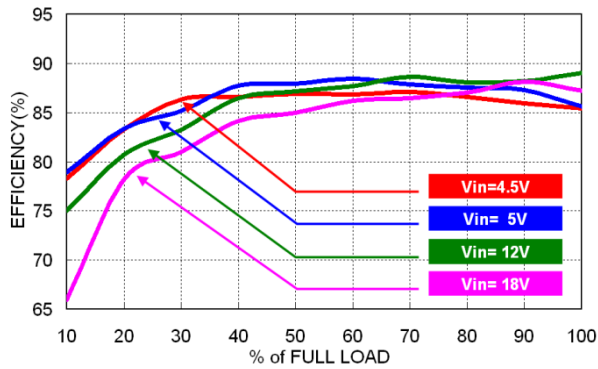


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

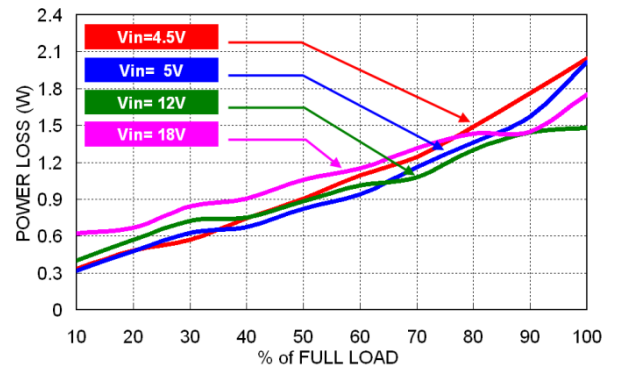


### TMR 12-1211WI

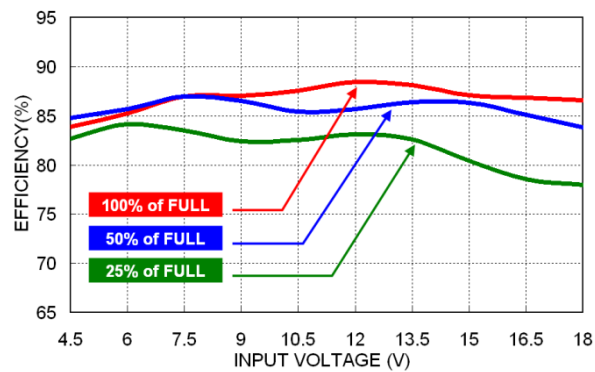
Efficiency versus Output Load



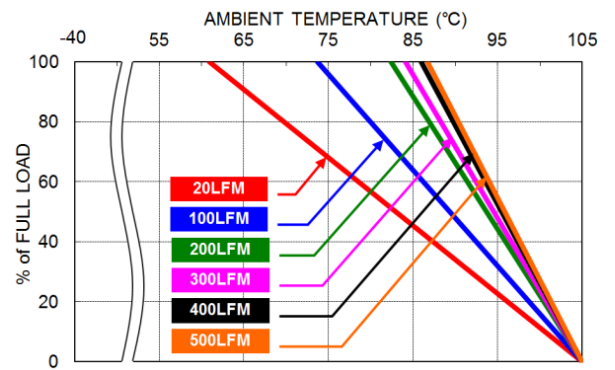
Power Dissipation versus Output Load



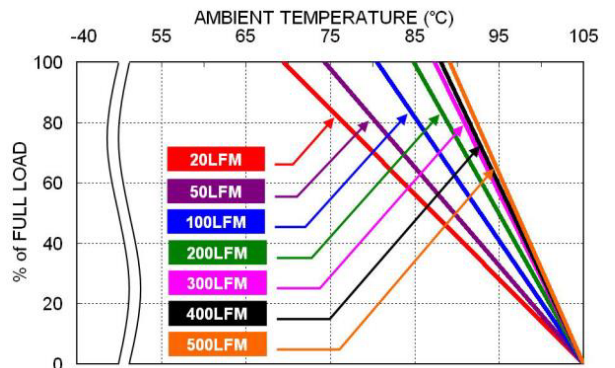
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

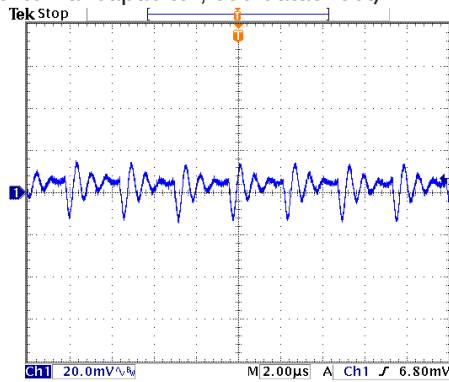


Derating Output Load versus Ambient Temperature with recommended PCB layout  
(see thermal considerations)

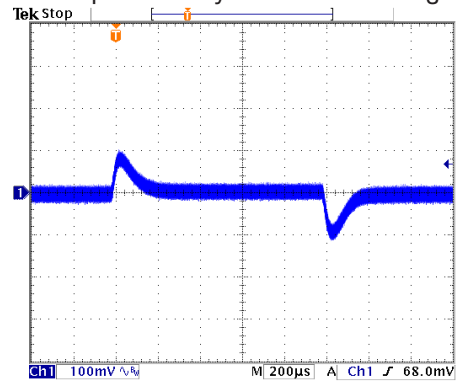


### TMR 12-1211WI

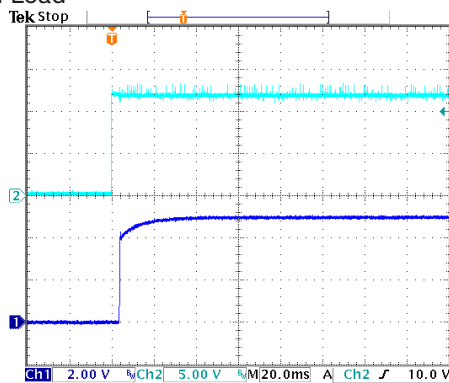
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



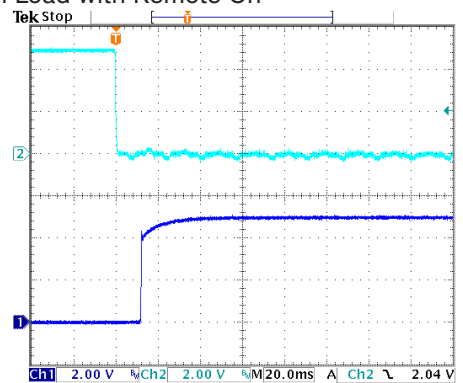
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load



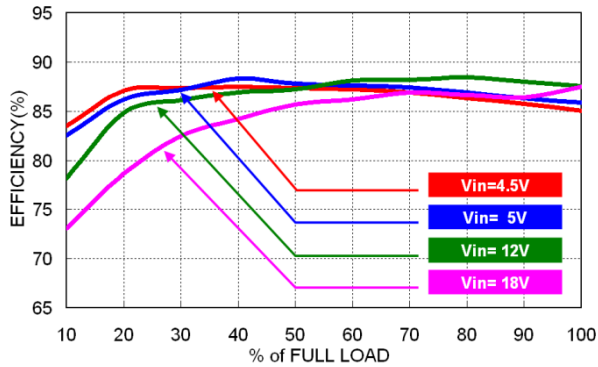
Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On



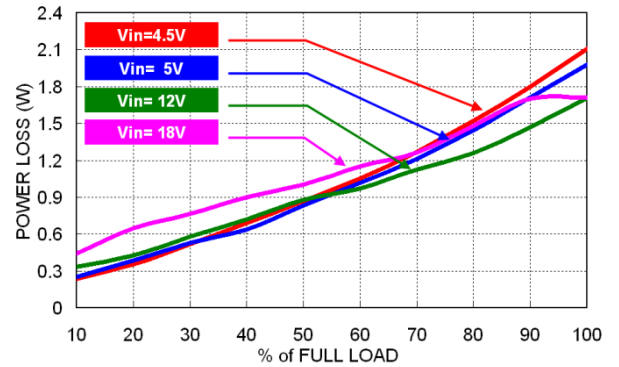


### On Demand model with 12 Vin and 9 Vout

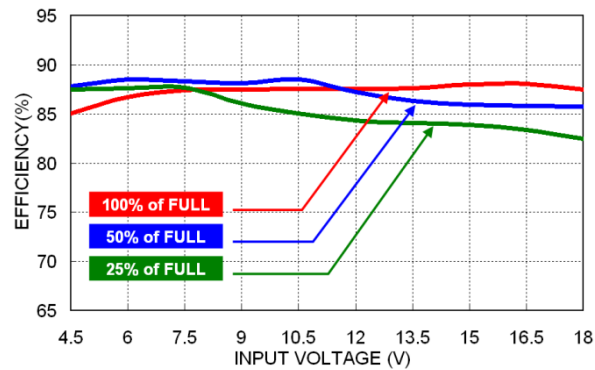
Efficiency versus Output Load



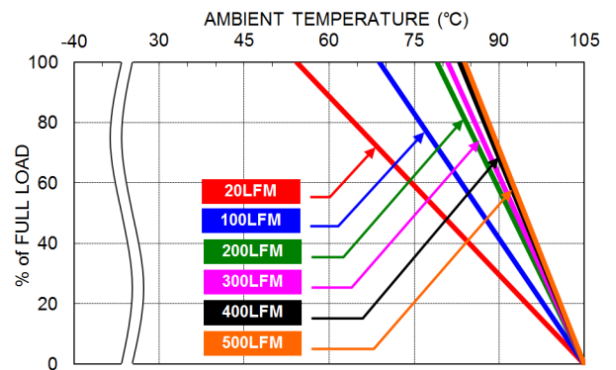
Power Dissipation versus Output Load



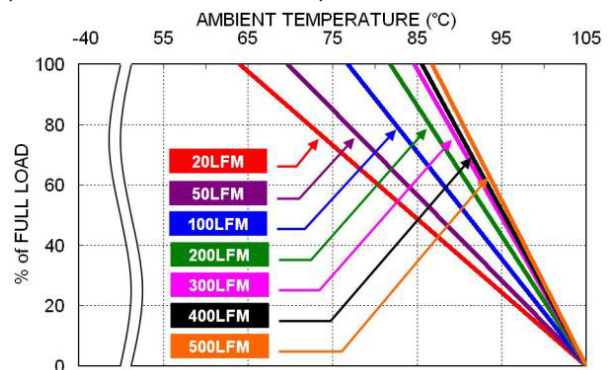
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

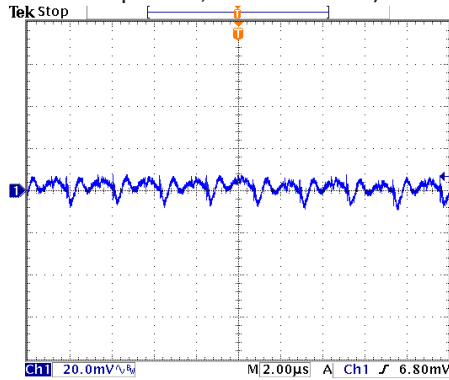


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

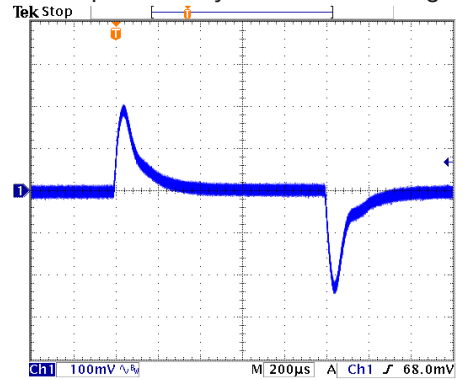


**On Demand model with 12 Vin and 9 Vout**

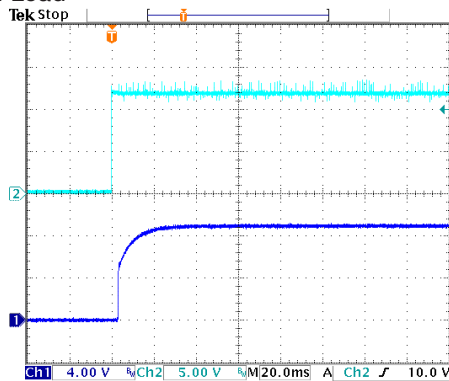
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



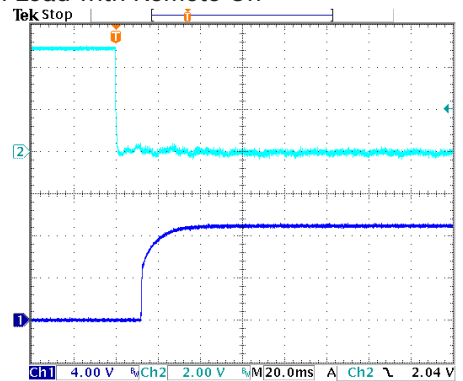
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

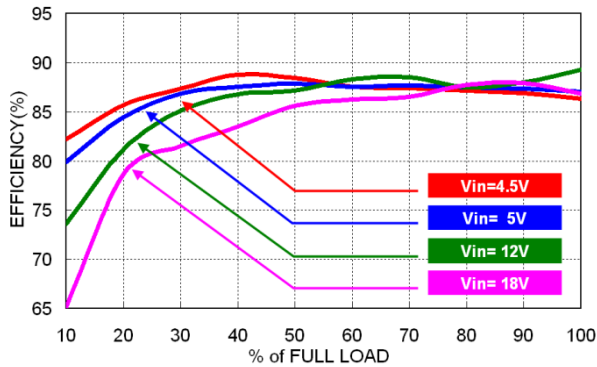


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

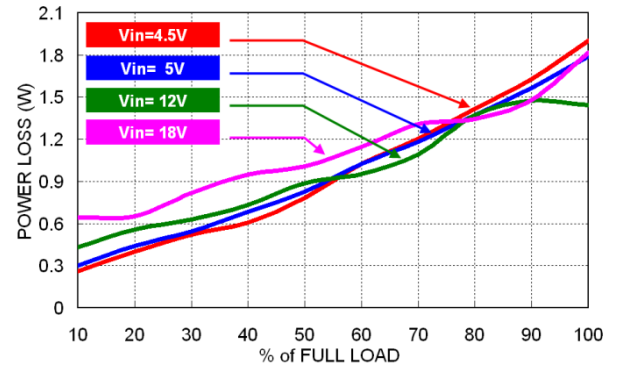


### TMR 12-1212WI

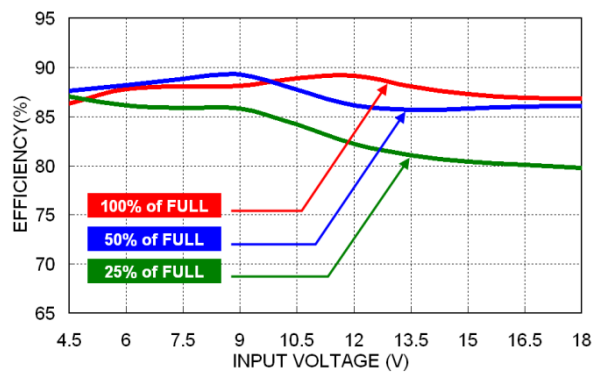
Efficiency versus Output Load



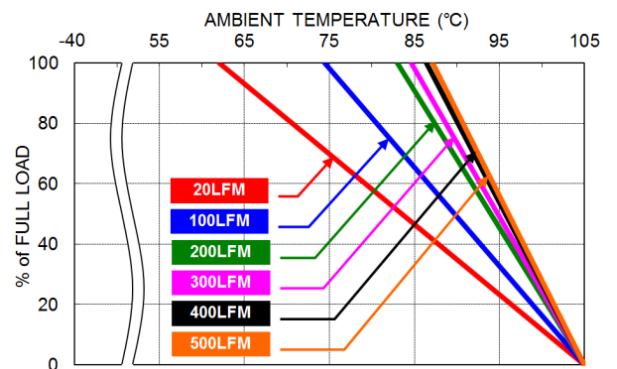
Power Dissipation versus Output Load



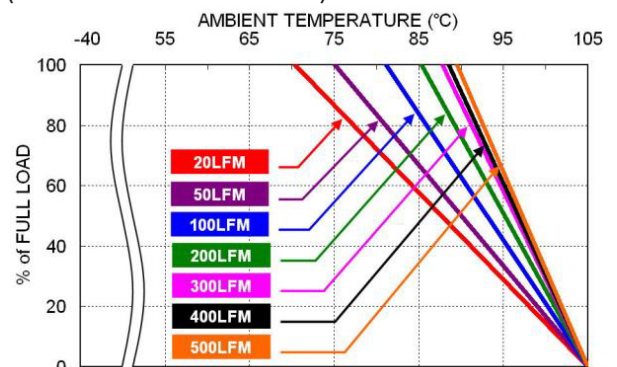
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

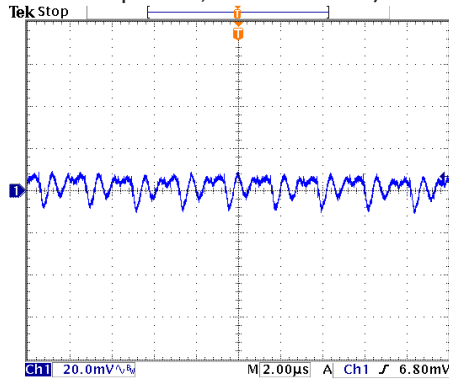


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

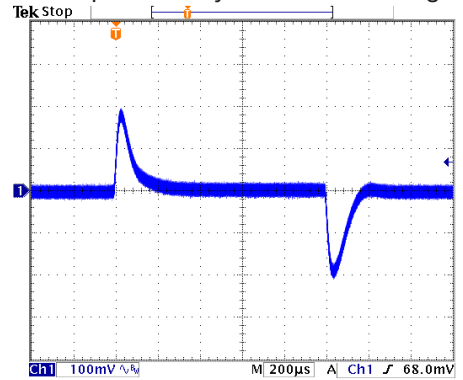


**TMR 12-1212WI**

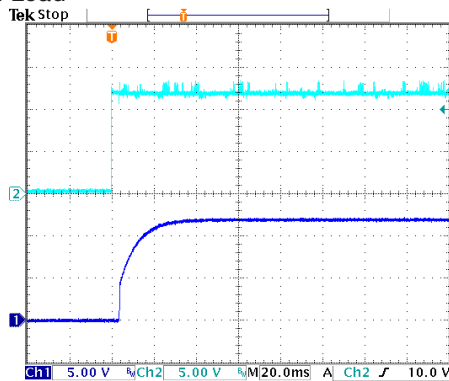
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



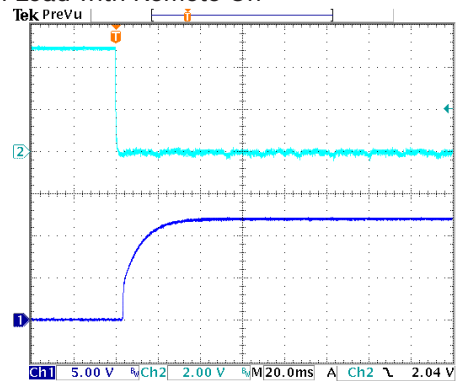
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

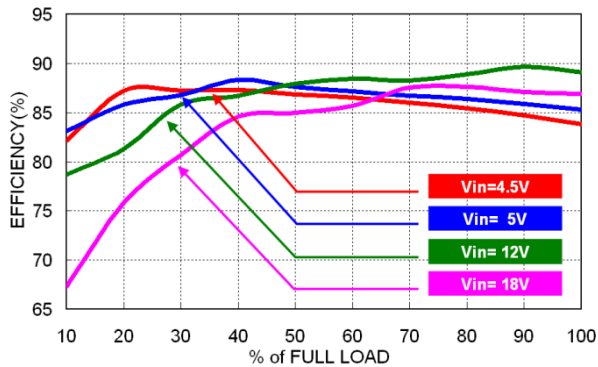


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

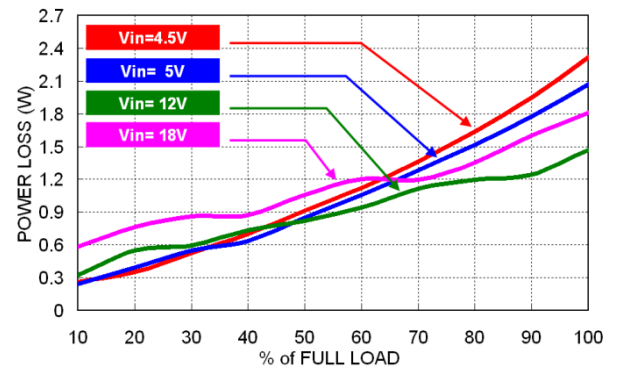


### TMR 12-1213WI

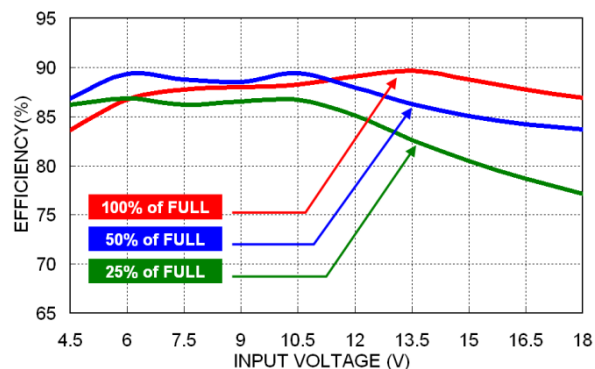
Efficiency versus Output Load



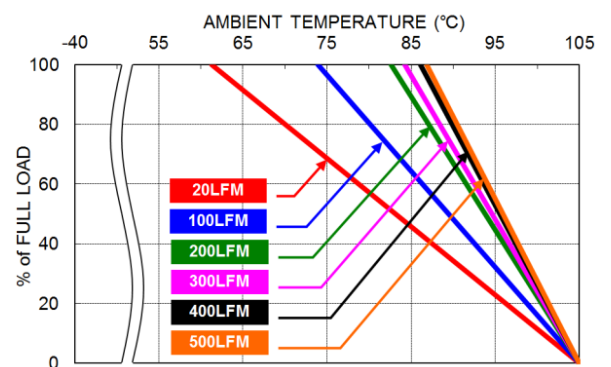
Power Dissipation versus Output Load



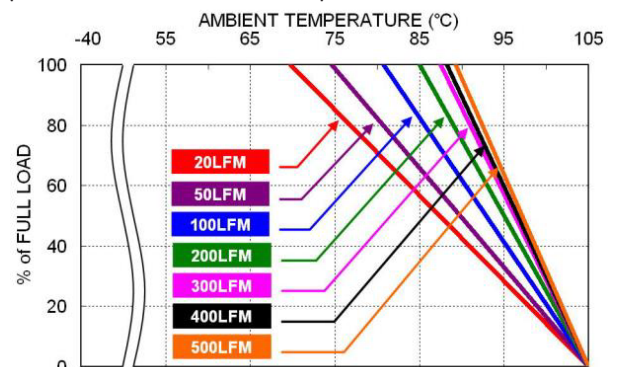
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

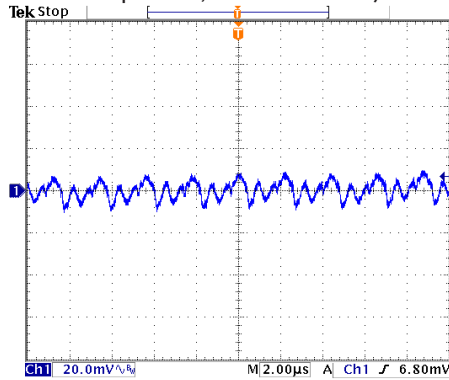


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

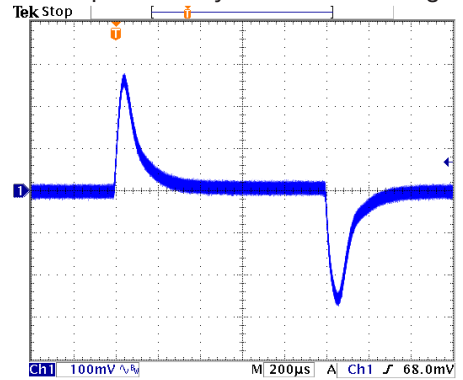


**TMR 12-1213WI**

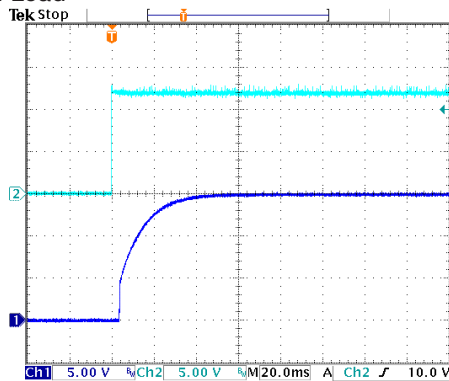
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



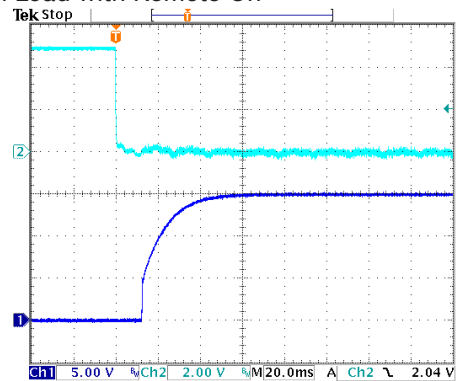
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

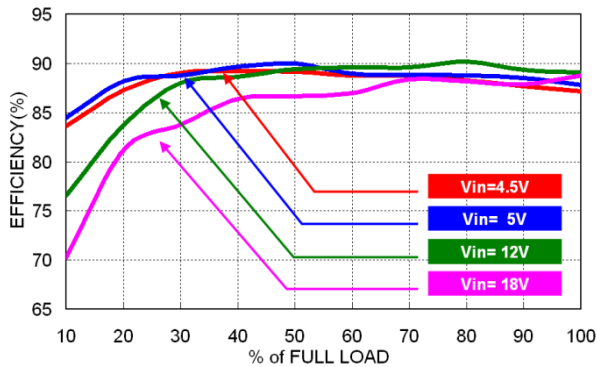


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

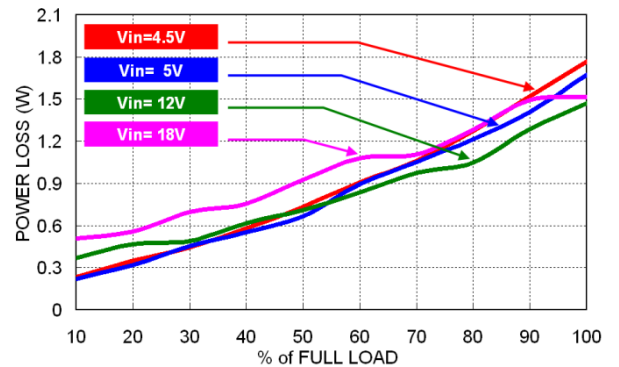


### TMR 12-1215WI

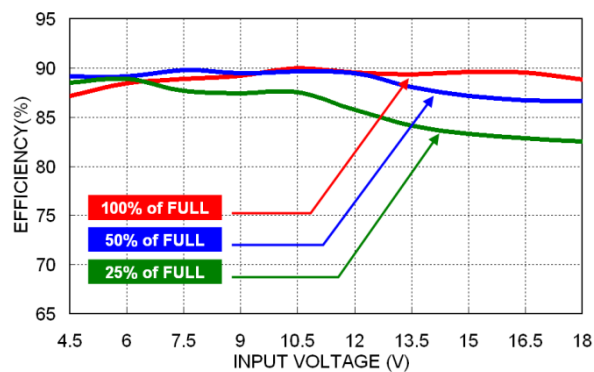
Efficiency versus Output Load



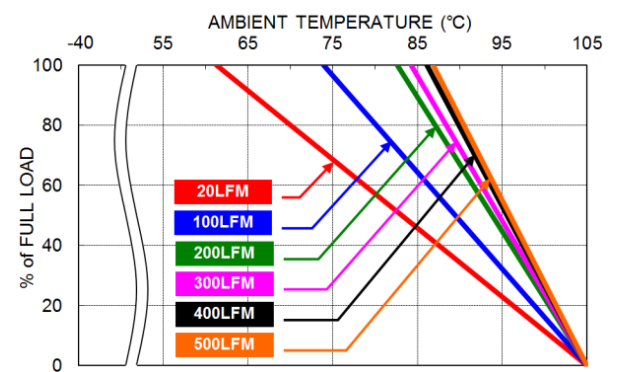
Power Dissipation versus Output Load



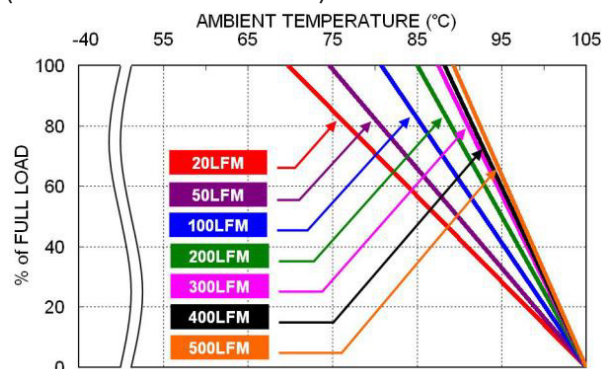
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

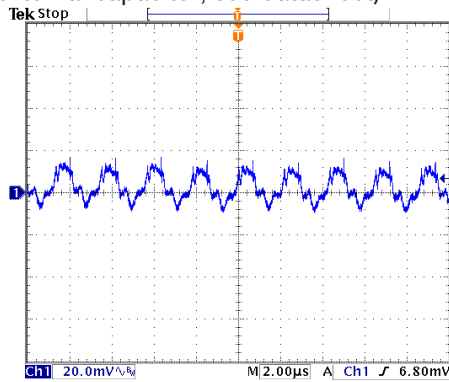


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

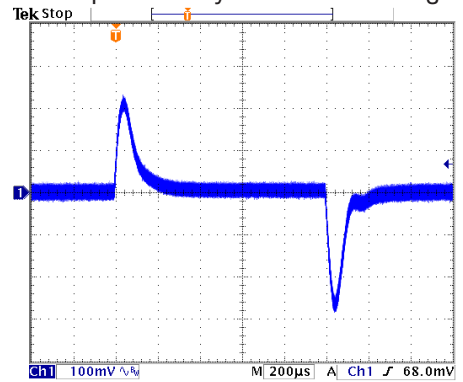


### TMR 12-1215WI

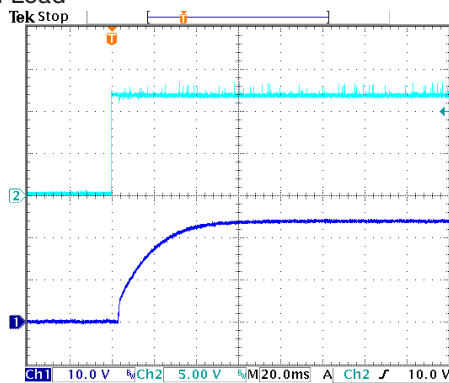
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



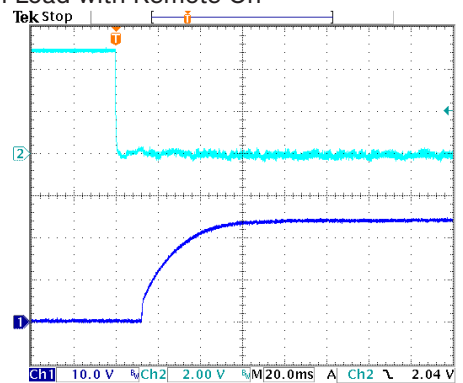
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load



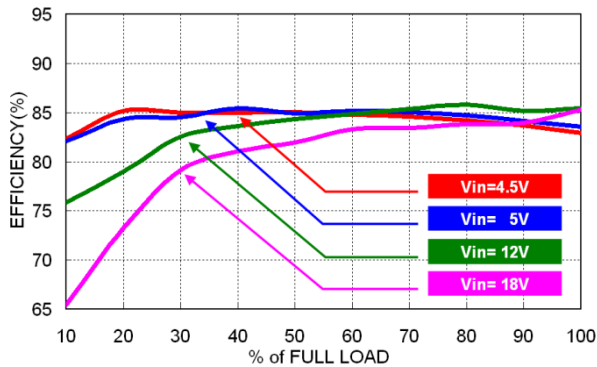
Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On



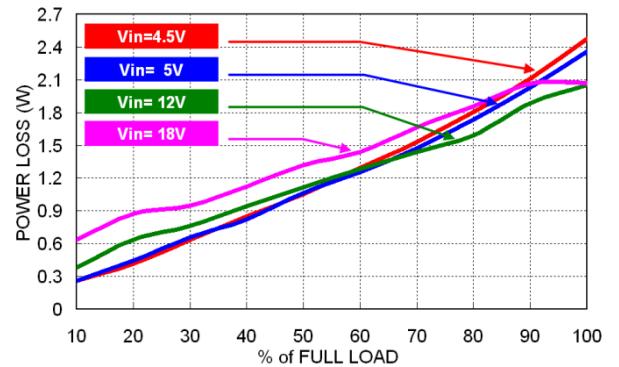


### TMR 12-1221WI

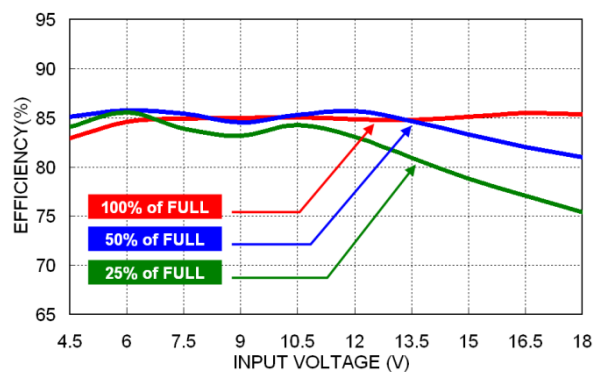
Efficiency versus Output Load



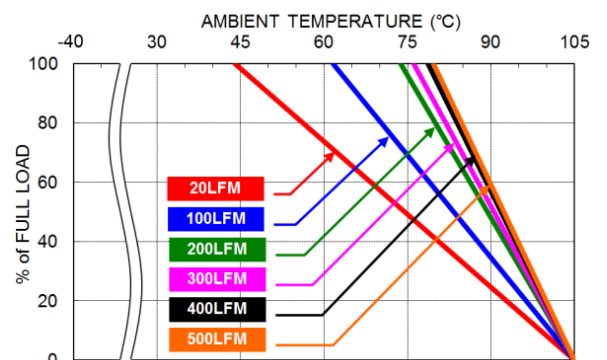
Power Dissipation versus Output Load



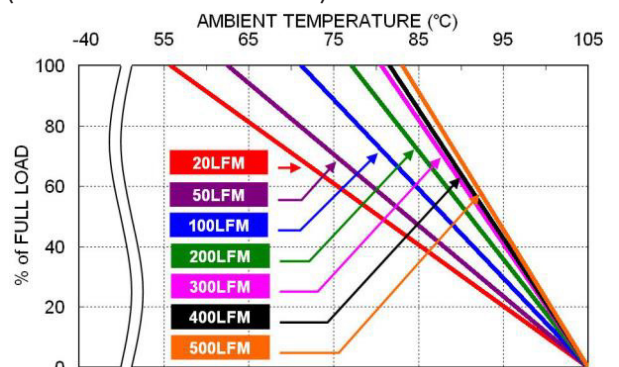
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

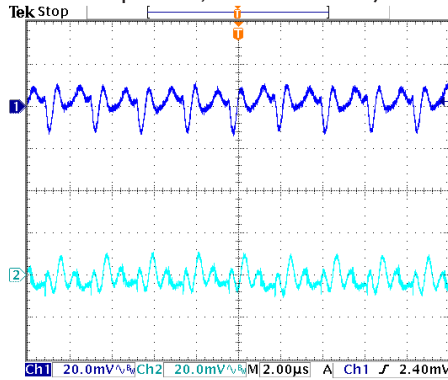


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

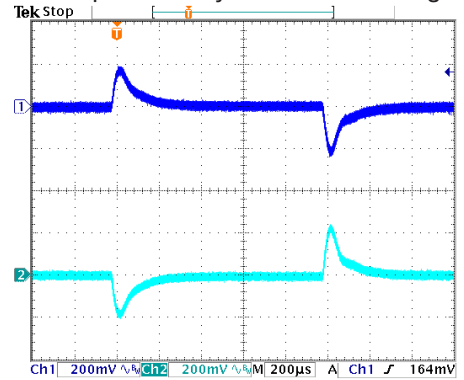


## TMR 12-1221WI

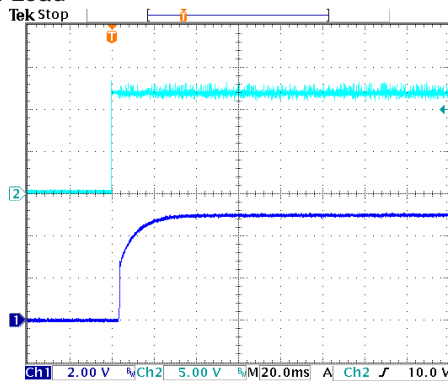
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



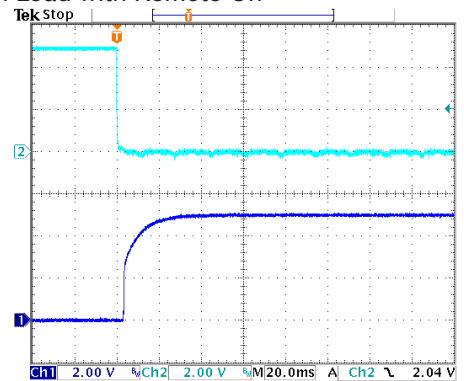
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

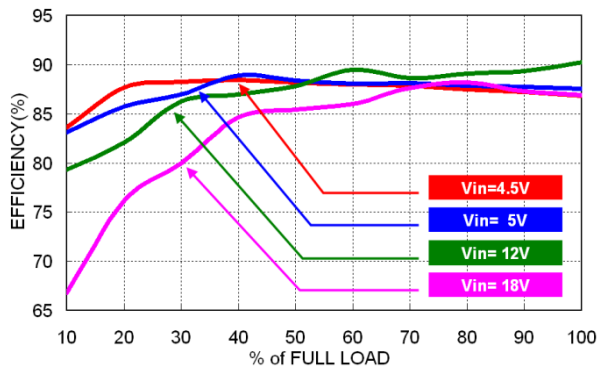


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

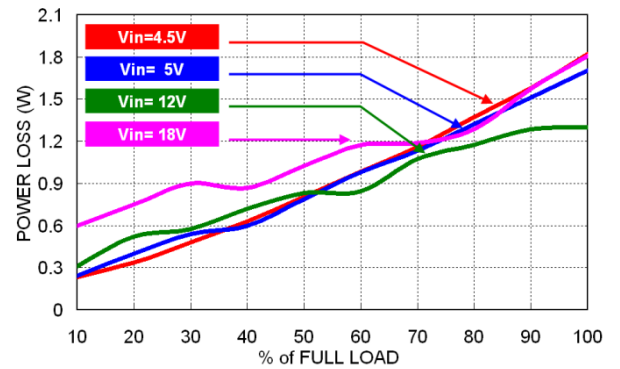


### TMR 12-1222WI

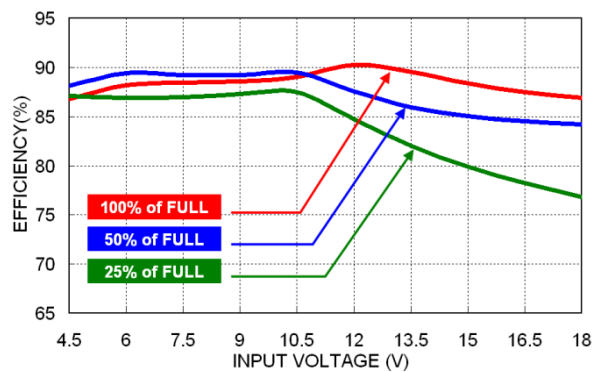
Efficiency versus Output Load



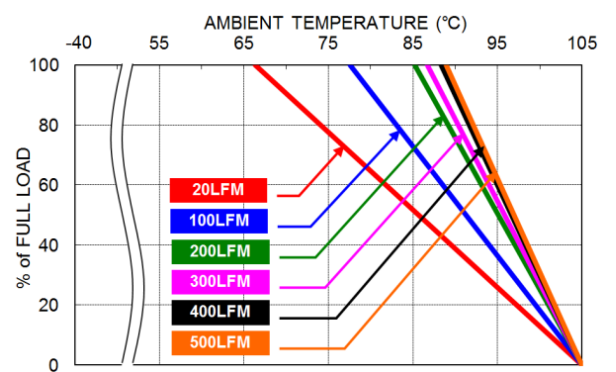
Power Dissipation versus Output Load



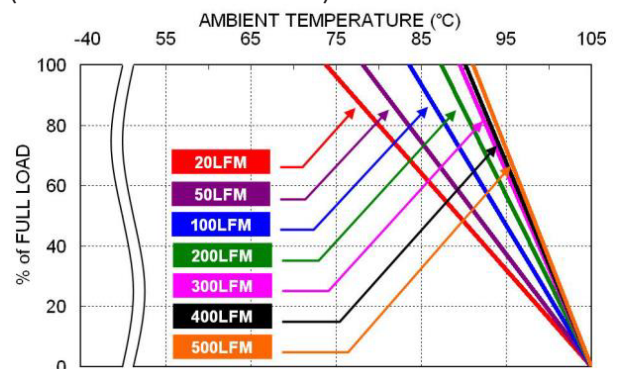
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

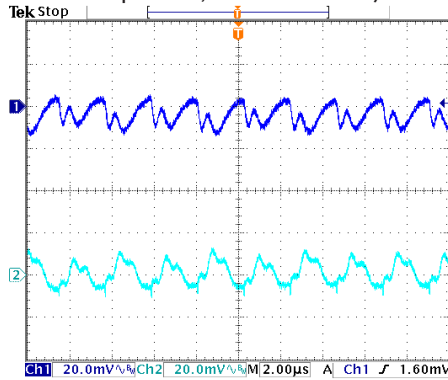


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

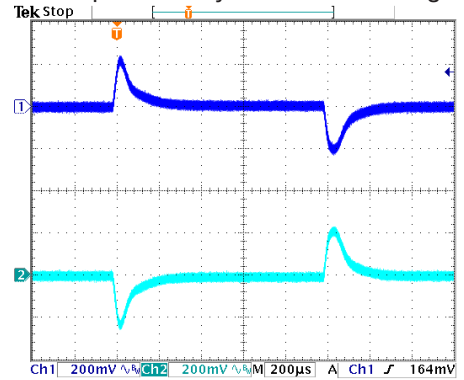


### TMR 12-1222WI

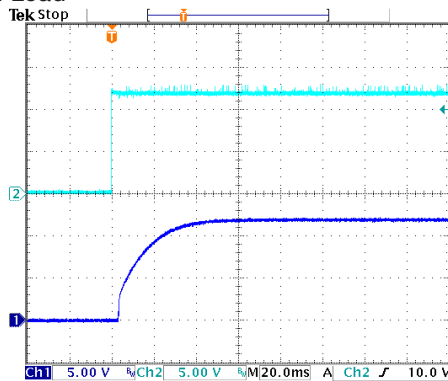
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



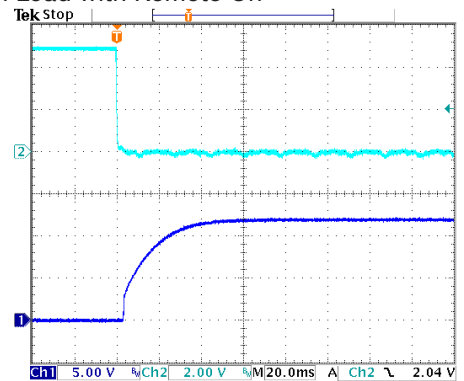
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

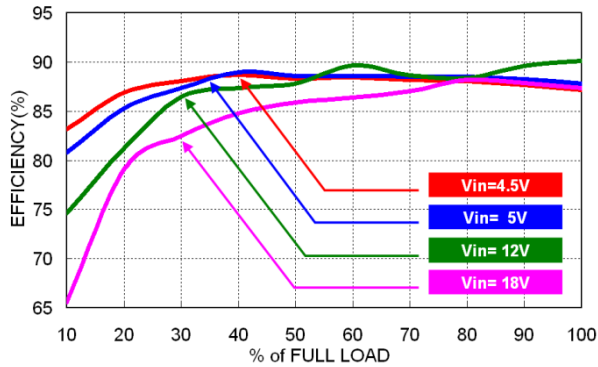


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

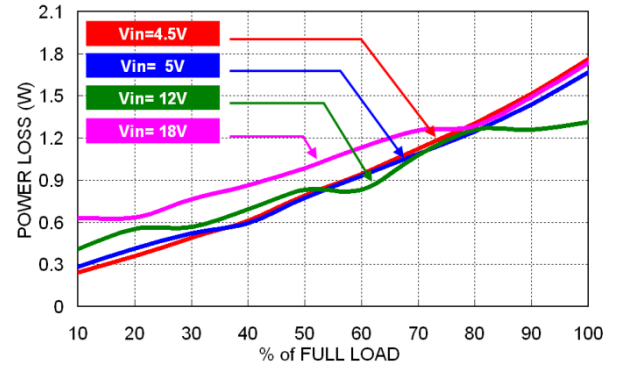


### TMR 12-1223WI

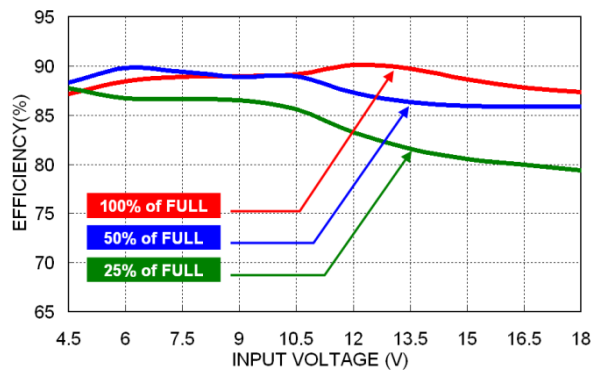
Efficiency versus Output Load



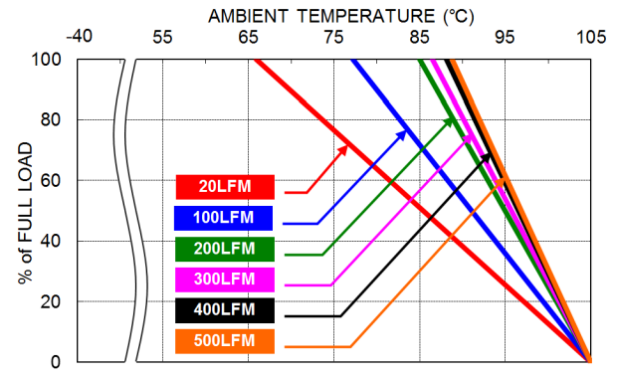
Power Dissipation versus Output Load



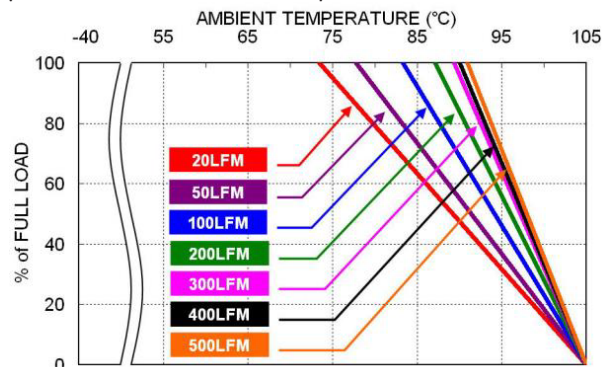
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

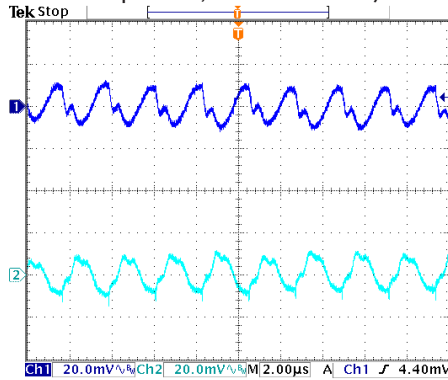


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

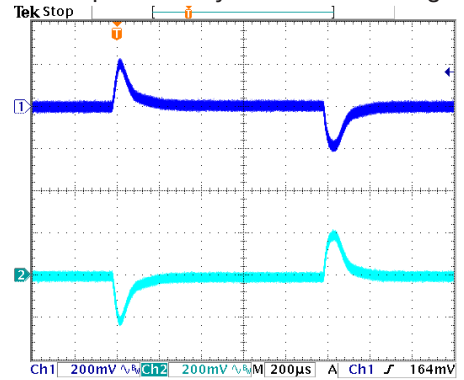


### TMR 12-1223WI

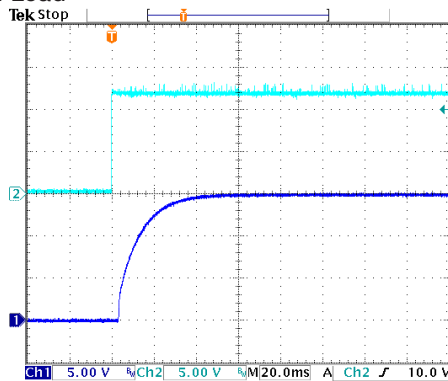
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



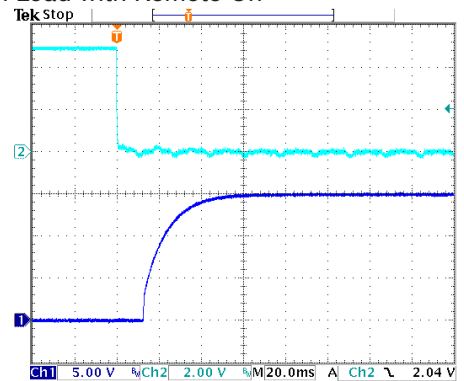
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

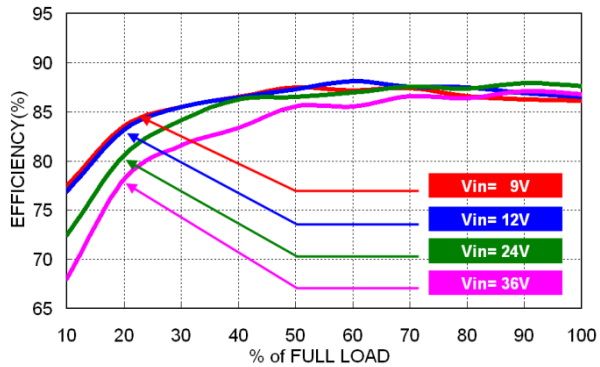


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

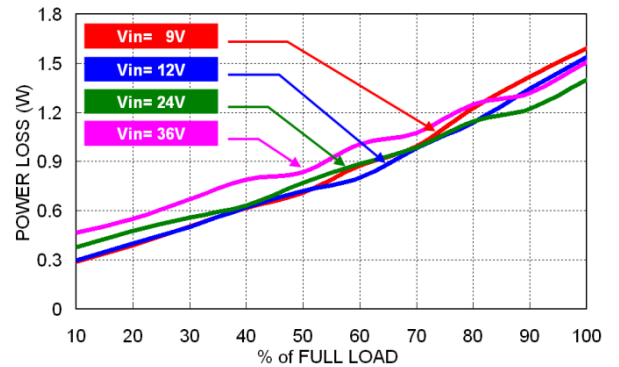


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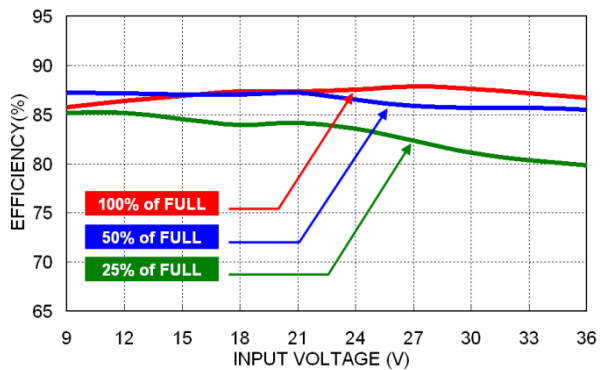
Efficiency versus Output Load



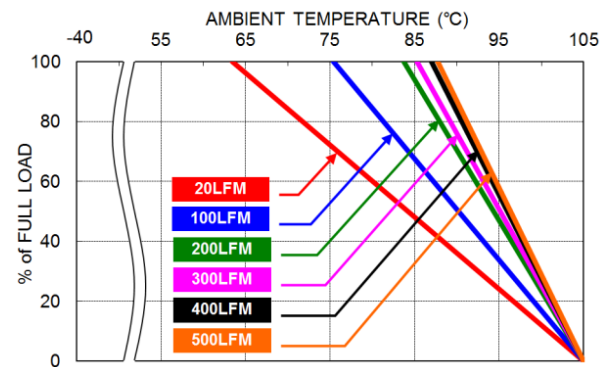
Power Dissipation versus Output Load



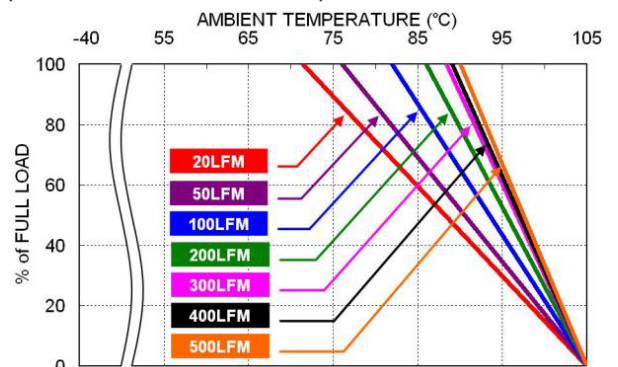
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

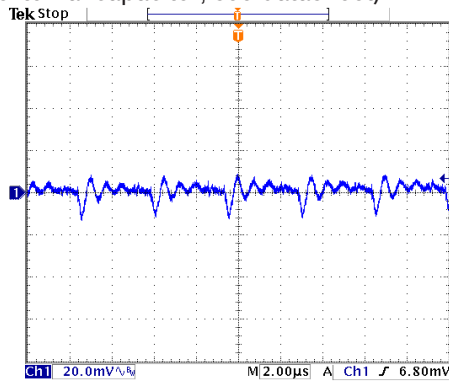


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

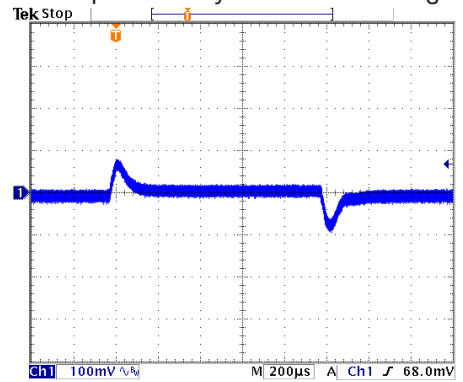


**TMR 12-2410WI**

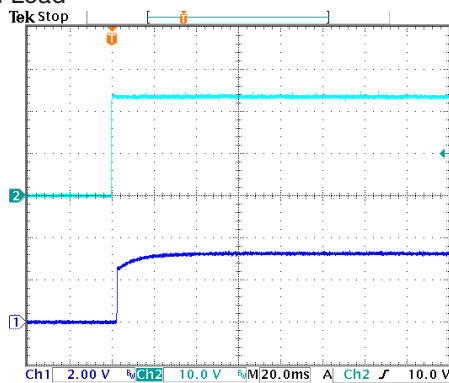
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



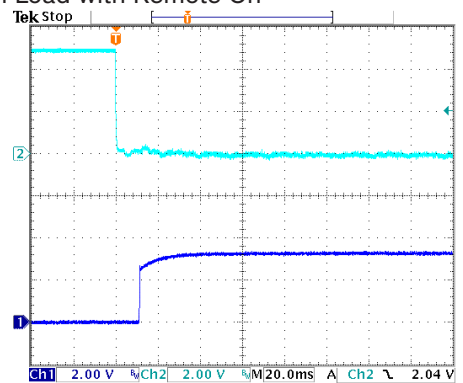
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load



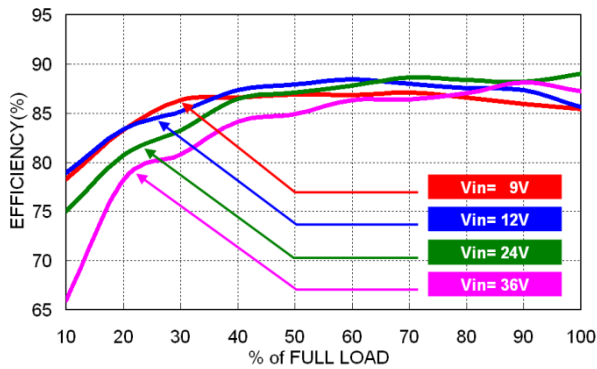
Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On



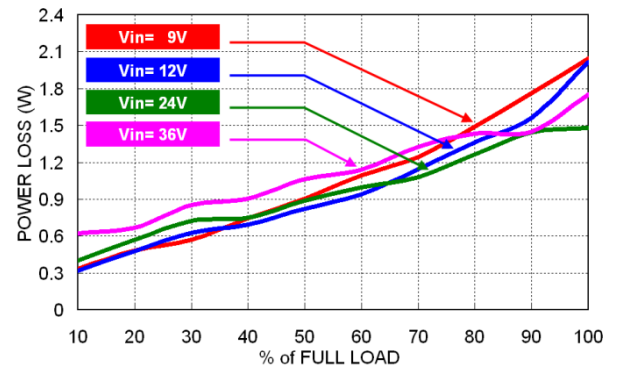


### TMR 12-2411WI

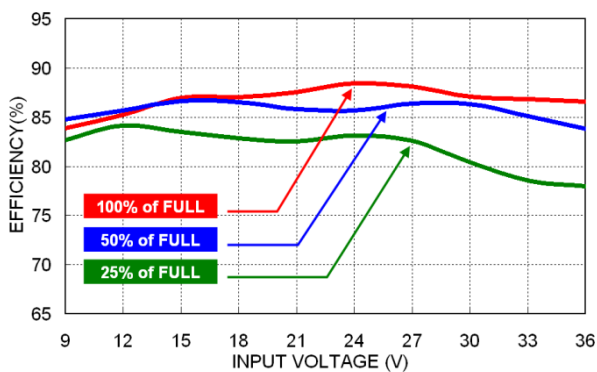
Efficiency versus Output Load



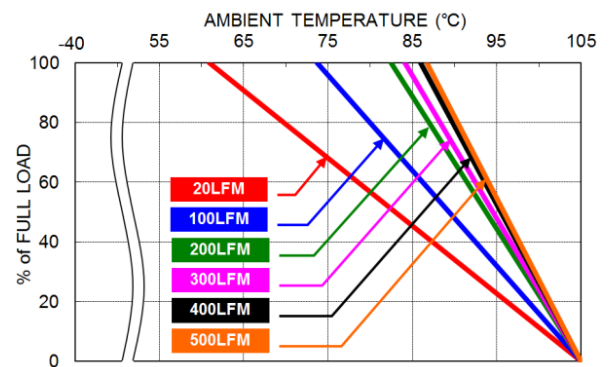
Power Dissipation versus Output Load



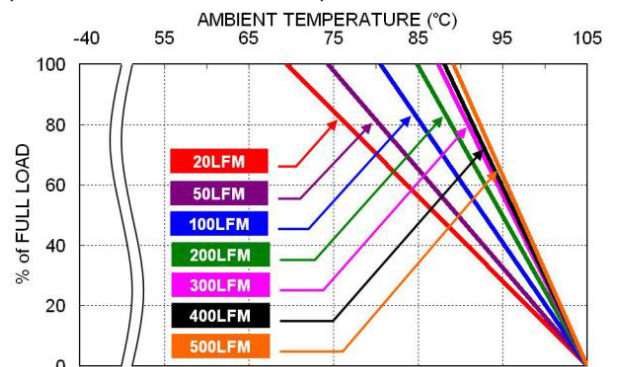
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

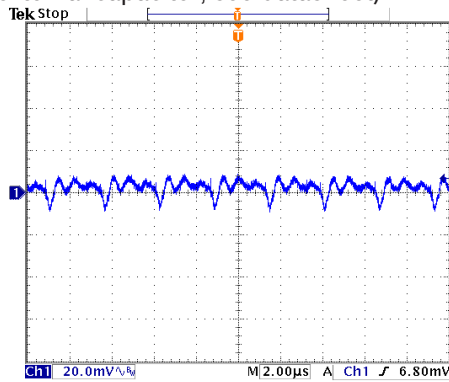


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

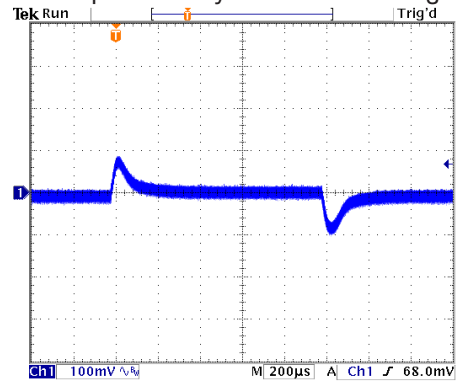


**TMR 12-2411WI**

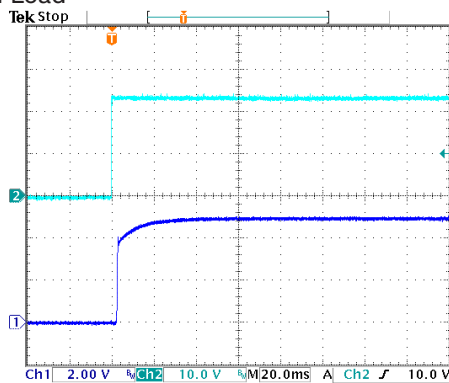
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



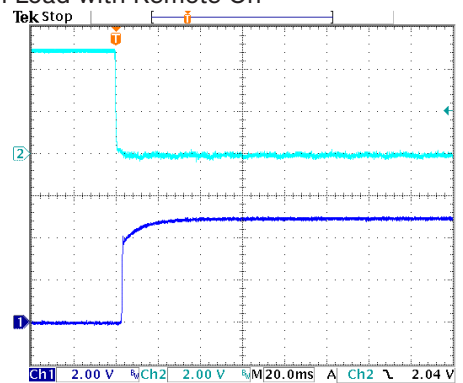
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

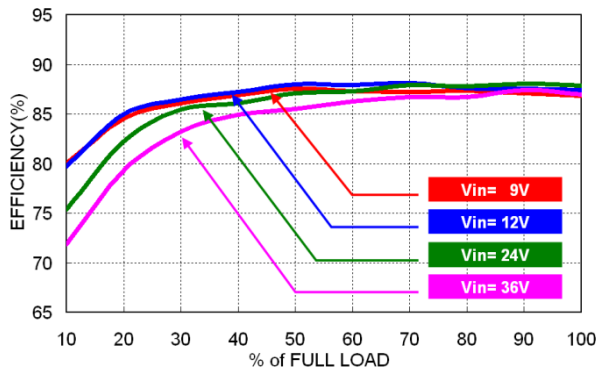


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

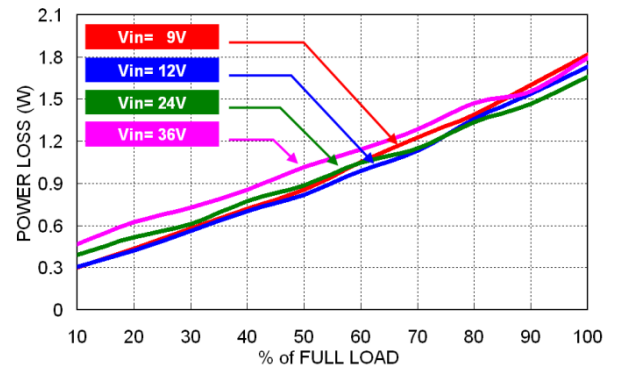


### On Demand model with 24 Vin and 9 Vout

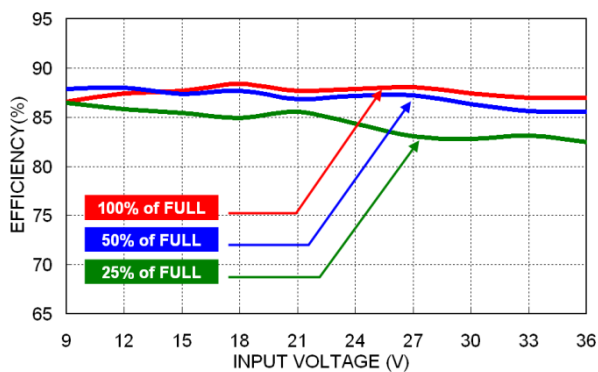
Efficiency versus Output Load



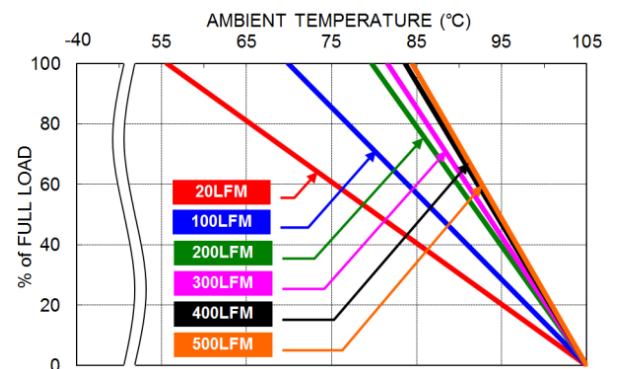
Power Dissipation versus Output Load



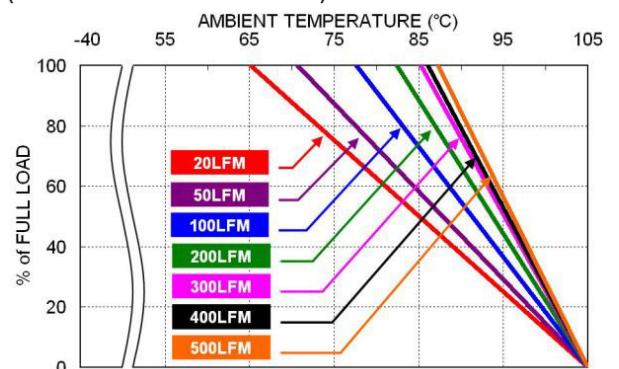
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

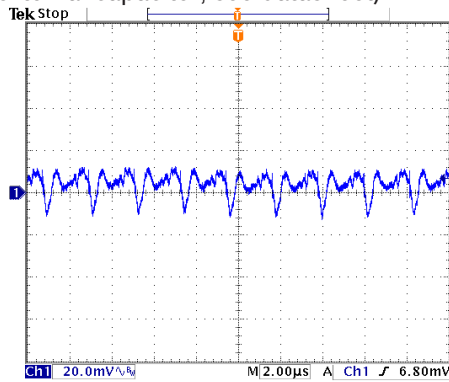


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

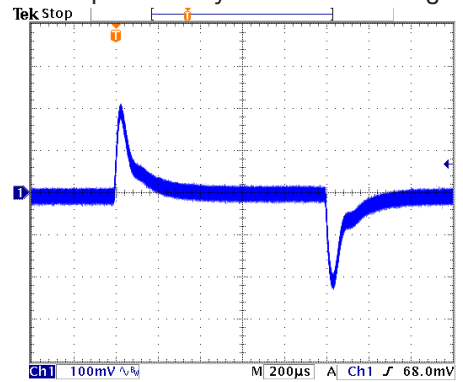


## On Demand model with 24 Vin and 9 Vout

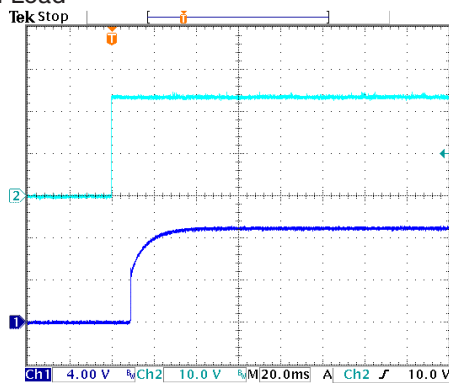
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



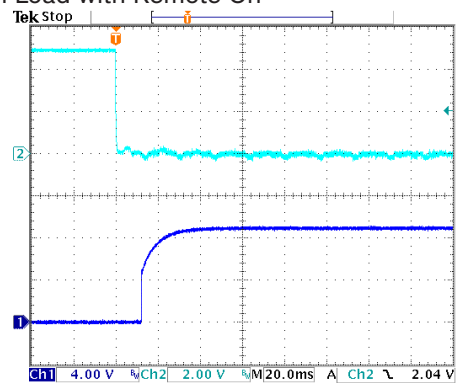
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

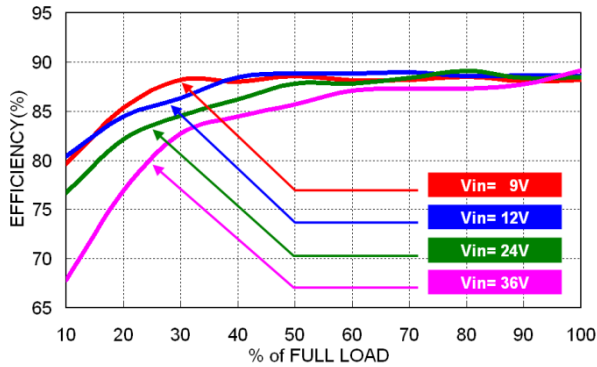


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

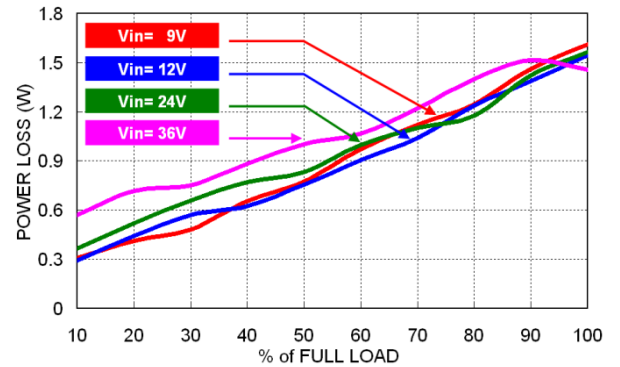


### TMR 12-2412WI

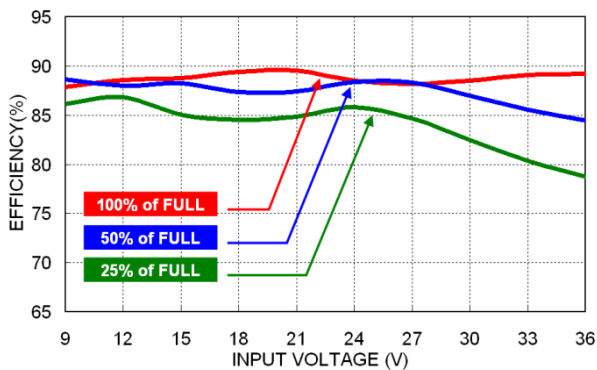
Efficiency versus Output Load



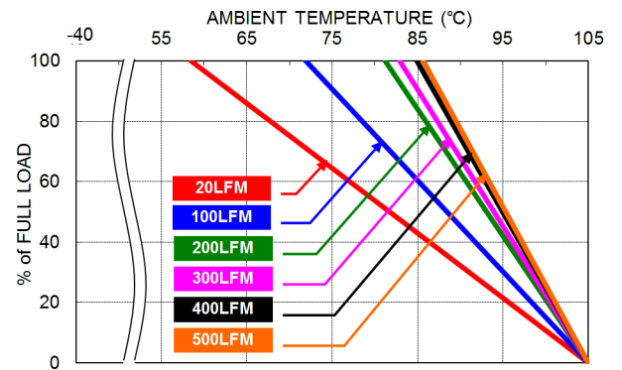
Power Dissipation versus Output Load



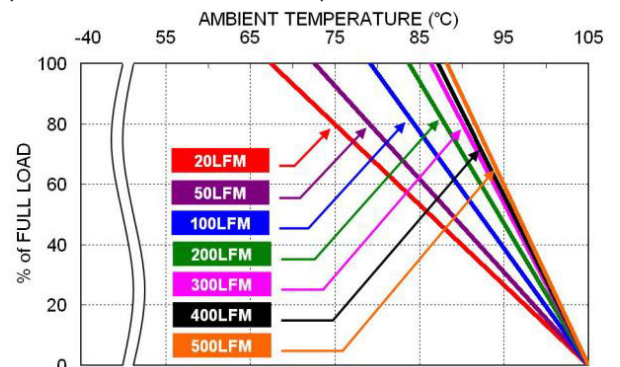
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

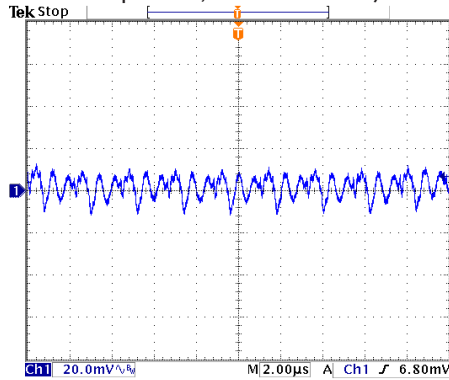


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

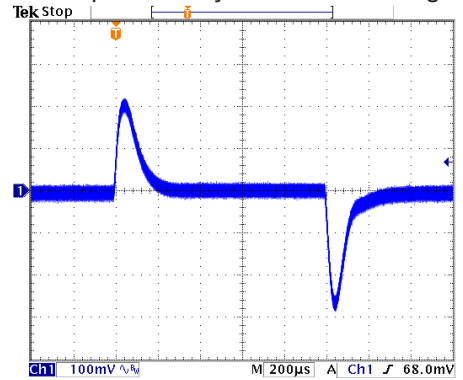


**TMR 12-2412WI**

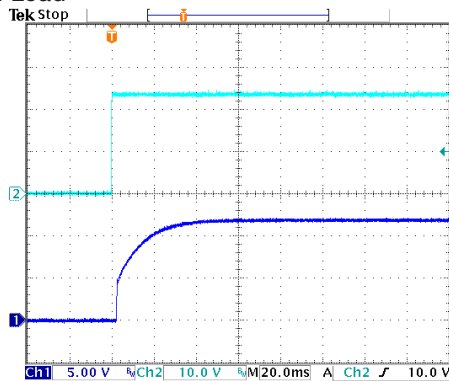
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



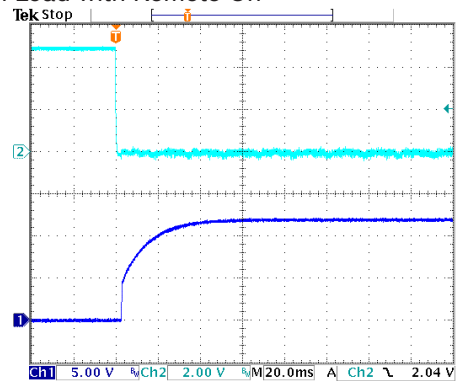
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

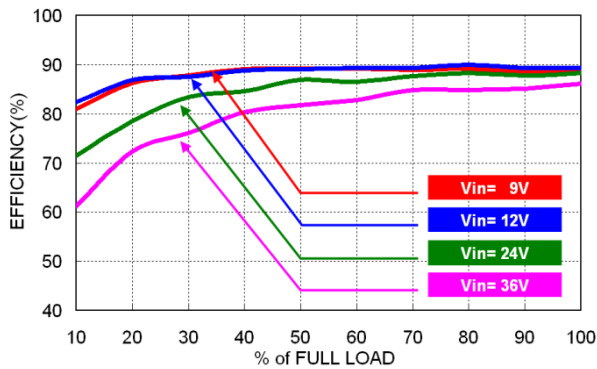


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

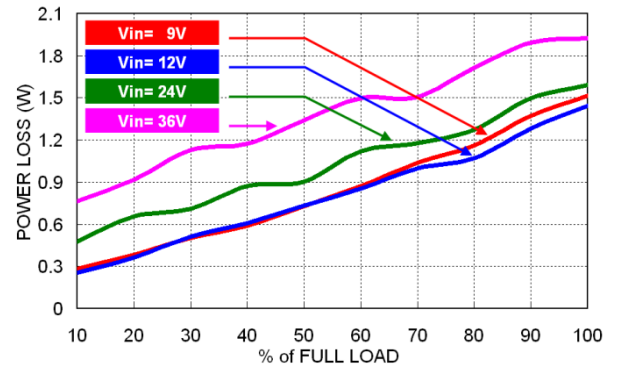


### TMR 12-2413WI

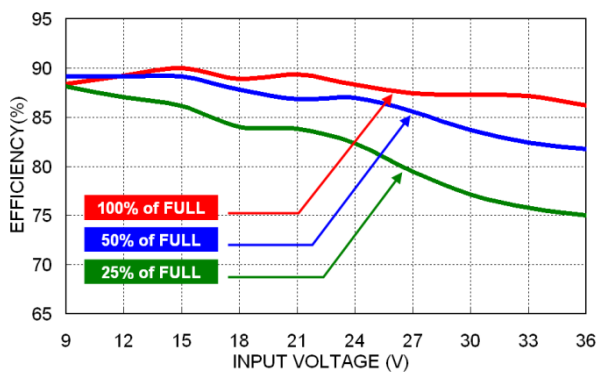
Efficiency versus Output Load



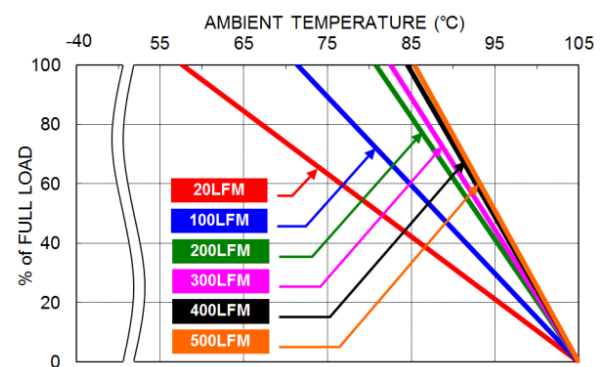
Power Dissipation versus Output Load



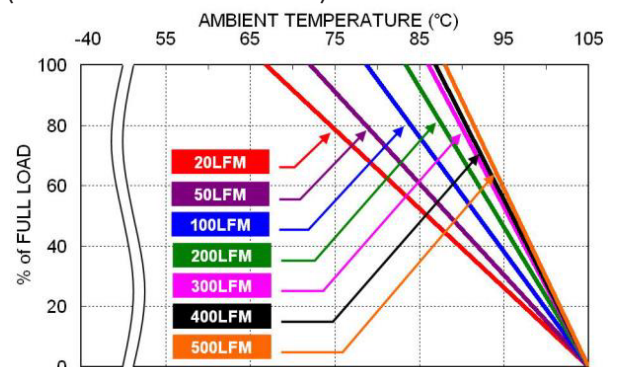
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

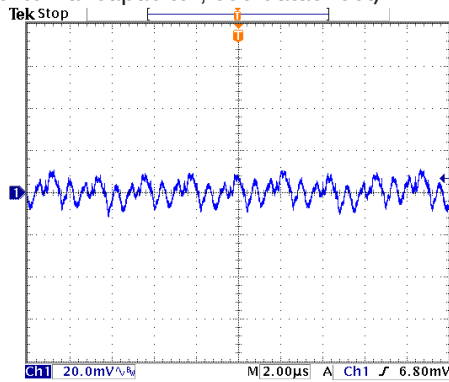


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

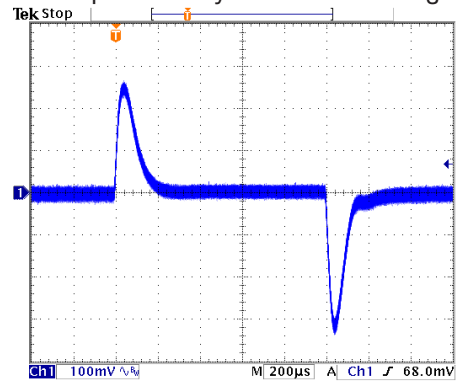


## TMR 12-2413WI

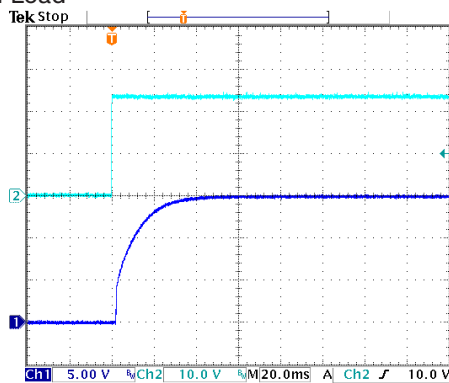
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



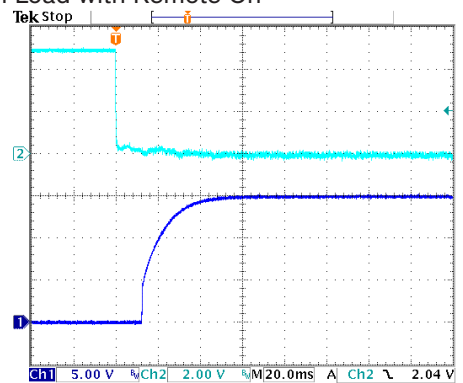
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load



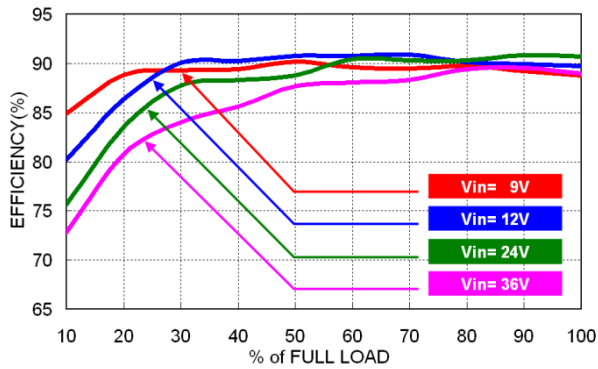
Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On



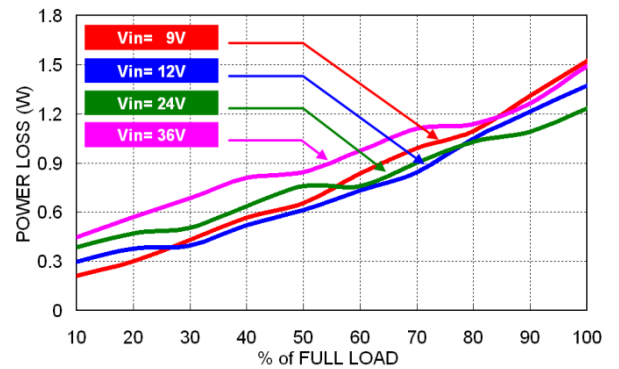


### TMR 12-2415WI

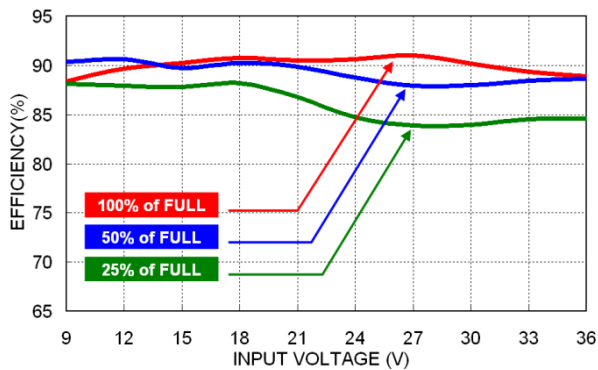
Efficiency versus Output Load



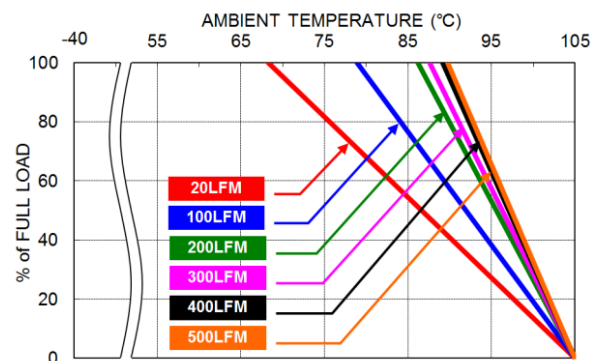
Power Dissipation versus Output Load



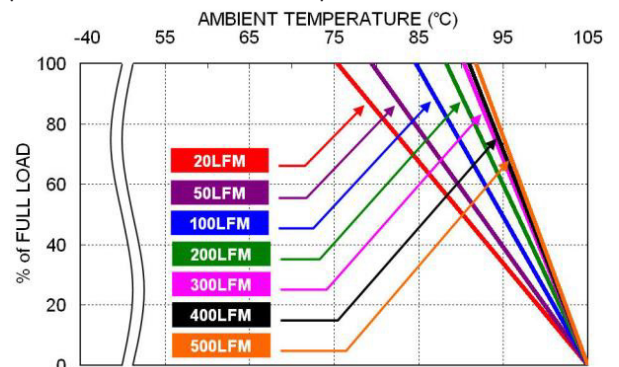
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

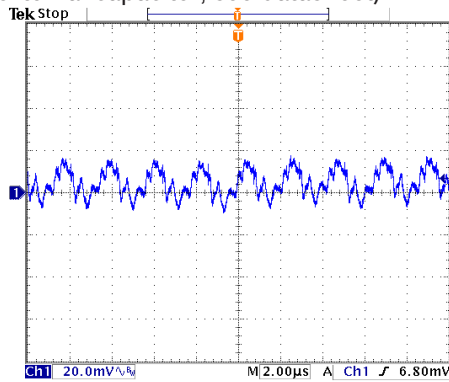


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

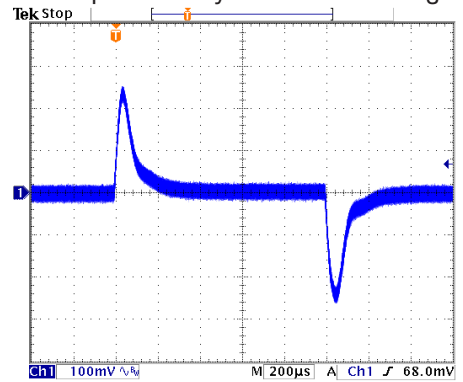


**TMR 12-2415WI**

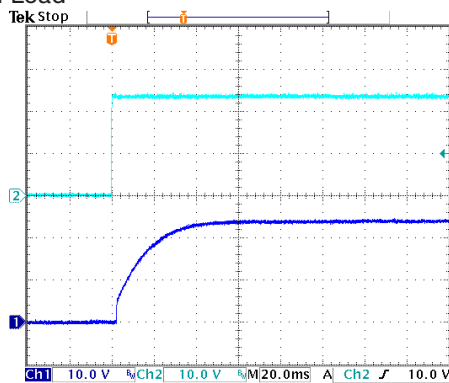
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



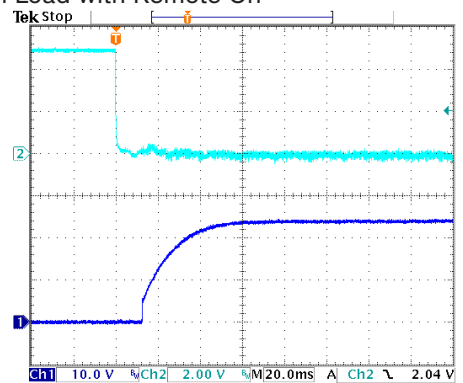
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

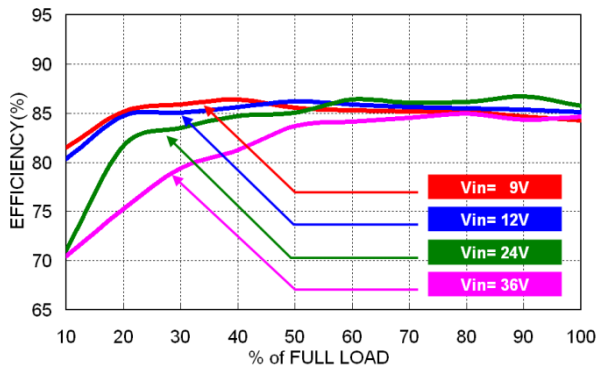


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

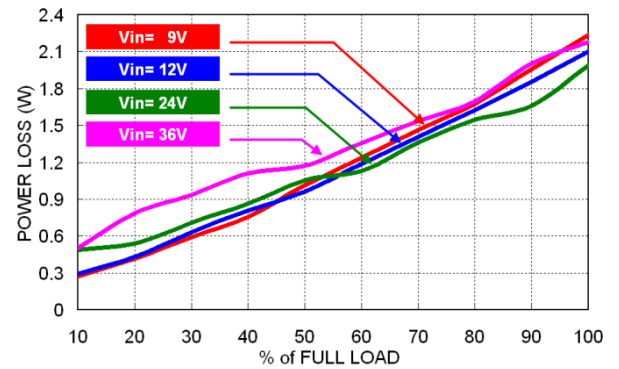


### TMR 12-2421WI

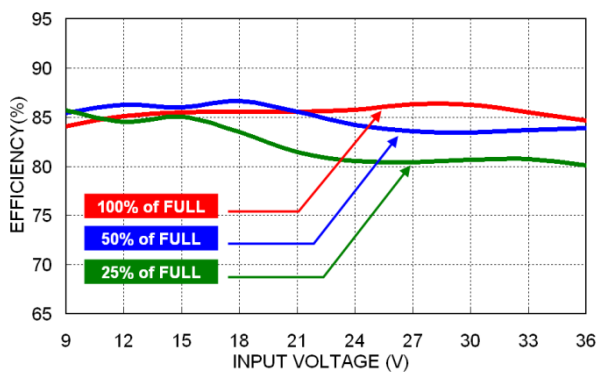
Efficiency versus Output Load



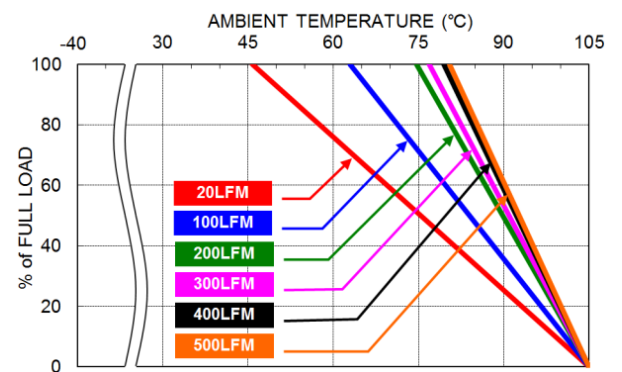
Power Dissipation versus Output Load



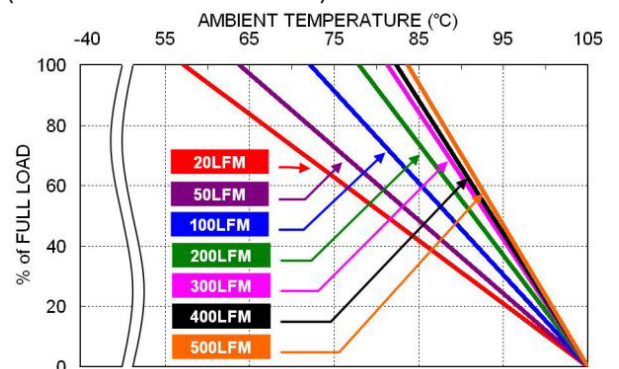
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

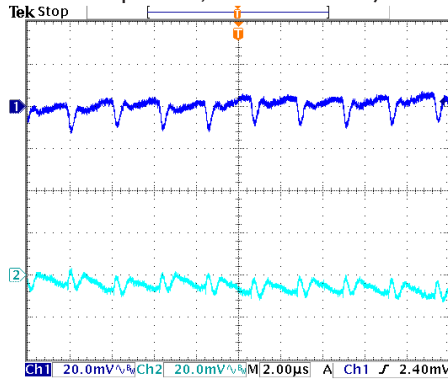


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

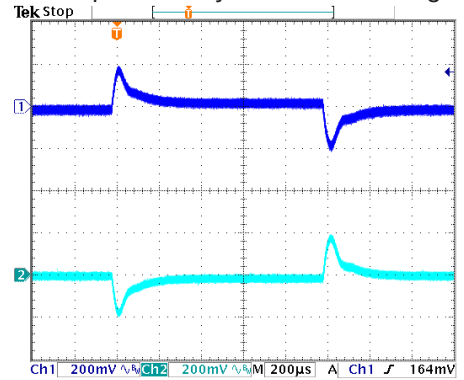


**TMR 12-2421WI**

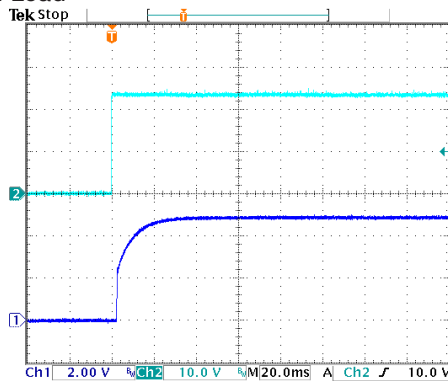
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



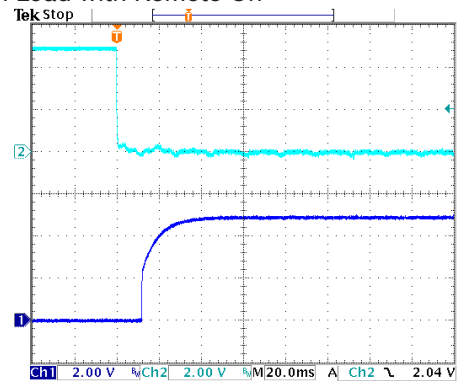
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

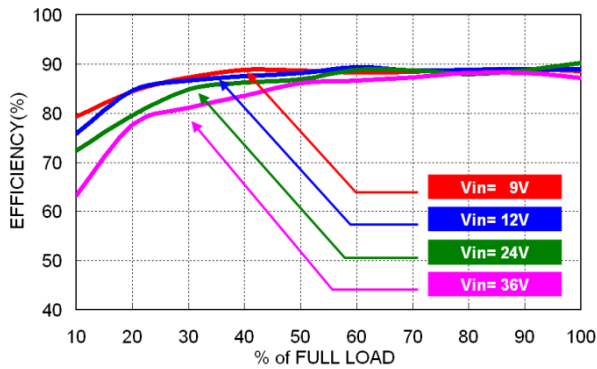


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

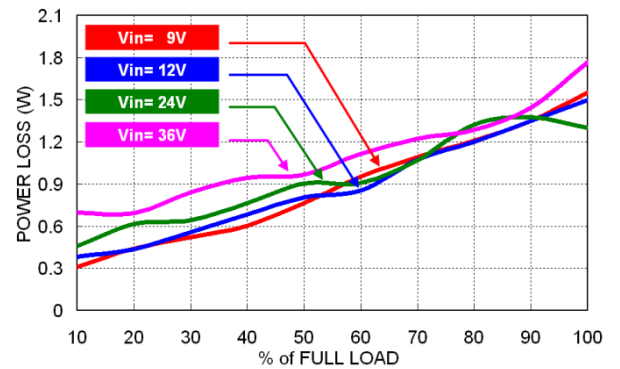


### TMR 12-2422WI

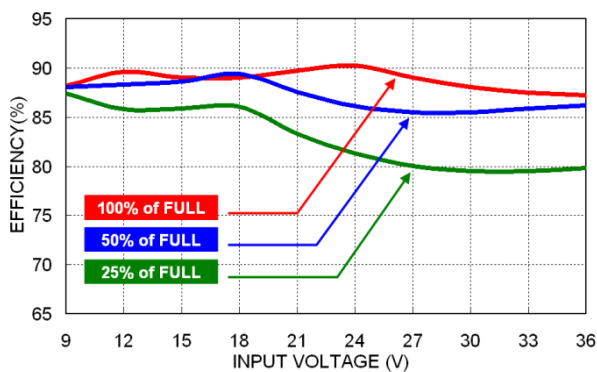
Efficiency versus Output Load



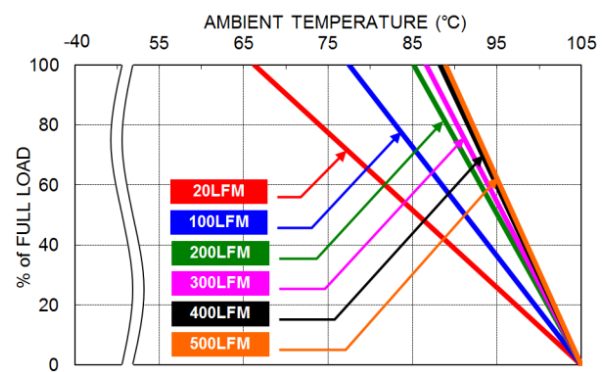
Power Dissipation versus Output Load



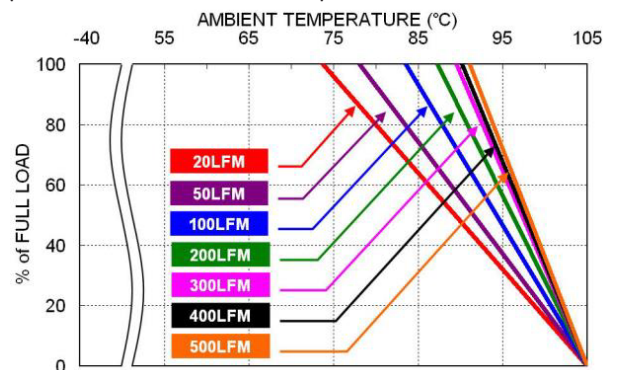
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

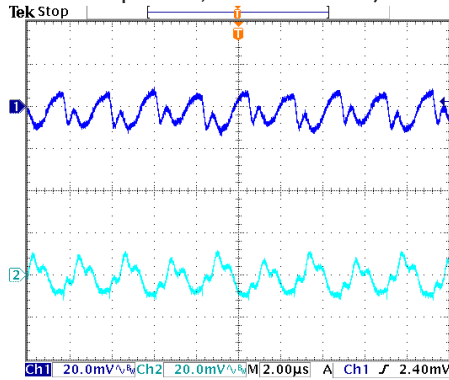


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

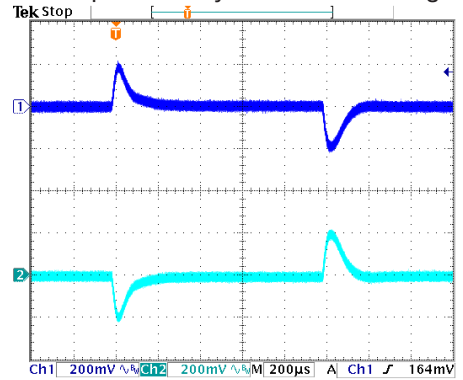


**TMR 12-2422WI**

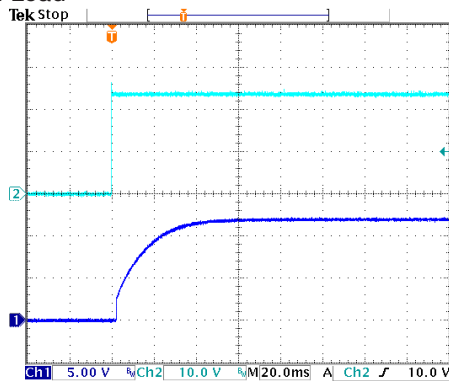
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



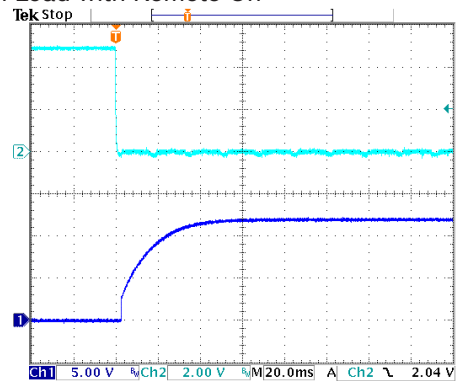
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

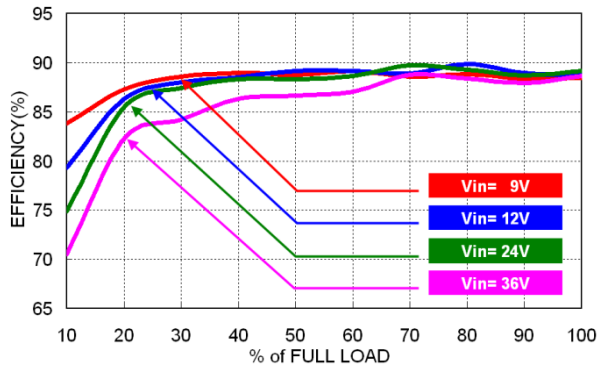


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

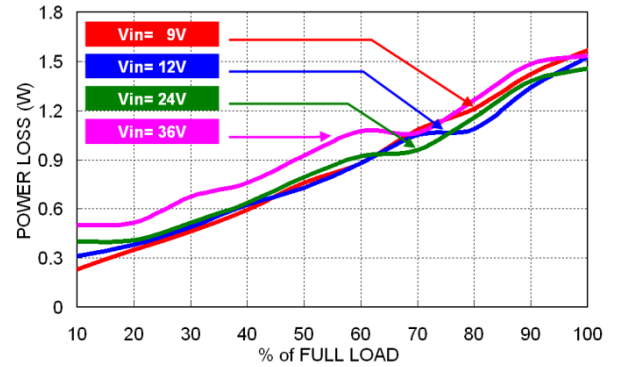


### TMR 12-2423WI

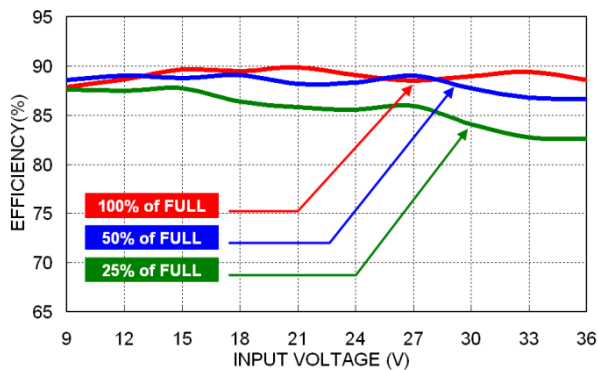
Efficiency versus Output Load



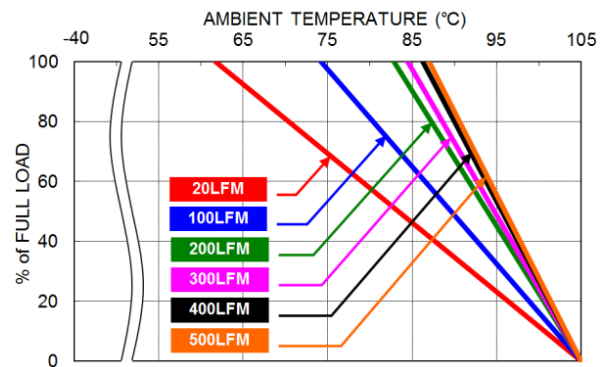
Power Dissipation versus Output Load



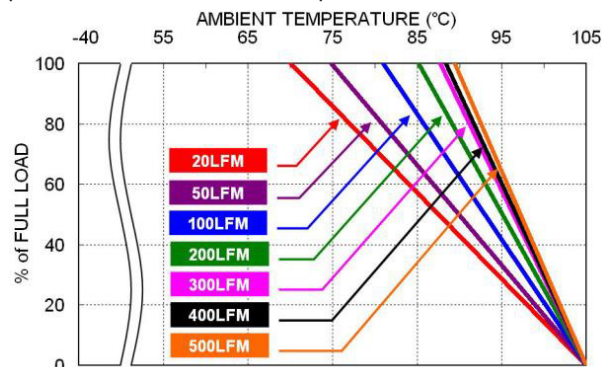
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

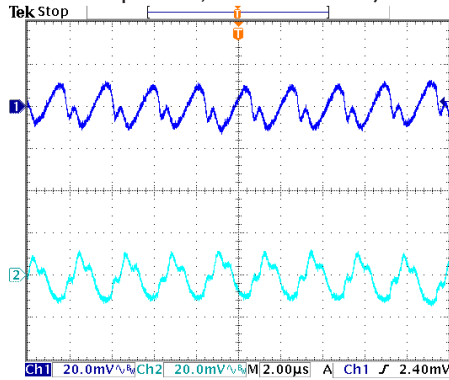


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

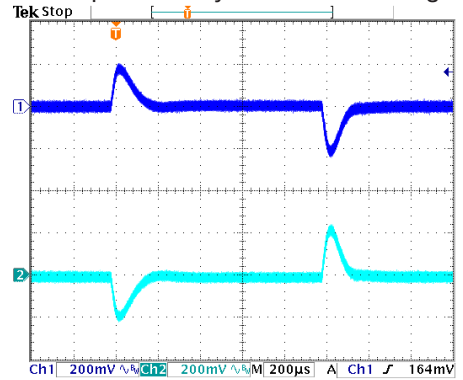


**TMR 12-2423WI**

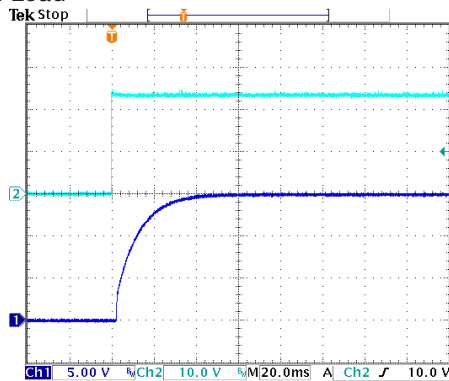
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



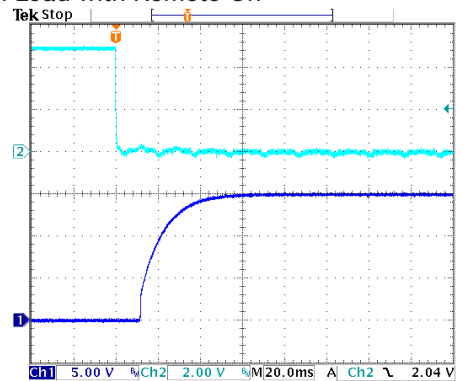
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load



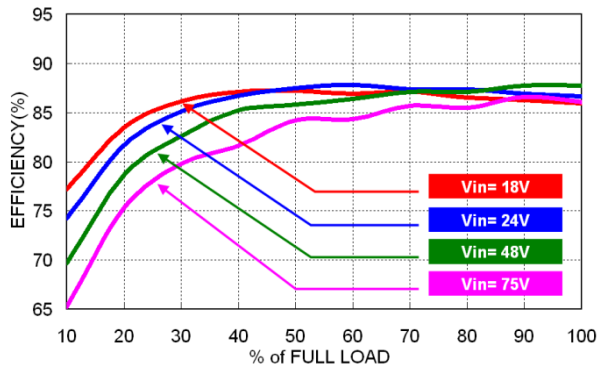
Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On



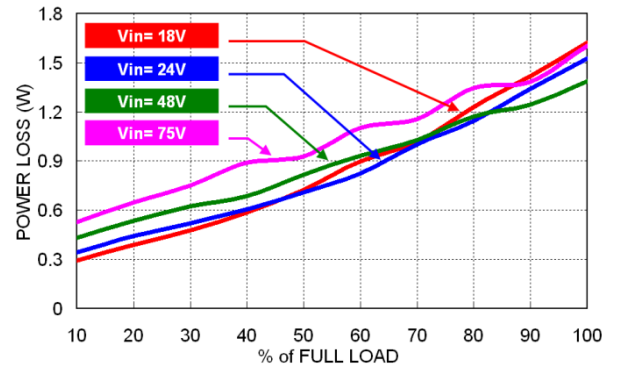


### TMR 12-4810WI

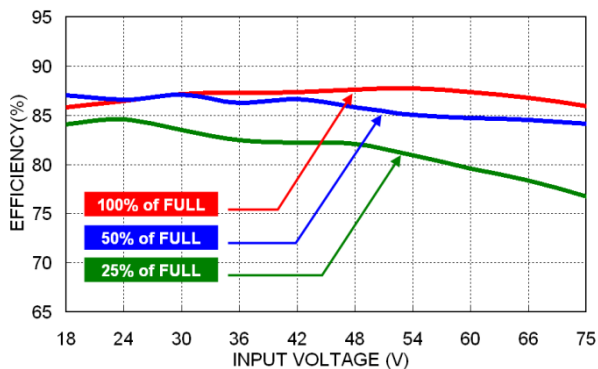
Efficiency versus Output Load



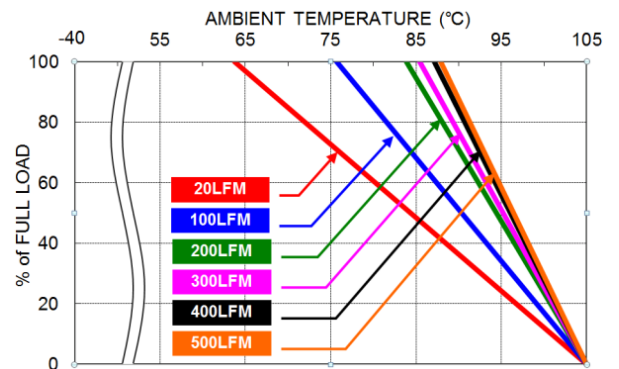
Power Dissipation versus Output Load



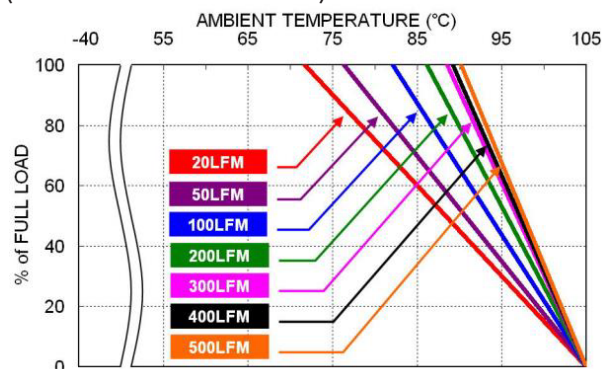
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

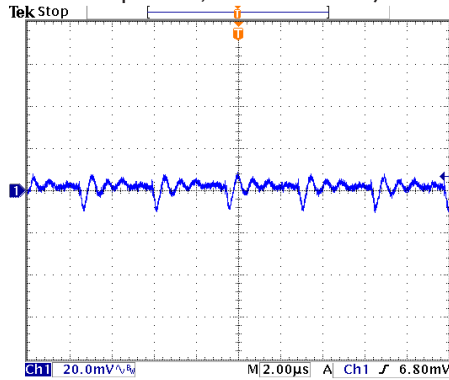


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

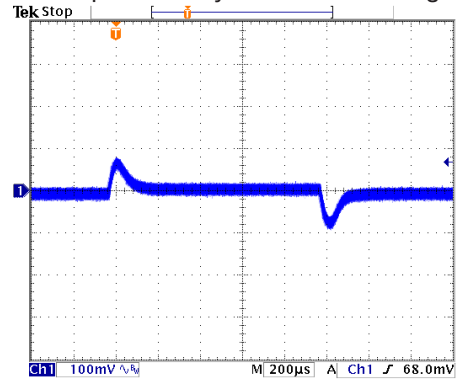


### TMR 12-4810WI

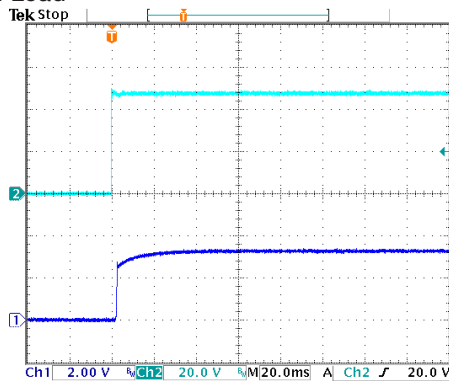
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



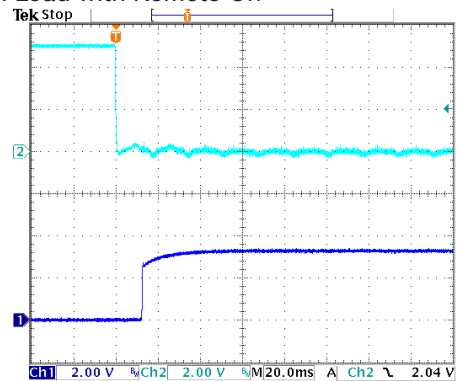
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

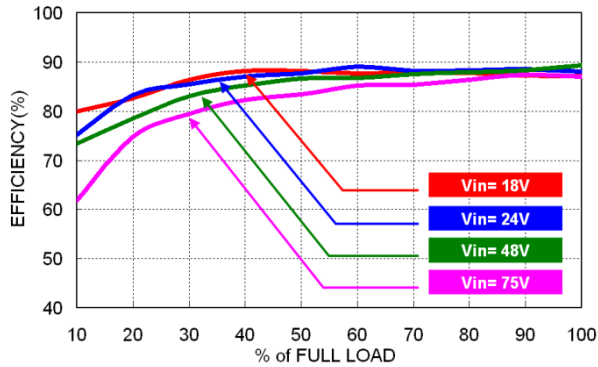


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

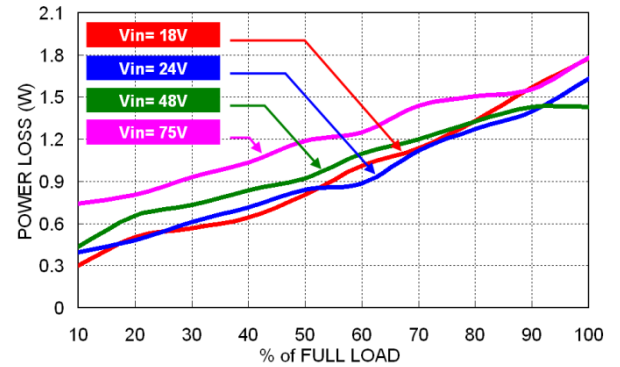


### TMR 12-4811WI

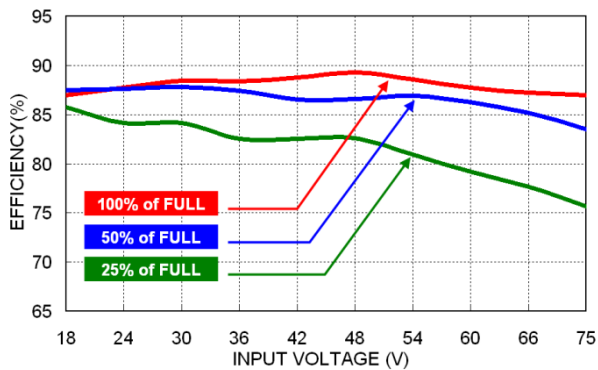
Efficiency versus Output Load



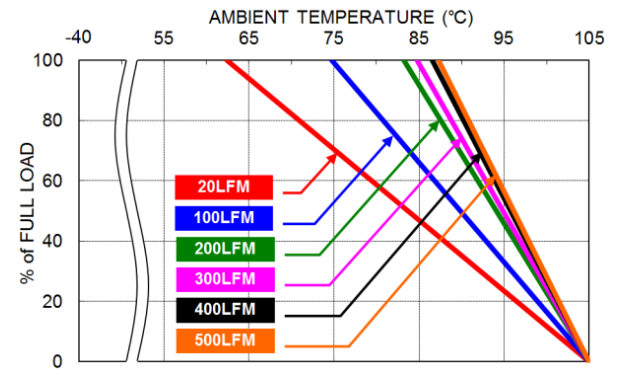
Power Dissipation versus Output Load



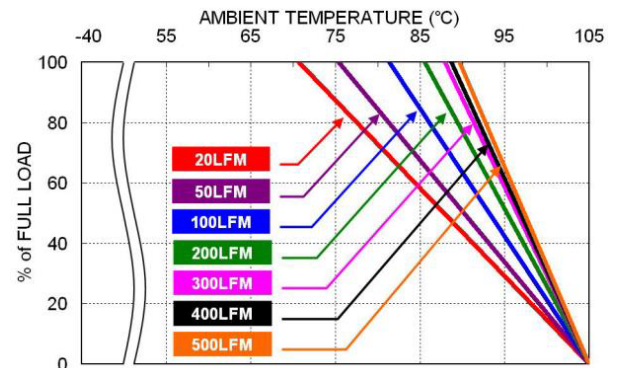
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

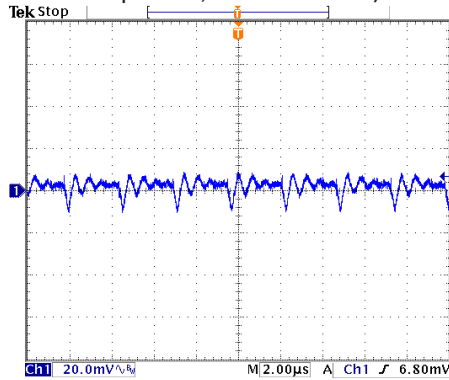


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

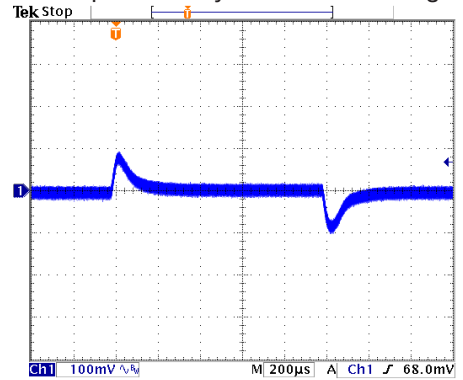


**TMR 12-4811WI**

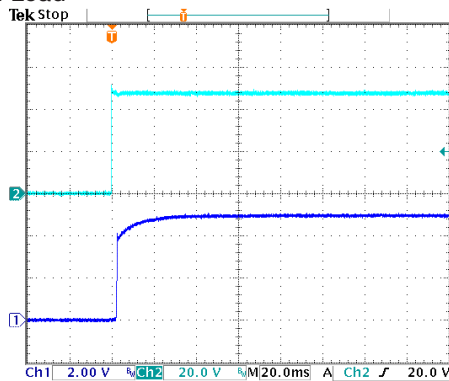
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



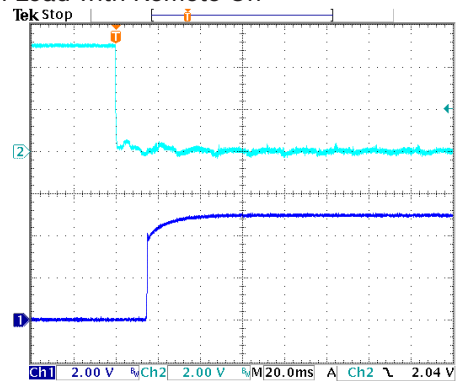
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

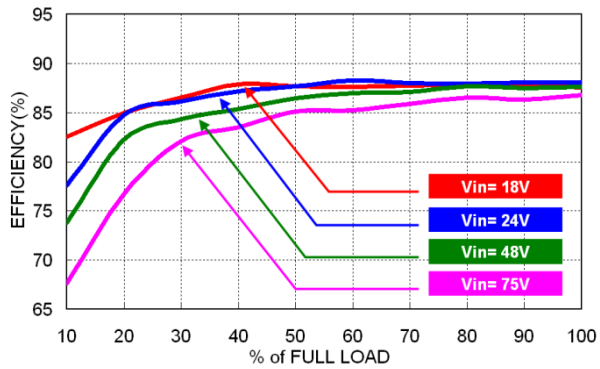


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

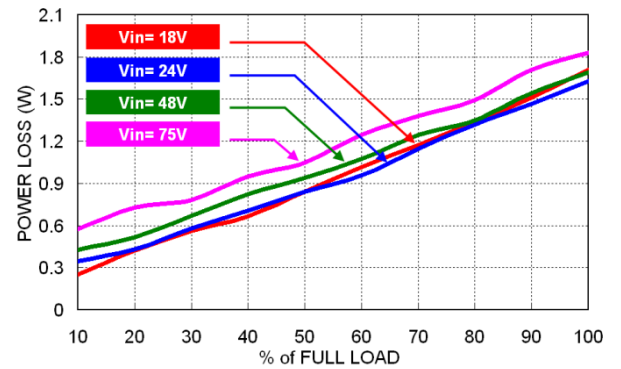


### On Demand model with 48 Vin and 9 Vout

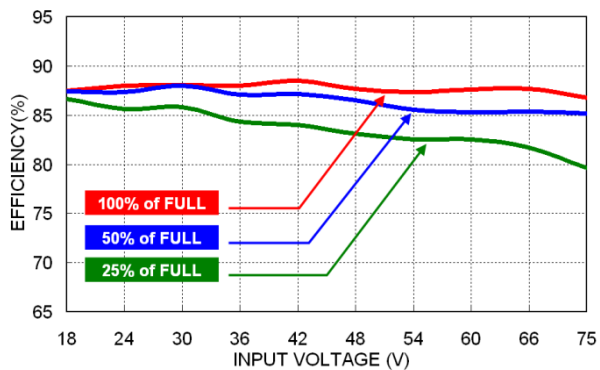
Efficiency versus Output Load



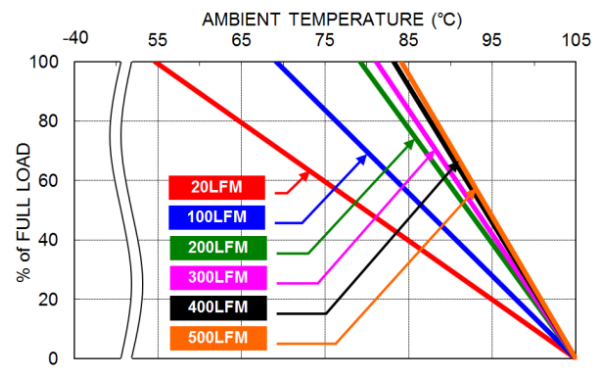
Power Dissipation versus Output Load



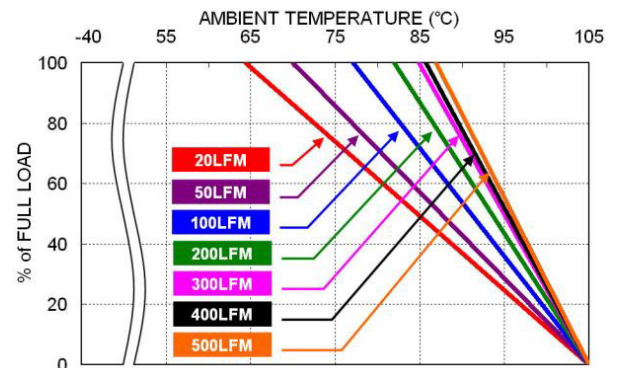
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

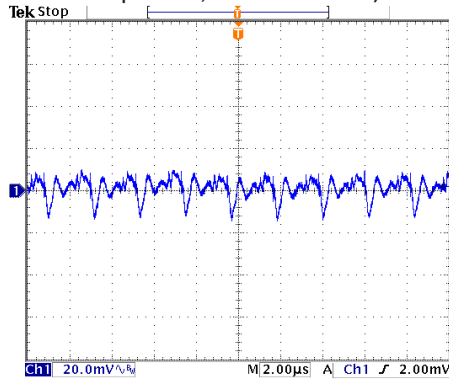


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

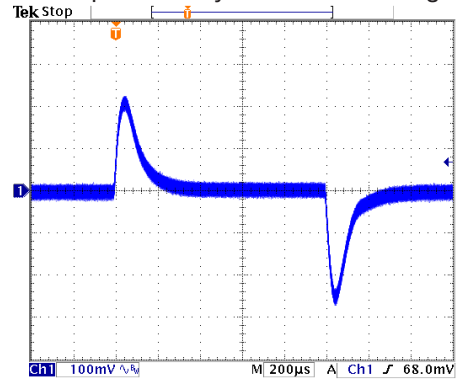


**On Demand model with 48 Vin and 9 Vout**

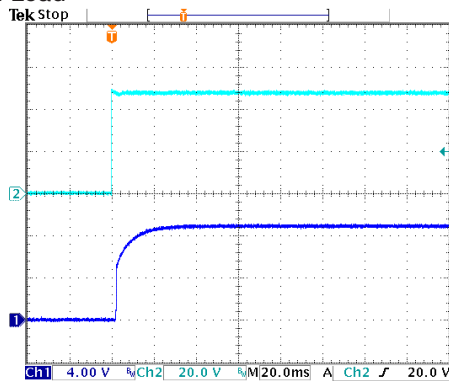
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



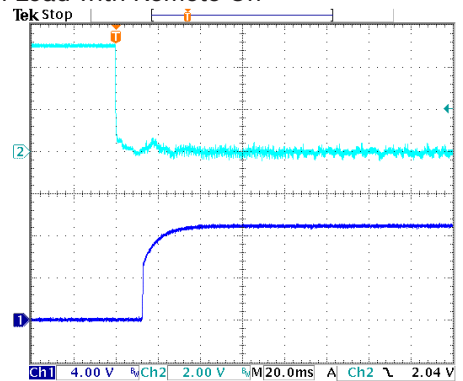
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

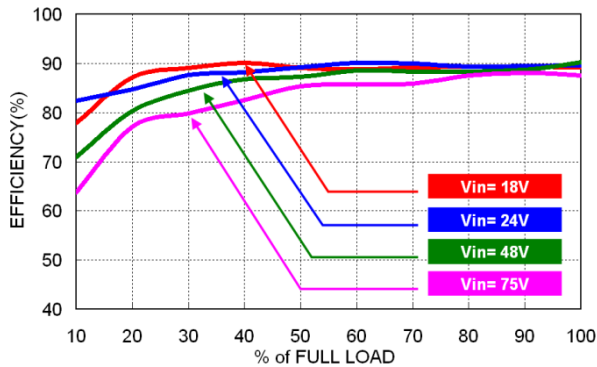


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

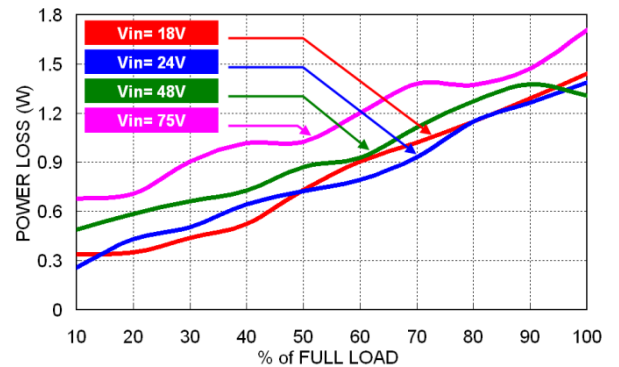


### TMR 12-4812WI

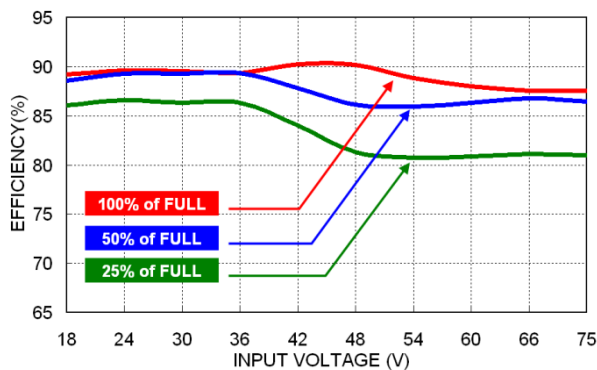
Efficiency versus Output Load



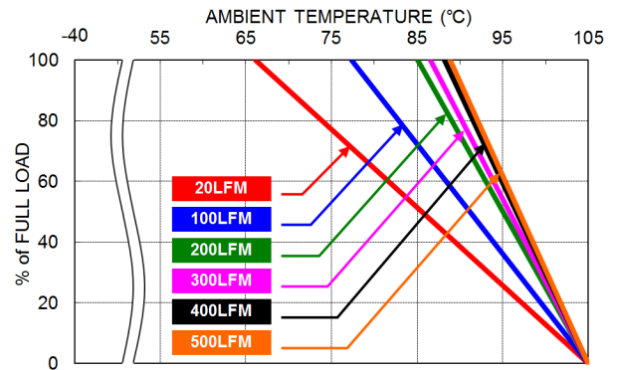
Power Dissipation versus Output Load



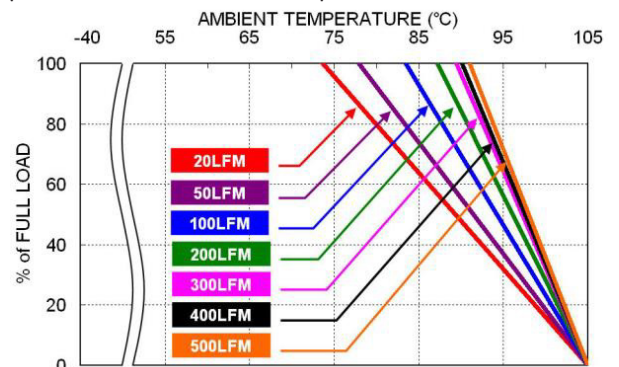
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

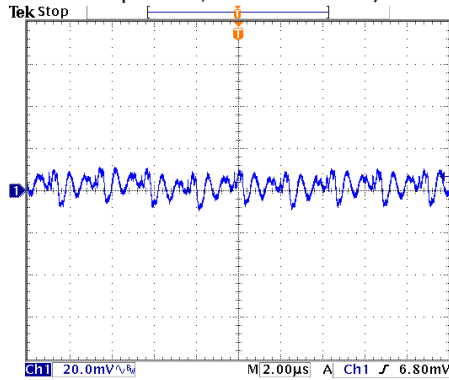


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

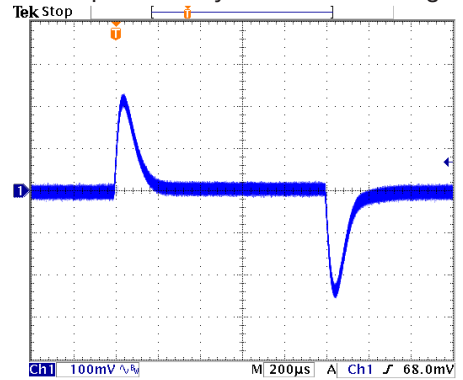


## TMR 12-4812WI

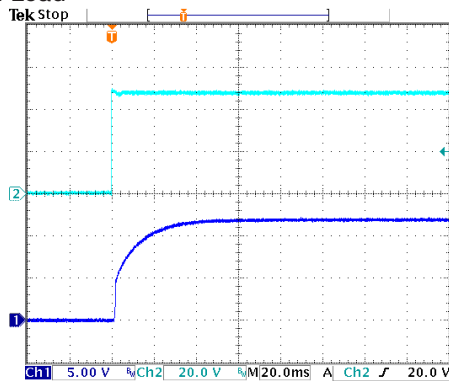
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



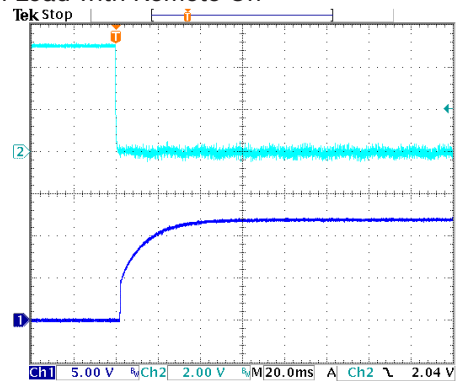
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load



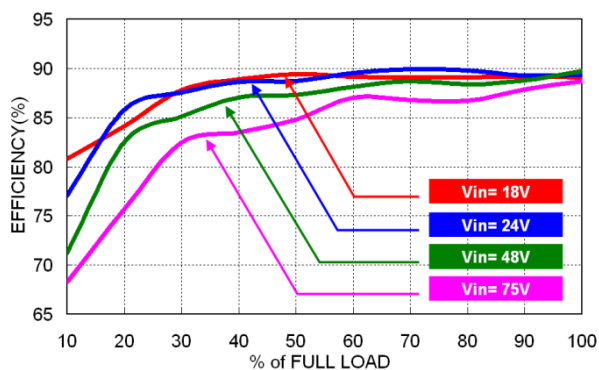
Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On



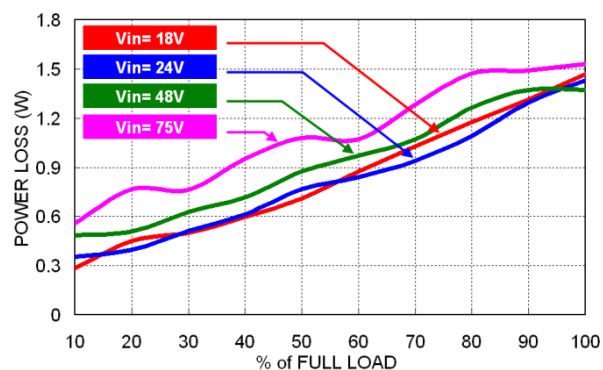


### TMR 12-4813WI

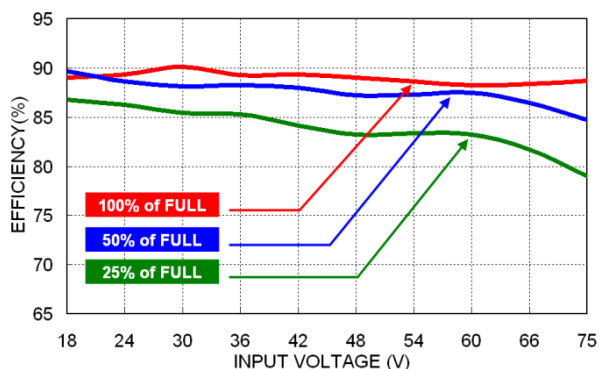
Efficiency versus Output Load



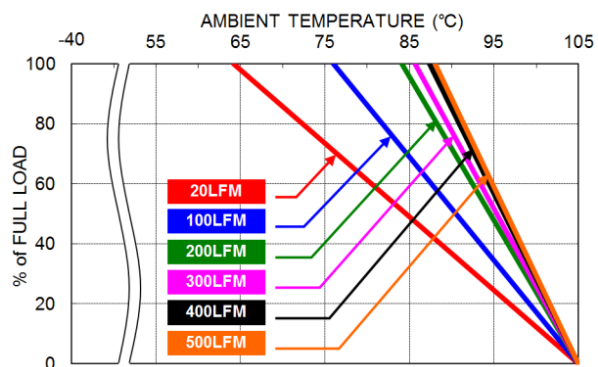
Power Dissipation versus Output Load



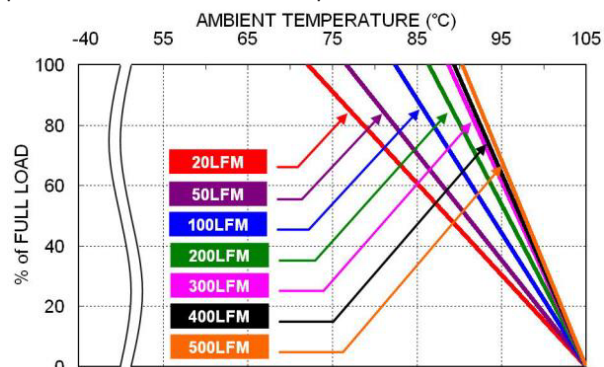
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

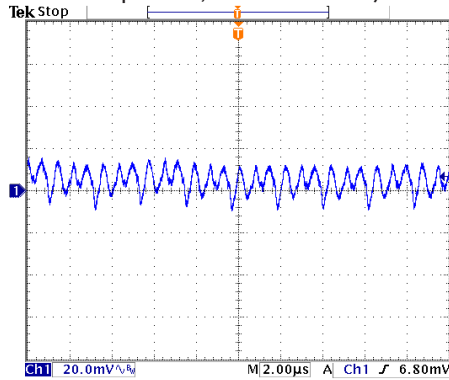


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

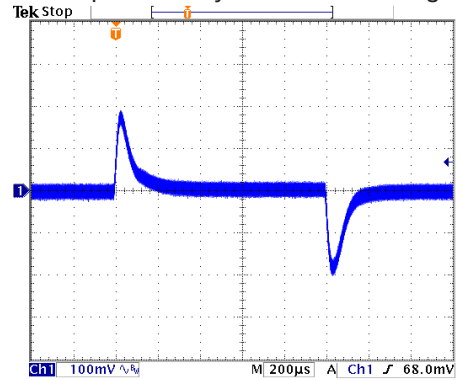


### TMR 12-4813WI

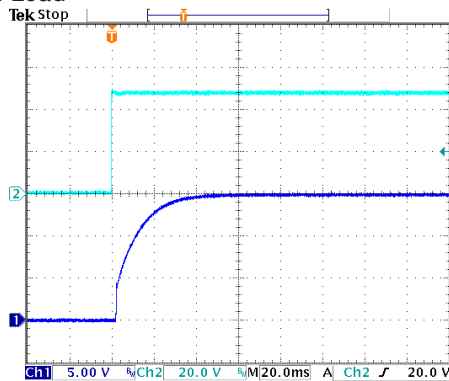
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



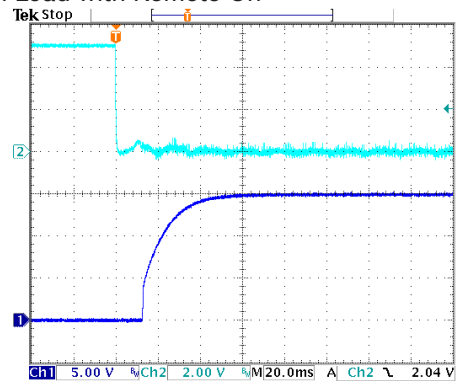
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

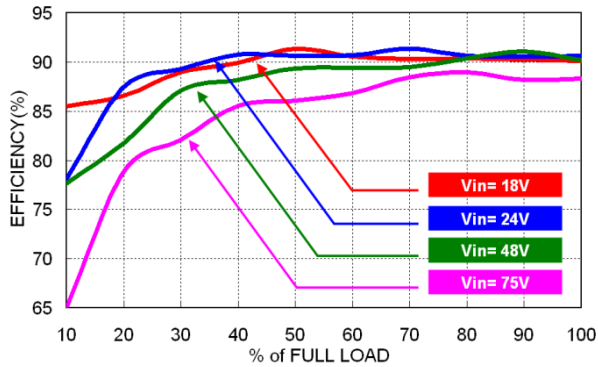


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

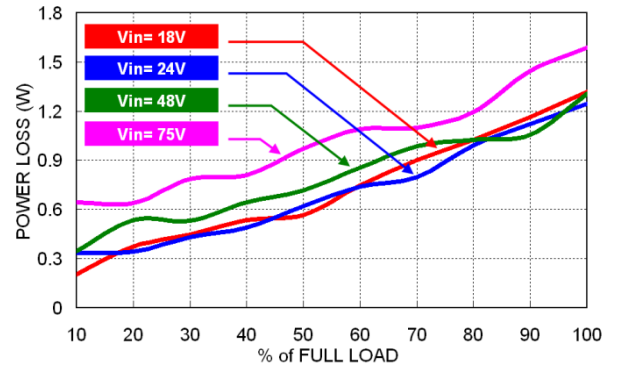


### TMR 12-4815WI

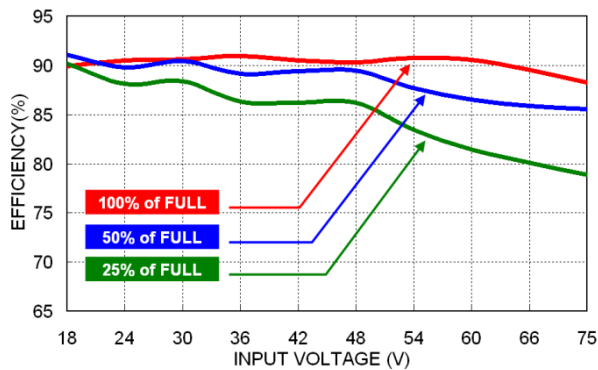
Efficiency versus Output Load



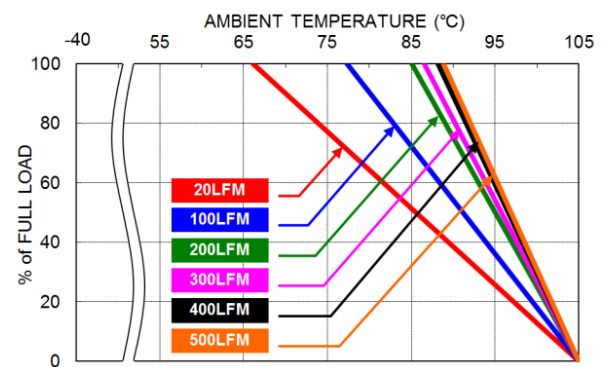
Power Dissipation versus Output Load



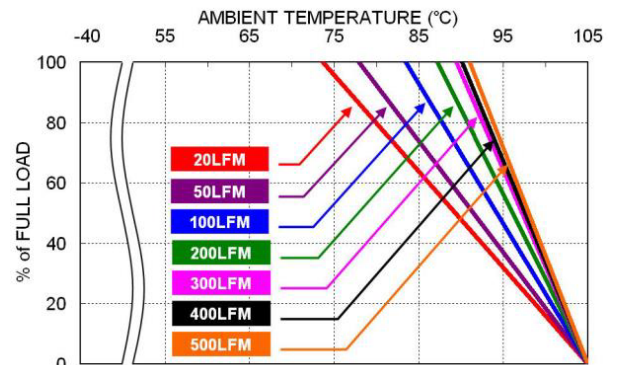
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

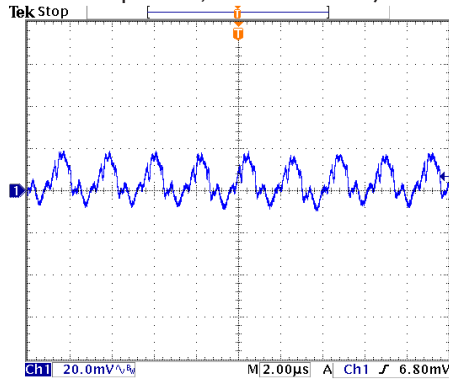


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

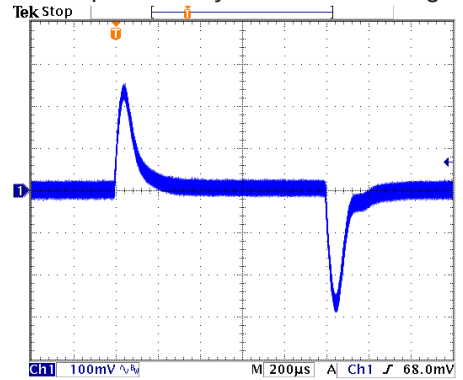


**TMR 12-4815WI**

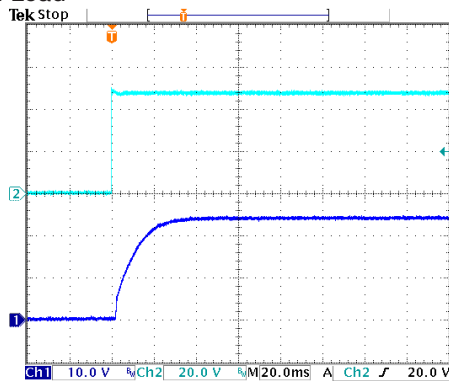
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



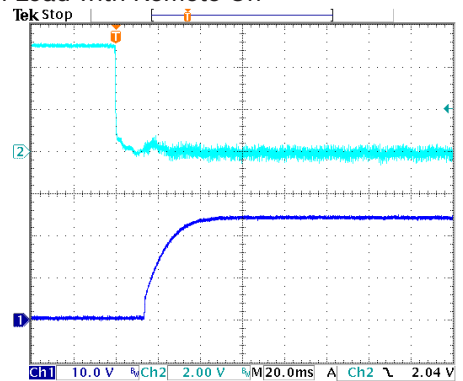
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

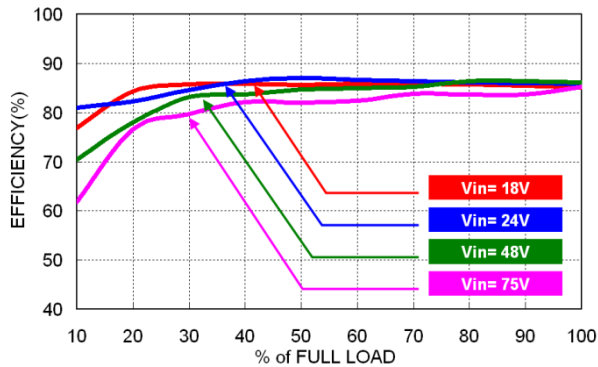


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

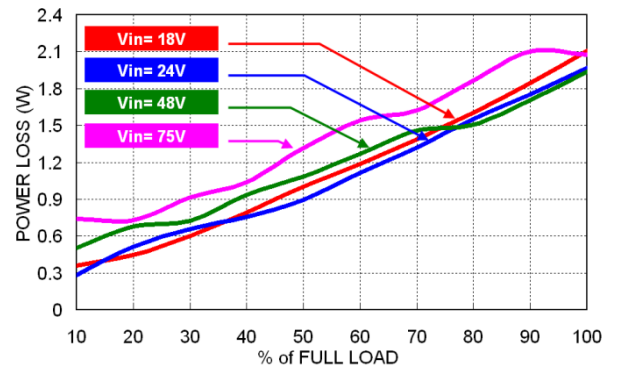


### TMR 12-4821WI

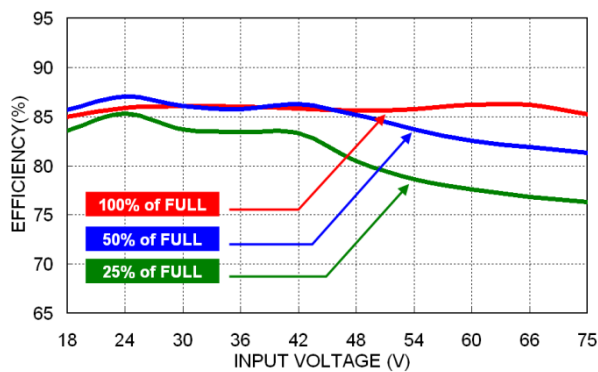
Efficiency versus Output Load



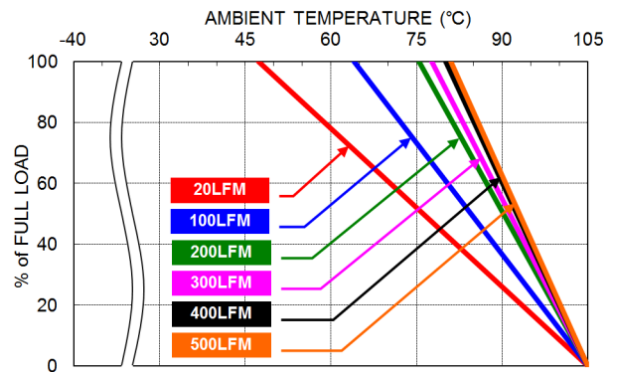
Power Dissipation versus Output Load



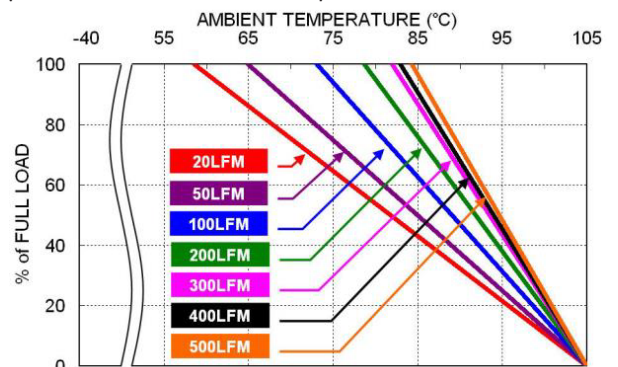
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

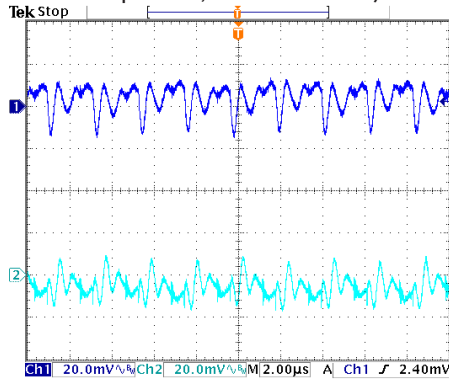


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

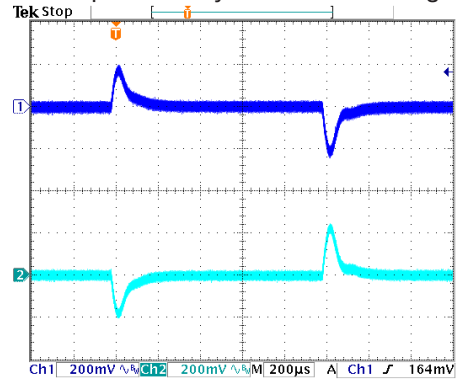


### TMR 12-4821WI

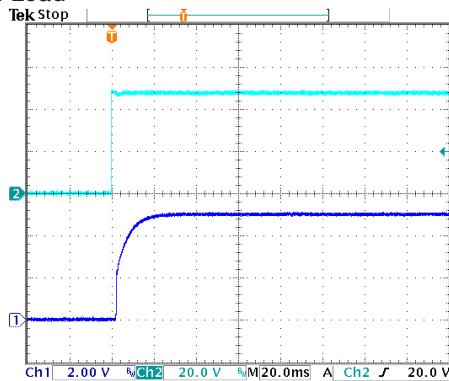
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



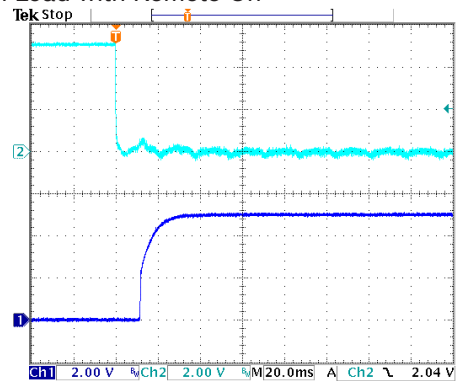
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load

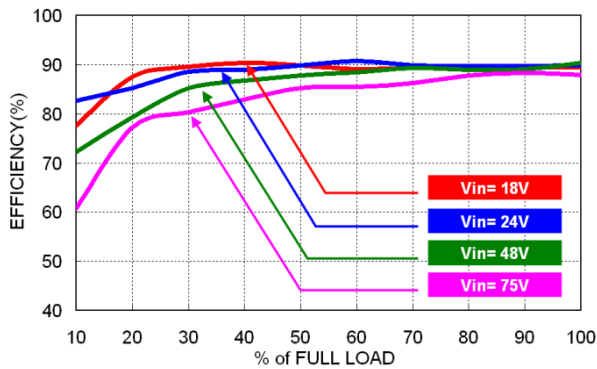


Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

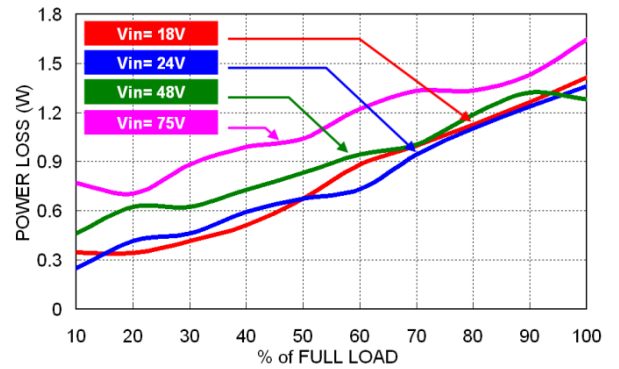


### TMR 12-4822WI

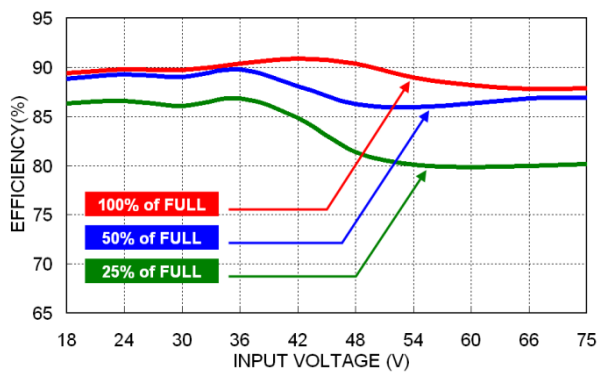
Efficiency versus Output Load



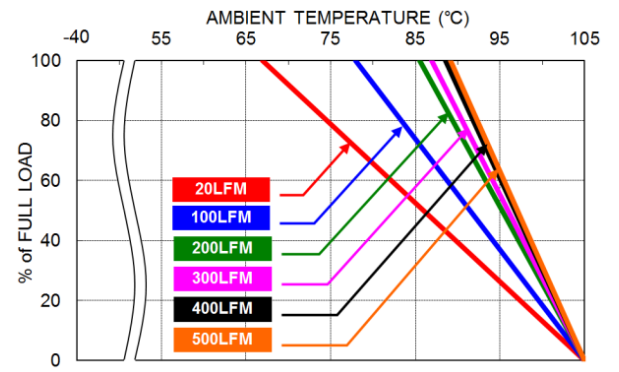
Power Dissipation versus Output Load



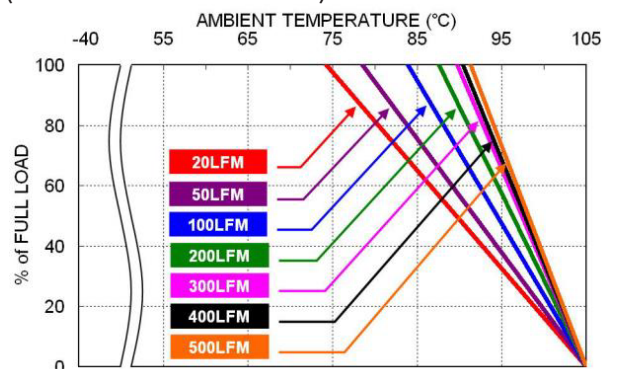
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

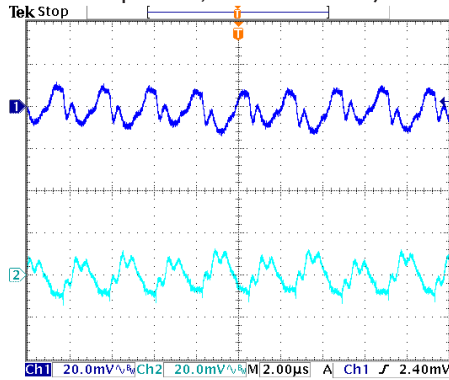


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

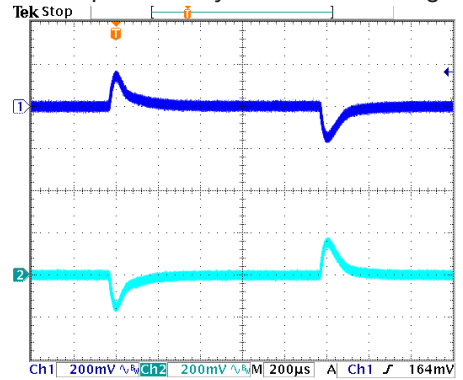


**TMR 12-4822WI**

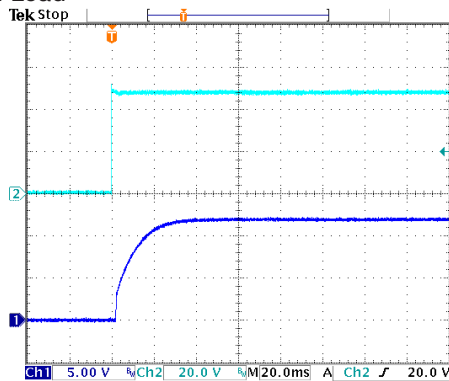
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



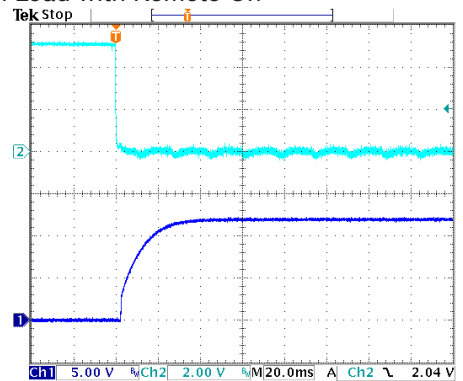
Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load



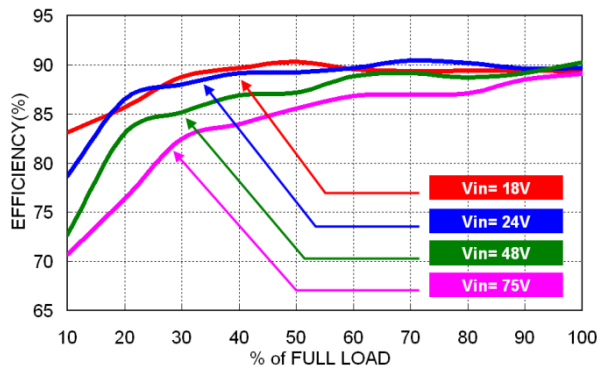
Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On



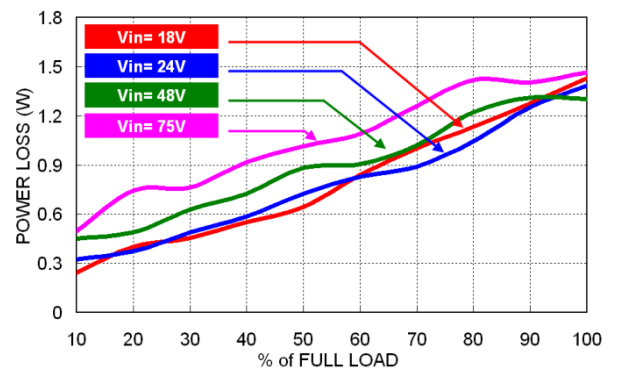


### TMR 12-4823WI

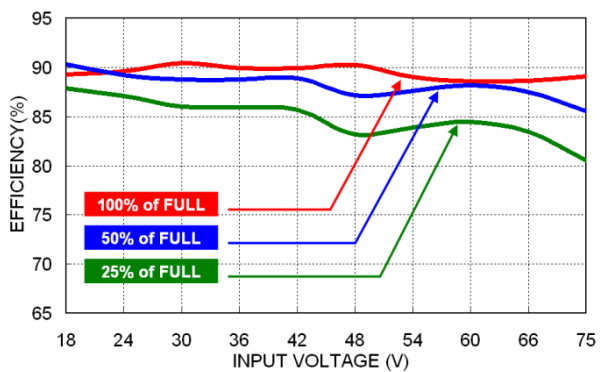
Efficiency versus Output Load



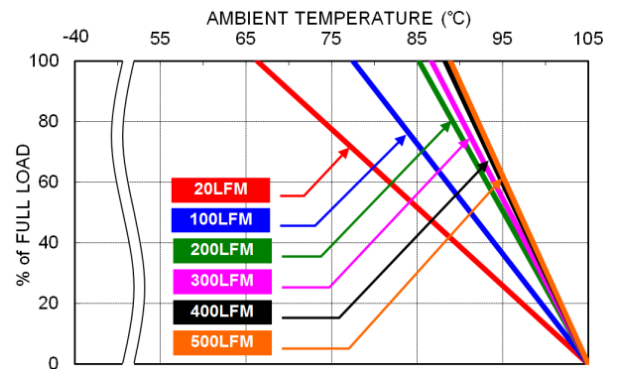
Power Dissipation versus Output Load



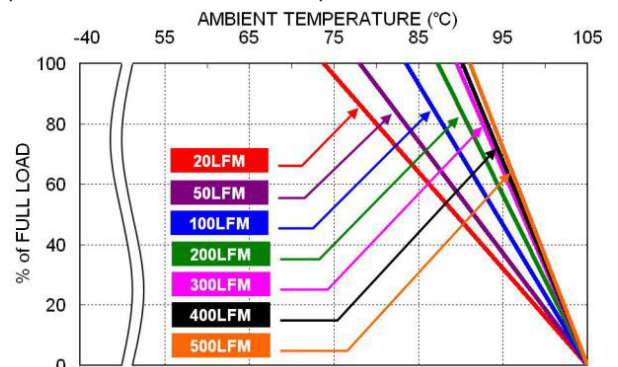
Efficiency versus Input Voltage



Derating Output Load versus Ambient Temperature

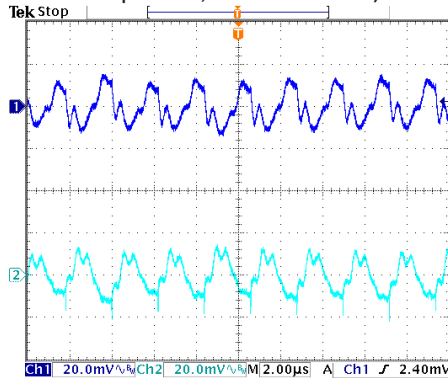


Derating Output Load versus Ambient Temperature with recommended PCB layout (see thermal considerations)

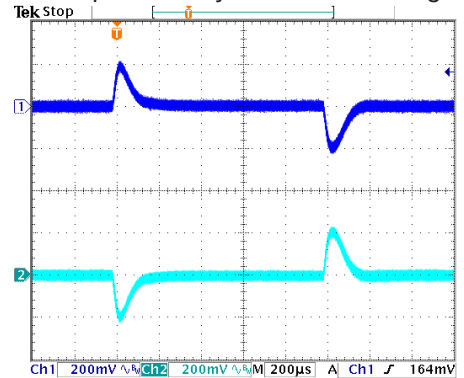


### TMR 12-4823WI

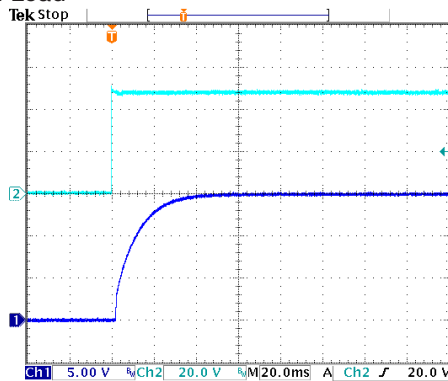
Typical Output Ripple and Noise  
(with external capacitor; see datasheet)



Transient Response to Dynamic Load Change (25%)



Typical Start-Up and Output Rise Characteristic  
at Full Load



Typical Input/Output Start-Up Characteristic  
at Full Load with Remote On

